# NASA WORKMANSHIP STANDARDS

# PICTORIAL REFERENCE



National Aeronautics and Space Administration

Johnson Space Center Houston, Texas USA 77058

## **INTRODUCTION**

The Inspectors Pictorial Reference provides full color visual examples of acceptance / rejection criteria which may be used for the design, manufacture and inspection of electrical / electronic equipment for high-reliability and space flight applications, and is a reference-only companion to the NASA Technical Standard, NASA-STD-8739 series of workmanship requirements documents.

# **ACCEPTANCE / REJECTION CRITERIA**

The following classification terms are used to identify acceptable and unacceptable workmanship conditions:

- PREFERRED A condition that is close to "perfect".
- MANDATORY A hard requirement that must be met.
- ACCEPTABLE A condition that may not be perfect, but meets the requirement.
- UNACCEPTABLE Does not meet the minimum requirement and that may be insufficient to ensure the form, fit, or function of the hardware in its end use.

# **REQUIREMENT REFERENCES**

Each Acceptance / Rejection Criteria example contains a reference to the applicable requirement(s) from the NASA Technical Standard Series, NASA-STD-8739.x. In instances where there is no specific requirement, the reference defaults to **Best Workmanship Practice**, which identifies a procedure, practice, or process attribute that has been demonstrated through use and experience, to result in a robust design and high reliability.

## SPECIAL REQUIREMENTS

Special requirements may exist which are not covered by, or do not comply with, the visual examples depicted in this reference, and which are in conflict with the requirements specified in the NASA-STD-8739 series documents. Engineering documentation shall contain the details for such instances, and shall take precedence over appropriate sections of this reference and the requirements document.

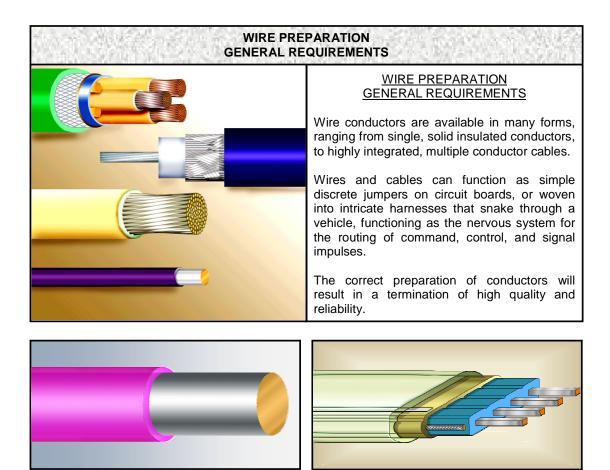
# **CONTROL COPY NUMBER / DISTRIBUTION**

Each Pictorial Reference is issued with a Control Copy Number, and all subsequent releases will be distributed in accordance with this numbering system. Each assignee will be required to remove and insert pages of each release in their assigned manual in accordance with instructions given on each release, to maintain an up-to-date and useful reference. Should an assignee no longer require a manual, it shall be returned to the Technology Division (NX) of Safety, Reliability, and Quality Assurance (SR&QA).

This document shall not be rewritten or reissued in any other form not approved by NASA.

## ACKNOWLEDGEMENTS

The illustrations and photographs contained in this reference represent a compilation of workmanship and "best design practices" from currently used industrial, military, and NASA-approved workmanship standards, compiled from technical expert sources within NASA, and from the Association Connecting Electronics Industries (IPC).



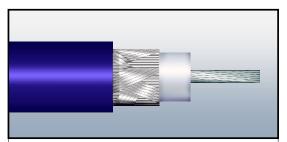
PREFERRED CHEMICAL STRIPPING PROCESS

The insulation jacket has been neatly removed, with no damage to the conductor or insulation. No wicking of stripper or cleaner agents evident.

**Note:** Chemical stripping is suitable for solid conductors only.

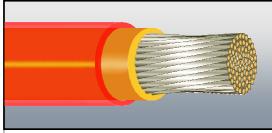
PREFERRED FLAT CABLE

The outer jacket, conductive shield (if supplied), and conductor insulation jackets have been neatly trimmed and removed, with minimal edge flash and no mechanical damage. The conductors are in planar orientation, and the drain conductor and/or shield are undamaged.



#### PREFERRED FLEXIBLE COAXIAL CABLE

The insulation jacket and shield(s) have been neatly trimmed, with minimal edge flash and no mechanical damage to the conductors, shielding, dielectric, or insulation jacket. The center conductor stranding exhibits a normal twist pattern (lay).



#### PREFERRED KAPTON<sup>®</sup> INSULATED CONDUCTORS

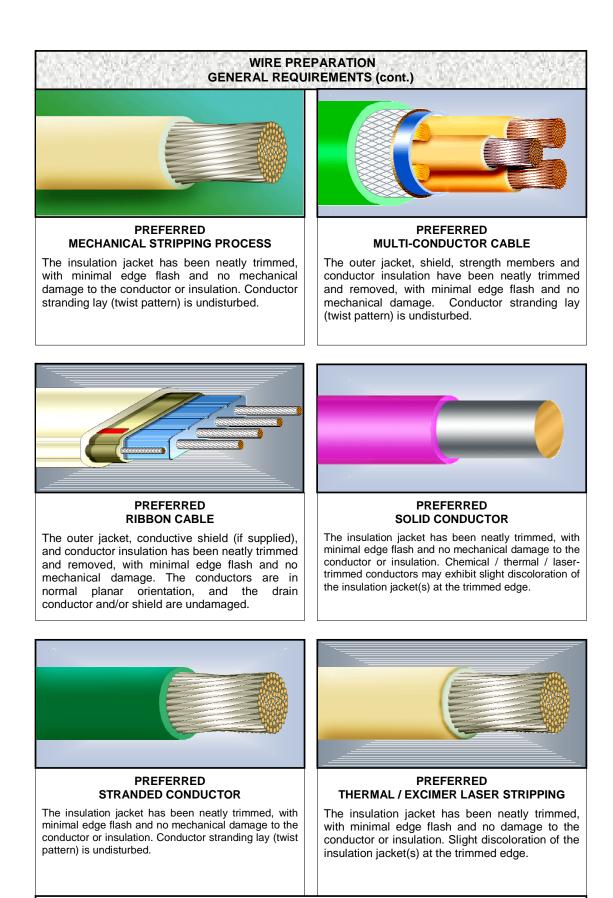
Kapton<sup>®</sup>-insulated conductors must be trimmed neatly and squarely, with minimal edge flash and no mechanical damage to the conductor or insulation.

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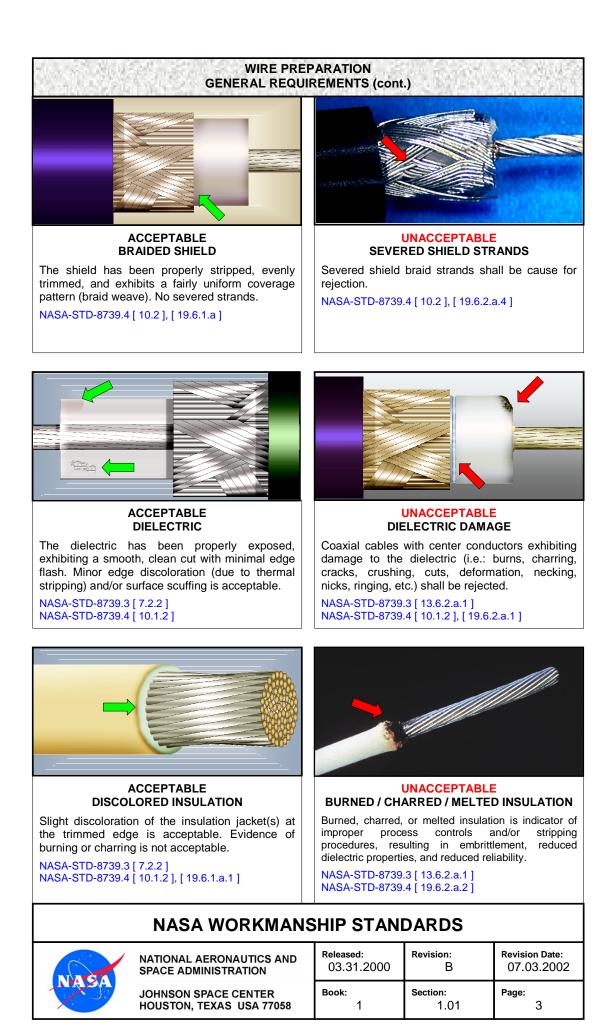
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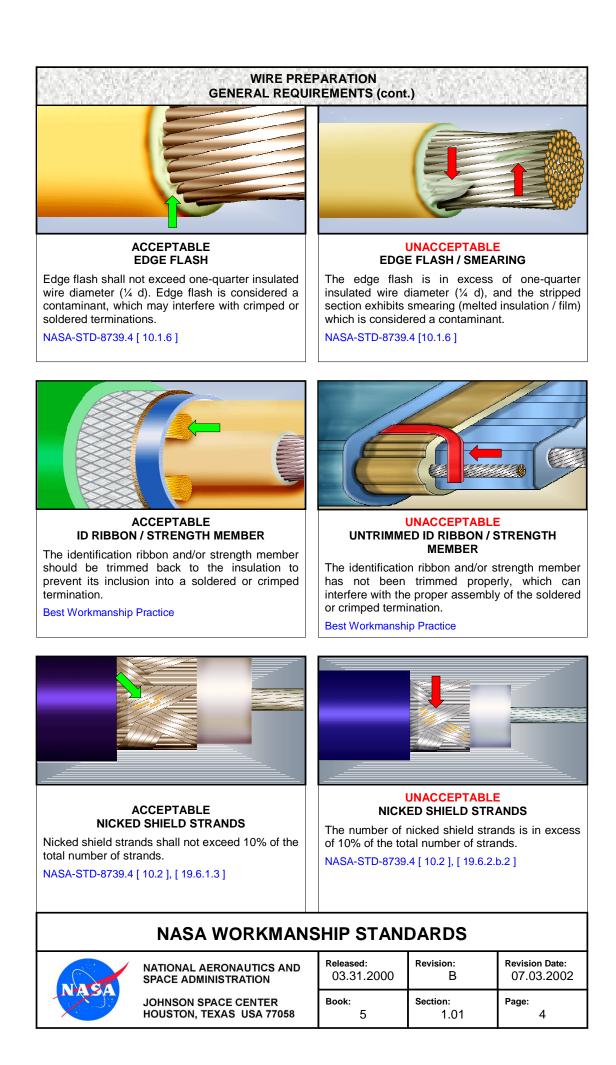


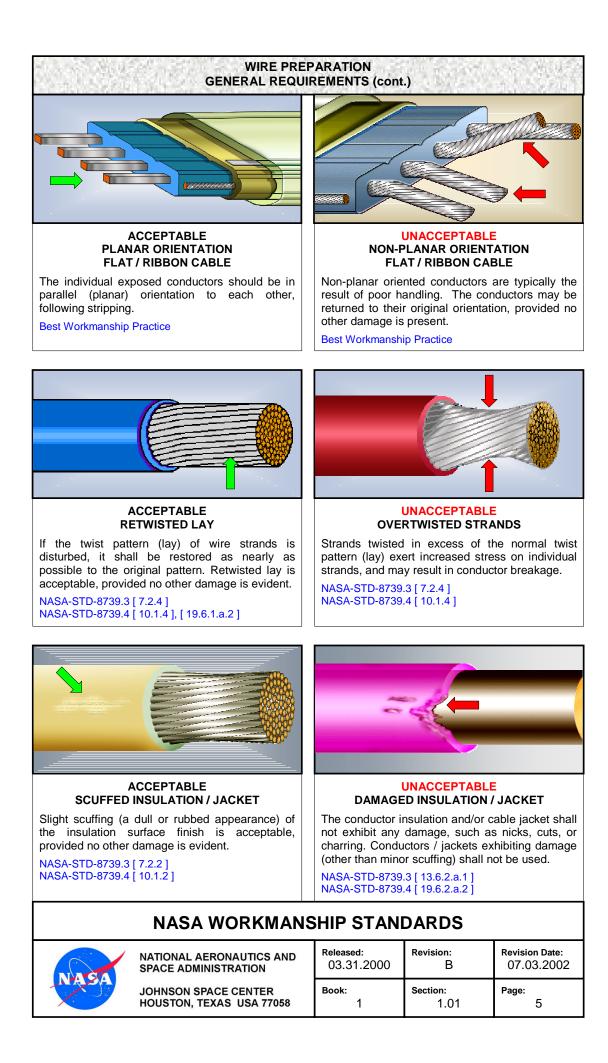
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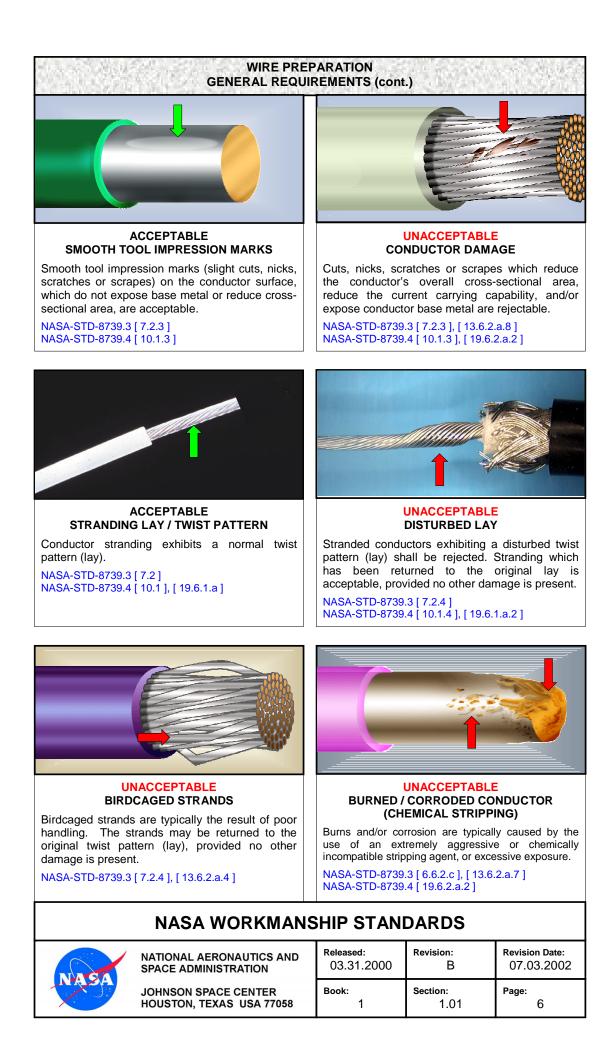
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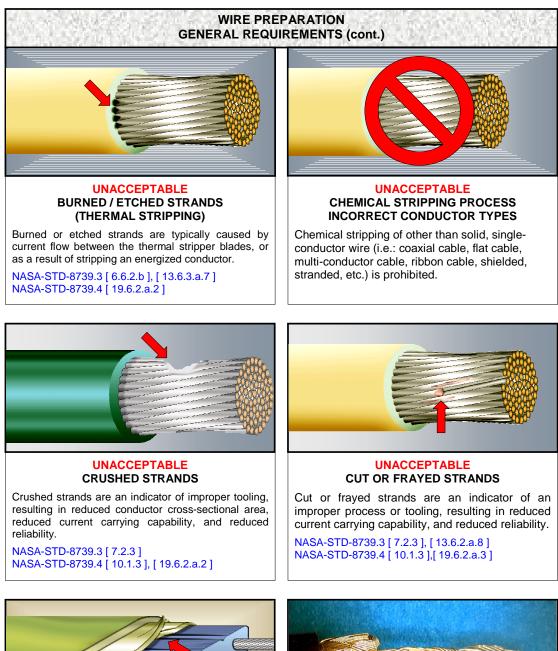
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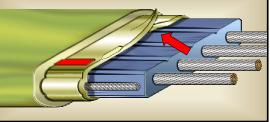








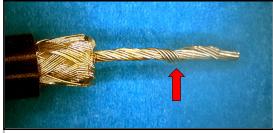




#### UNACCEPTABLE DAMAGED SHIELD

Cut, crushed, gouged, damaged, or nicked shielding may result in reduced electrical isolation and/or short circuits.

**Best Workmanship Practice** 



#### UNACCEPTABLE OVERLAPPING STRANDS

Strands, retwisted and overlapping each other, will result in increased stress and difficulty in insertion, or the forming of a mechanical wrap.

NASA-STD-8739.3 [ 7.2.4 ] NASA-STD-8739.4 [ 10.1.4 ]

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# WIRE PREPARATION **GENERAL REQUIREMENTS (cont.)** UNACCEPTABLE UNACCEPTABLE **RINGED CONDUCTORS / STRANDS** UNEVENLY TRIMMED INSULATION Ringing is a symptom of an improper process or Unevenly trimmed insulation may result in tooling. Ringing which reduces the overall cross-sectional area and/or results in exposed base metal reduced electrical isolation and/or short circuits, and may interfere with termination. shall be cause for rejection. **Best Workmanship Practice** NASA-STD-8739.3 [ 6.6.1 ], [ 7.2.3 ], [ 13.6.2.a.8 ] NASA-STD-8739.4 [ 6.6.1 ], [ 10.1.3 ], [ 19.6.2.a.2 ] **UNACCEPTABLE UNACCEPTABLE** UNEVENLY TRIMMED SHIELD WICKING

Unevenly trimmed shielding indicates poor technique and may result in improper electrical termination during connector assembly.

**Best Workmanship Practice** 

Wicking of chemical stripping and/or cleaning agents under the insulation jacket is a long-term reliability concern.

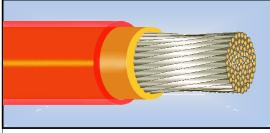
NASA-STD-8739.3 [ 13.6.2.a.9 ]

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# WIRE PREPARATION **MECHANICAL STRIPPING** MECHANICAL STRIPPING Mechanical stripping is an inexpensive, easy method of stripping most commonly used insulation materials, and is the preferred method for manually stripping film insulations such as Kapton<sup>®</sup>. In the process, a grooved knife-edge is used to cut the insulation jacket down to the conductor. The severed insulation section is then manually removed without damaging the conductor. See Section 1.01 "Wire Preparation, General Requirements", for common accept / reject criteria.

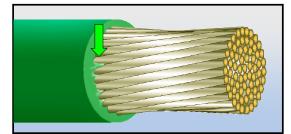
PREFERRED **GENERAL REQUIREMENTS** (ALL CONDUCTOR / INSULATION TYPES)

The insulation jacket has been neatly trimmed, with no edge flash and no mechanical damage to the conductor or insulation. Conductor stranding exhibits a normal twist pattern (lay).



PREFERRED **KAPTON® INSULATED CONDUCTORS** 

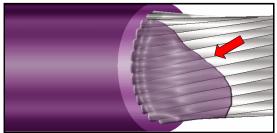
The insulation jacket has been trimmed neatly and squarely, with minimal edge flash and no mechanical damage to the conductor or insulation. Conductor stranding lay (twist pattern) is undisturbed.



ACCEPTABLE EDGE FLASH

Edge flash shall not exceed one-quarter insulated wire diameter (1/4 d). Edge flash is a thin layer of insulation that is produced during the stripping process, and is considered a contaminant.

NASA-STD-8739.4 [ 10.1.6 ]



#### **UNACCEPTABLE EXCESSIVE EDGE FLASH**

The edge flash is in excess of one-quarter insulated wire diameter (1/4 d), and may interfere with the proper completion of a crimped or soldered termination.

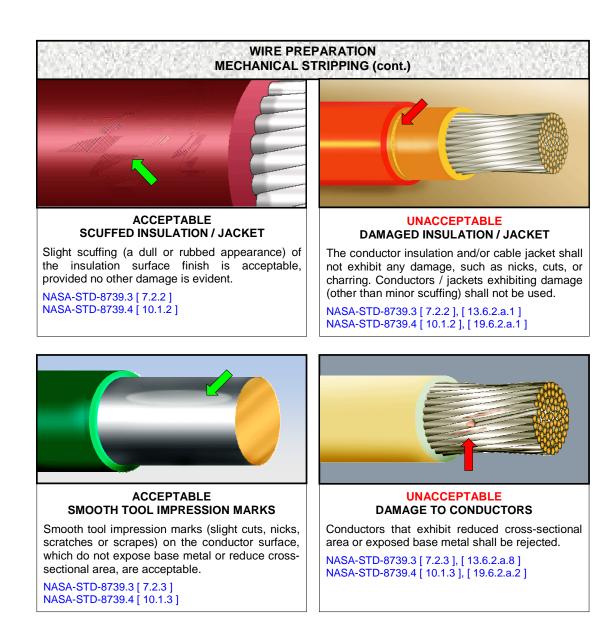
NASA-STD-8739.4 [ 10.1.6 ]

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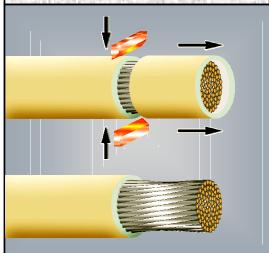
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#### WIRE PREPARATION EXCIMER LASER ABLATIVE AND THERMAL STRIPPING

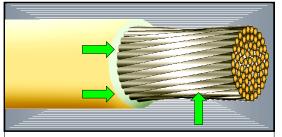


#### EXCIMER LASER ABLATIVE (ELA) AND THERMAL STRIPPING

Excimer laser ablative (ELA) stripping employs a laser to achieve extremely precise stripping of "difficult to strip" insulation materials and intricate conductor stripping geometries.

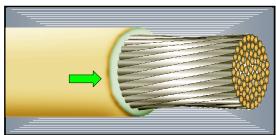
Thermal stripping is performed using a heated tool to melt a ring or groove into the insulation down to the conductor. The severed insulation section is then manually removed without damaging the conductor.

See Section 1.01 "Wire Preparation, General Requirements", for common accept / reject criteria.



PREFERRED GENERAL REQUIREMENTS

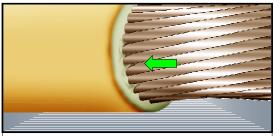
The insulation jacket has been neatly trimmed, with no edge flash, smearing, or damage. The conductor stranding lay (twist pattern) is undisturbed, and the conductor is undamaged.



ACCEPTABLE THERMAL DISCOLORATION

Slight discoloration of the insulation jacket(s) at the trimmed edge is acceptable. Evidence of burning or charring is not acceptable.

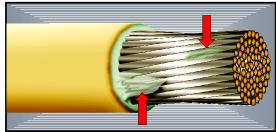
NASA-STD-8739.3 [ 7.2.2 ] NASA-STD-8739.4 [ 10.1.2 ], [ 19.6.1.a.1 ]



#### ACCEPTABLE EDGE FLASH

Edge flash shall not exceed one-quarter insulated wire diameter ( $\frac{1}{14}$  d). Edge flash (as depicted) is considered normal, and should not contaminate or interfere with a crimped or soldered termination.

NASA-STD-8739.4 [ 10.1.6 ]



#### UNACCEPTABLE EDGE FLASH / SMEARING

The edge flash is in excess of one-quarter insulated wire diameter (½ d), and the stripped section exhibits smearing (melted insulation / film), which is considered a contaminant.

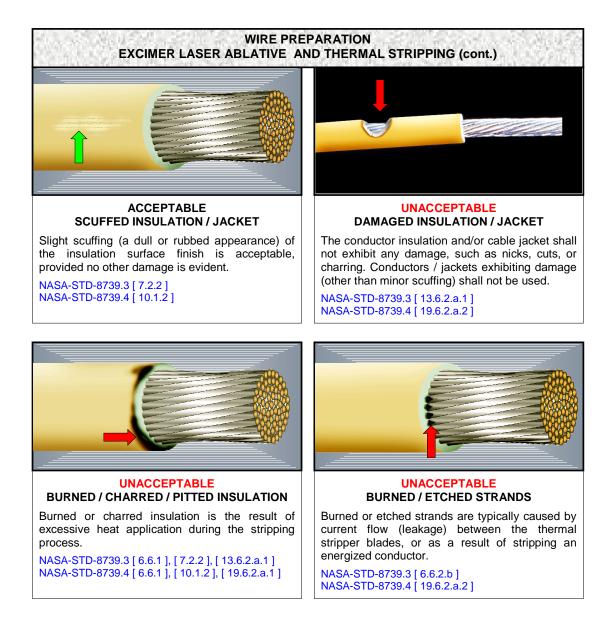
NASA-STD-8739.4 [10.1.6]

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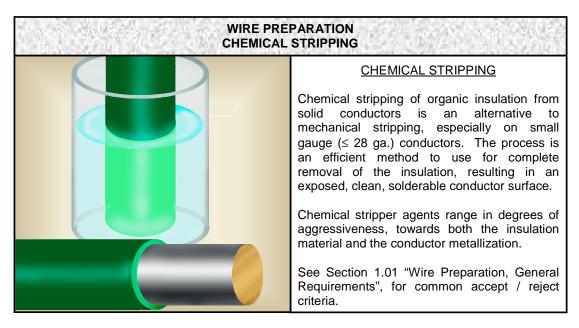


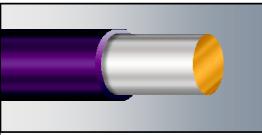
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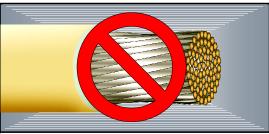




PREFERRED

The insulation jacket has been neatly removed, with no damage to the conductor or insulation. No wicking of stripper or cleaner agents under the insulation jacket is evident.

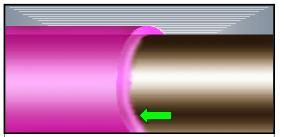
**Note:** Chemical stripping is suitable for solid conductors only.



UNACCEPTABLE INAPPROPRIATE CONDUCTOR TYPES Chemical stripping of other than solid, single-

conductor wire (i.e.: coaxial cable, flat cable, multi-conductor cable, ribbon cable, shielded, stranded, etc.) is prohibited.

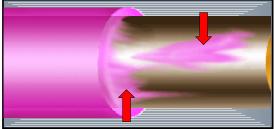
Best Workmanship Practice



#### ACCEPTABLE EDGE FLASH / SMEARING

Edge flash shall not exceed one-quarter insulated wire diameter (¼ d). Edge flash and smearing (as depicted) is considered normal, and should not contaminate or interfere with a crimped or soldered termination.

NASA-STD-8739.4 [ 10.1.6 ]



#### UNACCEPTABLE EDGE FLASH / SMEARING

The edge flash is in excess of one-quarter insulated wire diameter ( $\frac{1}{4}$  d), and the stripped section exhibits smearing (melted insulation / film) which is considered a contaminant.

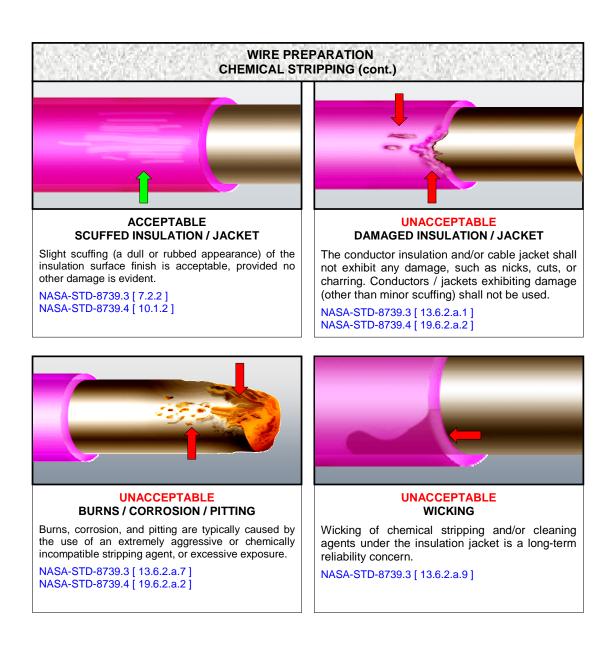
NASA-STD-8739.4 [ 10.1.6 ]

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#### **CRIMPED TERMINATIONS GENERAL REQUIREMENTS**

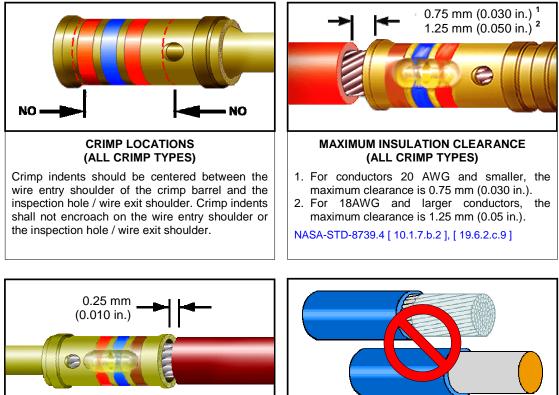


#### **CRIMPED TERMINATIONS**

Crimping is an efficient and highly reliable method to assemble and terminate conductors, and typically provides a stronger, more reliable termination method than that achieved by soldering.

Crimp terminations are available in different styles, depending upon the design application and connectivity requirements.

This section details the generic accept / reject criteria of commonly used crimp termination styles. See 2.02 - 2.10 for specific accept / reject criteria applicable to individual crimp styles.



MINIMUM INSULATION CLEARANCE (ALL CRIMP TYPES)

The minimum insulation clearance for all crimped connections is 0.25 mm (0.010 in.).

NASA-STD-8739.4 [ 10.1.7.b.1 ], [ 19.6.2.c.9 ]



SOLDER-TINNED STRANDED WIRE SOLID WIRE

Crimping of solid wire, component leads, or stranded wire that has been solder-tinned, is prohibited.

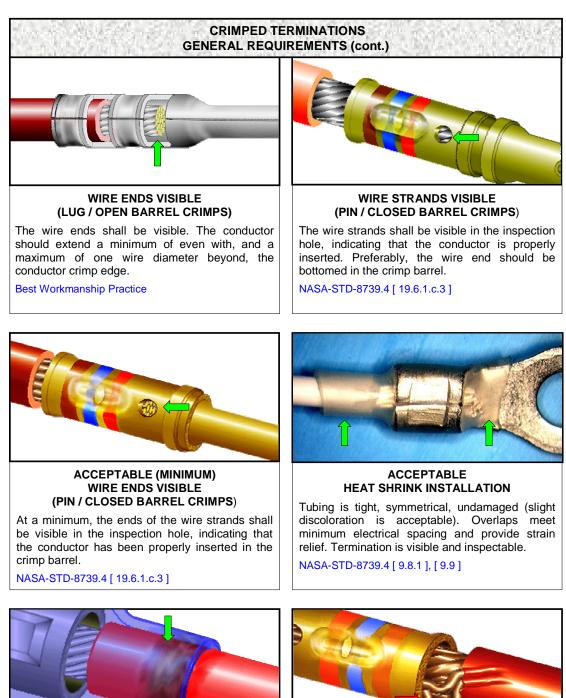
NASA-STD-8739.4 [ 4.3.4 ]

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ACCEPTABLE DISCOLORATION

Slight discoloration of the shrink tubing is acceptable. Evidence of burning or charring is not acceptable.

**Best Workmanship Practice** 



#### **UNACCEPTABLE BIRDCAGED STRANDS**

Birdcaged strands reduce the conductor's overall strength and increase the possibility of shorting.

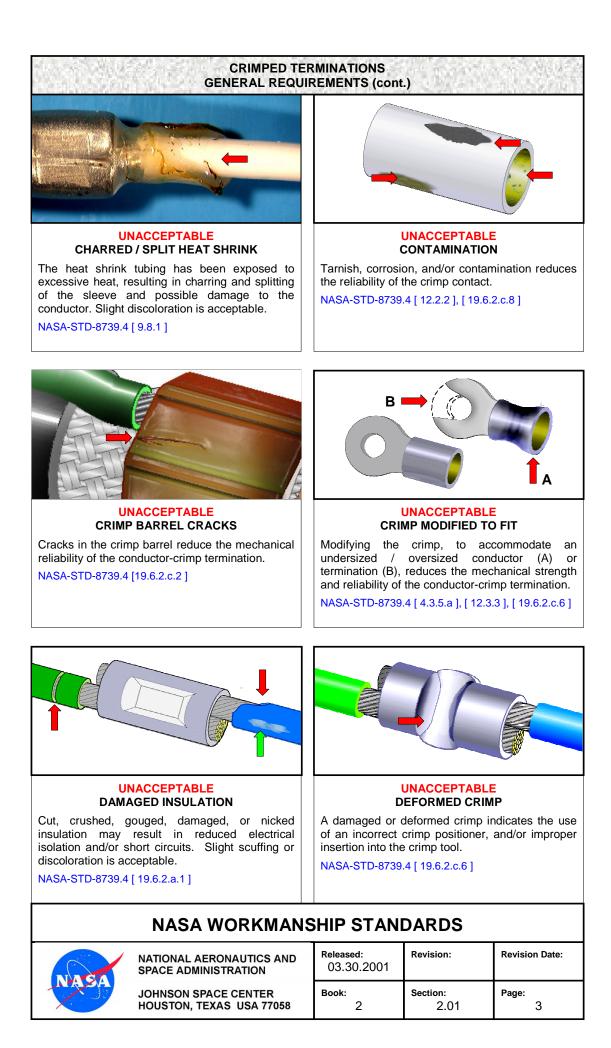
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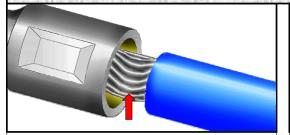


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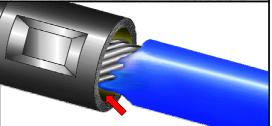


#### CRIMPED TERMINATIONS GENERAL REQUIREMENTS (cont.)



UNACCEPTABLE DISTURBED LAY

Disturbing the lay of wire strands during crimping may reduce the reliability of the crimp termination. Best Workmanship Practice



#### UNACCEPTABLE EDGE FLASH / INSULATION WHISKERS

Excessive edge flash or insulation whiskers that extend into the conductor crimp section may interfere with the proper mechanical and electrical termination of the crimp.

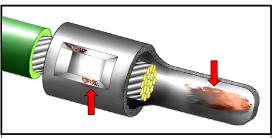
NASA-STD-8739.4 [ 19.6.2.c.10 ]



UNACCEPTABLE EXCESSIVE CONDUCTOR LENGTH

The conductor should extend a minimum of flush with, and a maximum of one (1) wire diameter beyond the conductor crimp edge.

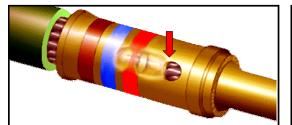
**Best Workmanship Practice** 



UNACCEPTABLE EXPOSED BASE METAL

Exposed base metal reduces the reliability of the crimp.

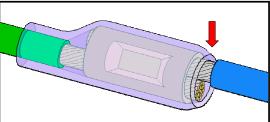
NASA-STD-8739.4 [ 12.2.5 ], [19.6.2.c.6 ]



#### UNACCEPTABLE IMPROPER CRIMP LOCATION (INSPECTION HOLE)

The indents shall not encroach on or distort the inspection hole.

NASA-STD-8739.4 [ 19.6.2.c.7]



#### UNACCEPTABLE IMPROPER HEAT SHRINK LENGTH

Heat shrink tubing conforms to the crimp outline, but does not extend over the wire to provide any sealing or strain relief to the conductor.

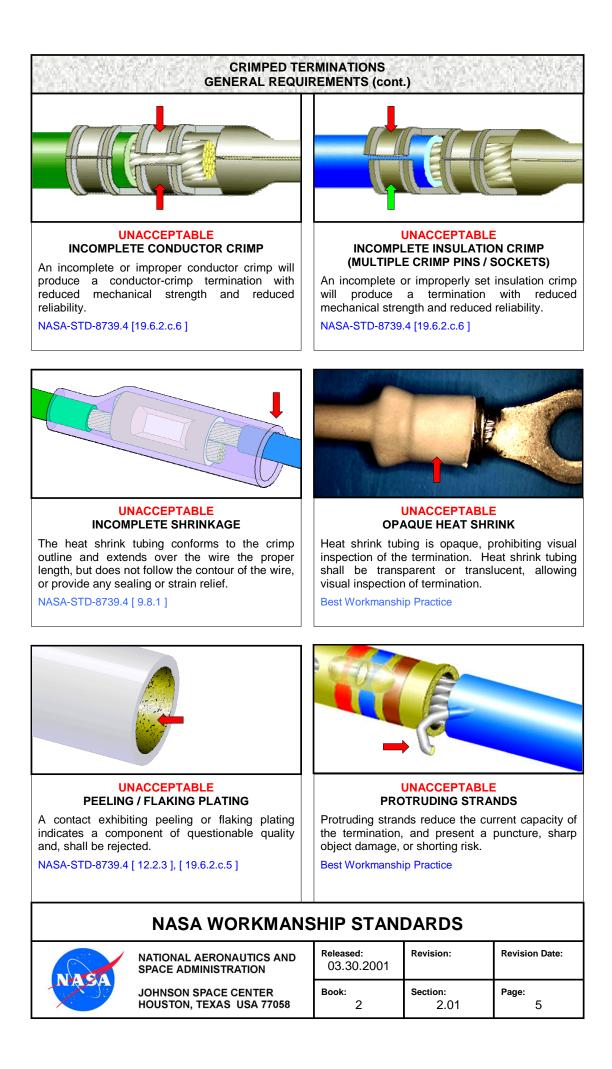
NASA-STD-8739.4 [ 9.9 ]

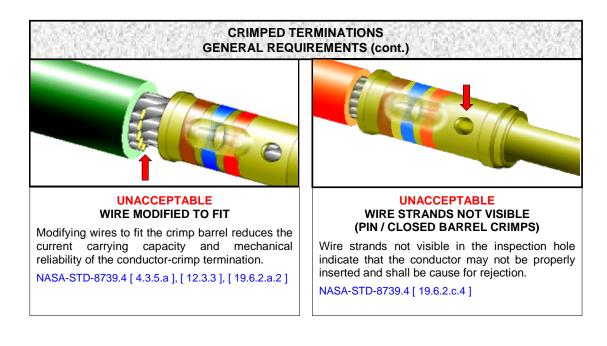
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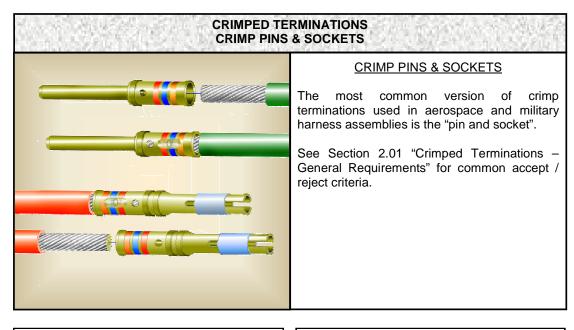
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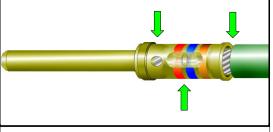
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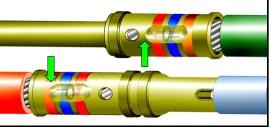




#### PREFERRED

The contact has been deformed only by tool indenters. Indents symmetrical and centered between the inspection hole and the wire entry shoulder. No exposed base metal or other damage. Wire strands are visible in the inspection hole. Proper insulation spacing.

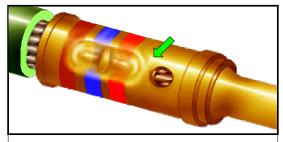
NASA-STD-8739.4 [ 19.6.1.c ]



ACCEPTABLE **CRIMP LOCATION – VIEW 1** 

The indents are at the maximum allowable positions (adjacent to the crimp boundary), but will not encroach on or distort the wire entry shoulder of the crimp barrel and the inspection hole if the contact is rotated (see view 2).

NASA-STD-8739.4 [ 19.6.1.c ]



ACCEPTABLE **CRIMP LOCATION – VIEW 2** 

The indent is located adjacent to the inspection hole, but does not encroach on or distort the inspection hole.

NASA-STD-8739.4 [ 19.6.1.c ]

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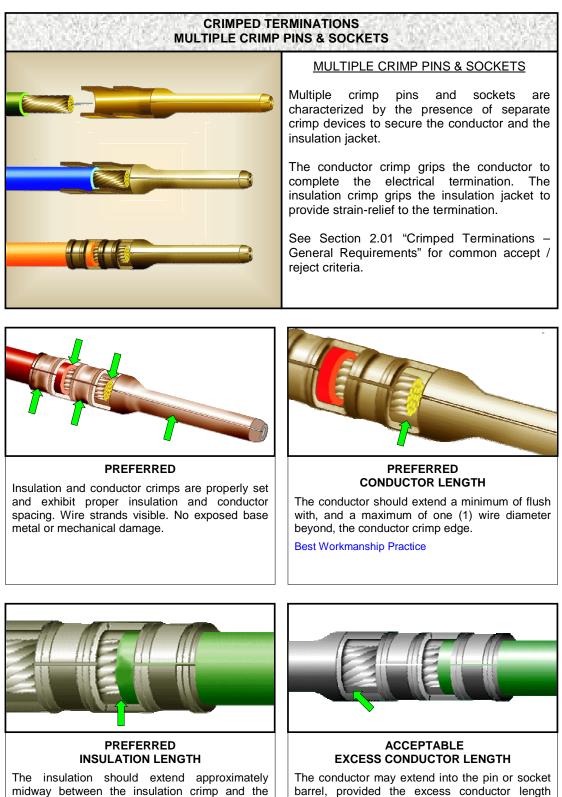


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CRIMPED TERMINATIONS CRIMP PINS & SOCKETS (cont.)

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midway between the insulation crimp and the conductor crimp.

**Best Workmanship Practice** 



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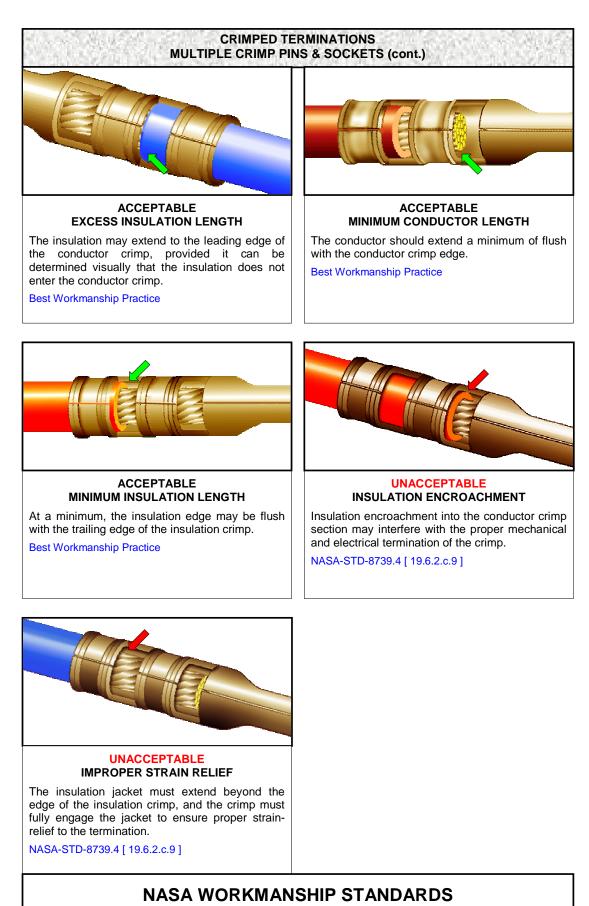
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does not interfere with the mechanical and

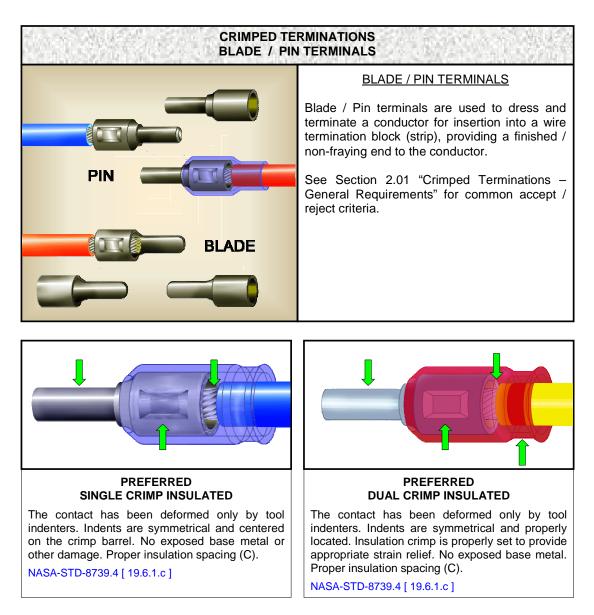
electrical mating of the pin and/or socket.

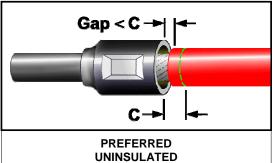


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The contact has been deformed only by tool indenters. Indents are symmetrical and centered on the crimp barrel. No exposed base metal or other damage. Proper insulation spacing (C). NASA-STD-8739.4 [19.6.1.c]

146A-01D-0739.4 [ 19.0.1.0 ]

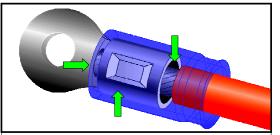
NASA WORKMANSHIP STANDARDS					
	NATIONAL AERONAUTICS AND SPACE ADMINISTRATION	Released: 03.30.2001	Revision:	Revision Date:	
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#### CRIMPED TERMINATIONS BLADE / PIN TERMINALS (cont.)

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NASA WORKMANSHIP STANDARDS					
	NATIONAL AERONAUTICS AND SPACE ADMINISTRATION	Released: 03.30.2001	Revision:	Revision Date:	
NASA	JOHNSON SPACE CENTER HOUSTON, TEXAS USA 77058	Book: 2	Section: 2.04	Page: 2	

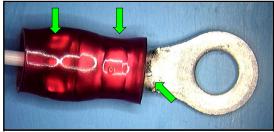
# CRIMPED TERMINATIONS<br/>RING LUG TERMINALSRING LUG TERMINALSRING LUG TERMINALSRing lug terminals are used to dress and<br/>terminate a conductor in a configuration<br/>requiring a mechanically captured connection<br/>to a termination point or post.The "capture" feature of a ring lug prevents the<br/>terminal from falling off the termination post,<br/>even if the compression nut has loosened. This<br/>additional security feature may be beneficial in<br/>high vibration applications.See Section 2.01, "Crimped Terminations –<br/>General Requirements", for common accept /<br/>reject criteria.



PREFERRED SINGLE CRIMP INSULATED

The contact has been deformed only by tool indenters. Indents are symmetrical and centered on the crimp barrel. No exposed base metal or other damage. Wire strand ends are visible. Proper insulation spacing (C).

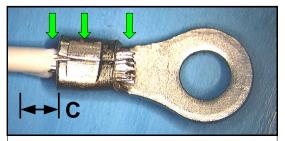
NASA-STD-8739.4 [ 19.6.1.c ]



PREFERRED DUAL CRIMP INSULATED

The contact has been deformed only by tool indenters. Indents are symmetrical and properly located. Insulation crimp is properly set to provide appropriate strain relief. Wire strand ends are visible. Proper insulation spacing (C).

NASA-STD-8739.4 [ 19.6.1.c ]



PREFERRED UNINSULATED

The contact has been deformed only by tool indenters. Indents are symmetrical and centered on the crimp barrel. No exposed base metal or other damage. Wire strand ends are visible. Proper insulation spacing (C).

NASA-STD-8739.4 [ 19.6.1.c ]

# NASA WORKMANSHIP STANDARDS



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#### CRIMPED TERMINATIONS RING LUG TERMINALS (cont.)

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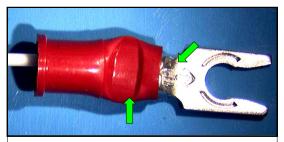
#### CRIMPED TERMINATIONS SPADE LUG TERMINALS



#### SPADE LUG TERMINALS

Spade lug terminals are used to dress and terminate a conductor to a termination point or post with a mechanically secure, "partially captured" connection. The "partially captured" feature allows the terminal to be removed from a termination post without completely removing the compression nut (as is required with ring lugs). This security feature may be beneficial in moderate vibration environments where there is a requirement for the termination to be disconnected.

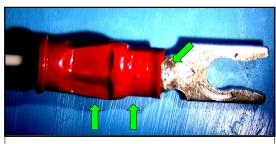
See Section 2.01 "Crimped Terminations – General Requirements" for common accept / reject criteria.



PREFERRED SINGLE CRIMP INSULATED

The contact has been deformed only by tool indenters. Indents are symmetrical and centered on the crimp barrel. No exposed base metal or other damage. Wire strand ends are visible. Proper insulation spacing (C).

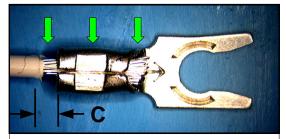
NASA-STD-8739.4 [ 19.6.1.c ]



PREFERRED DUAL CRIMP INSULATED

The contact has been deformed only by tool indenters. Indents are symmetrical and properly located. Insulation crimp is properly set to provide appropriate strain relief. Wire strand ends are visible. Proper insulation spacing (C).

NASA-STD-8739.4 [ 19.6.1.c ]



PREFERRED UNINSULATED

The contact has been deformed only by tool indenters. Indents are symmetrical and centered on the crimp barrel. No exposed base metal or other damage. Wire strand ends are visible. Proper insulation spacing (C).

NASA-STD-8739.4 [ 19.6.1.c ]

# NASA WORKMANSHIP STANDARDS



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#### CRIMPED TERMINATIONS SPADE LUG TERMINALS (cont.)

1.14

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NASA WORKMANSHIP STANDARDS				
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# **CRIMPED TERMINATIONS BUTT SPLICES BUTT SPLICES** Butt splices are used to dress and terminate multiple conductors of the same or different gauges in an end-to-end or series configuration. See Section 2.01 "Crimped Terminations -General Requirements" for common accept / reject criteria.



PREFERRED SINGLE CRIMP INSULATED

The contact has been deformed only by tool indenters. Indents are symmetrical and centered on the crimp barrel. No exposed base metal or other damage. Wire strand ends are visible. Proper insulation spacing (C).

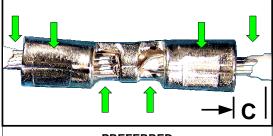
NASA-STD-8739.4 [ 19.6.1.c ]



PREFERRED **DUAL CRIMP INSULATED** 

The contact has been deformed only by tool indenters. Indents are symmetrical and properly located. Insulation crimp is properly set, providing appropriate strain relief. Wire strand ends are visible. Proper insulation spacing (C).

NASA-STD-8739.4 [ 19.6.1.c ]



PREFERRED UNINSULATED

The contact has been deformed only by tool indenters. Indents are symmetrical and centered on the crimp barrel. No exposed base metal or other damage. Wire strand ends are visible. Proper insulation spacing (C).

NASA-STD-8739.4 [ 19.6.1.c ]

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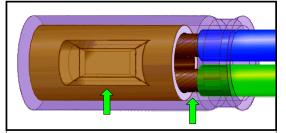
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CRIMPED TERMINATIONS BUTT SPLICES (cont.)

10.45

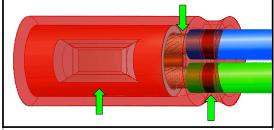
NASA WORKMANSHIP STANDARDS				
NASA	NATIONAL AERONAUTICS AND SPACE ADMINISTRATION	Released: 03.30.2001	Revision:	Revision Date:
	JOHNSON SPACE CENTER HOUSTON, TEXAS USA 77058	Book: 2	Section: 2.07	Page: 2

CRIMPED TERMINATIONS END SPLICES				
	END SPLICES End Splices are used to terminate two or more conductors in a "pig-tail" configuration, and to "dead-end" a single conductor. They can be used as inline splices if proper strain relief is provided. See Section 2.01 "Crimped Terminations – General Requirements" for common accept / reject criteria.			



PREFERRED SINGLE CRIMP INSULATED

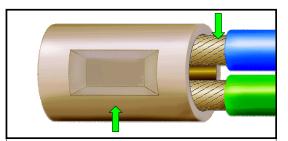
The contact has been deformed only by tool indenters. Indents are symmetrical and centered on the crimp barrel. No exposed base metal or other damage. Proper insulation spacing (C). NASA-STD-8739.4 [19.6.1.c]



PREFERRED DUAL CRIMP INSULATED

The contact has been deformed only by tool indenters. Indents are symmetrical and properly located. Insulation crimp is properly set to provide appropriate strain relief. No exposed base metal. Proper insulation spacing (C).

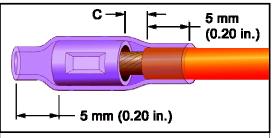
NASA-STD-8739.4 [ 19.6.1.c ]



PREFERRED UNINSULATED

The contact has been deformed only by tool indenters. Indents are symmetrical and centered on the crimp barrel. No exposed base metal or other damage. Proper insulation spacing (C).

NASA-STD-8739.4 [ 19.6.1.c ]



#### ACCEPTABLE DEAD-END CONFIGURATION

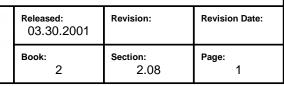
The contact has been deformed only by tool indenters. Indents are symmetrical and centered on the crimp barrel. No exposed base metal or other damage. Proper insulation spacing (C). Shrink tubing has been properly installed.

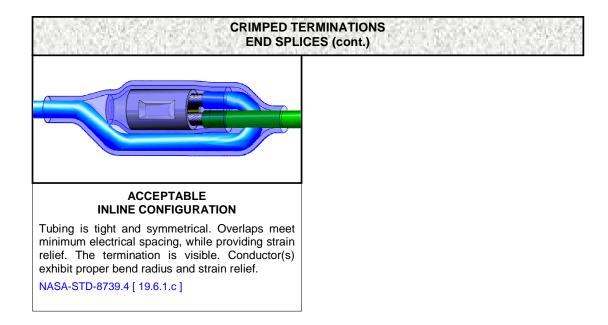
NASA-STD-8739.4 [ 19.6.1.c ]

# NASA WORKMANSHIP STANDARDS



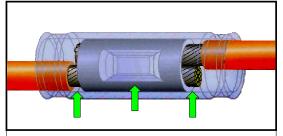
NATIONAL AERONAUTICS AND SPACE ADMINISTRATION





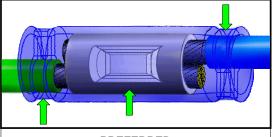
NASA WORKMANSHIP STANDARDS					
NASA	NATIONAL AERONAUTICS AND SPACE ADMINISTRATION	Released: 03.30.2001	Revision:	Revision Date:	
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CRIMPED TERMINATIONS PARALLEL SPLICES			
PARALLEL SPLICES			
	Parallel splices are used to dress and terminate multiple conductors, of the same or different gauges, in a parallel configuration.		
	See Section 2.01 "Crimped Terminations – General Requirements" for common accept / reject criteria.		



PREFERRED SINGLE CRIMP INSULATED

The contact has been deformed only by tool indenters. Indents are symmetrical and centered on the crimp barrel. No exposed base metal or other damage. Wire strand ends are visible. Proper insulation spacing (C).

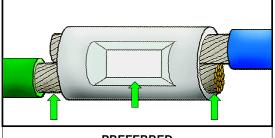


PREFERRED DUAL CRIMP INSULATED

The contact has been deformed only by tool indenters. Indents are symmetrical and properly located. Insulation crimps are properly set to provide appropriate strain relief. No exposed base metal. Proper insulation spacing (C).

NASA-STD-8739.4 [ 19.6.1.c ]

NASA-STD-8739.4 [ 19.6.1.c ]



PREFERRED UNINSULATED

The contact has been deformed only by tool indenters. Indents are symmetrical and centered on the crimp barrel. No exposed base metal or other damage. Wire strand ends are visible. Proper insulation spacing (C).

NASA-STD-8739.4 [ 19.6.1.c ]

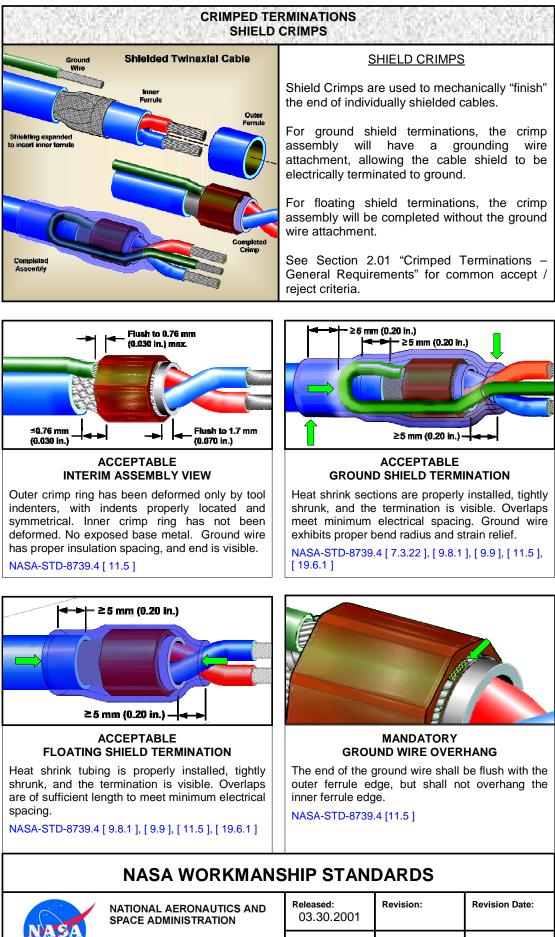
# NASA WORKMANSHIP STANDARDSNATIONAL AERONAUTICS AND<br/>SPACE ADMINISTRATIONReleased:<br/>03.30.2001Revision:<br/>03.30.2001Revision Date:JOHNSON SPACE CENTER<br/>HOUSTON, TEXAS USA 77058Book:<br/>2Section:<br/>2.09Page:<br/>1

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CRIMPED TERMINATIONS PARALLEL SPLICES (cont.)

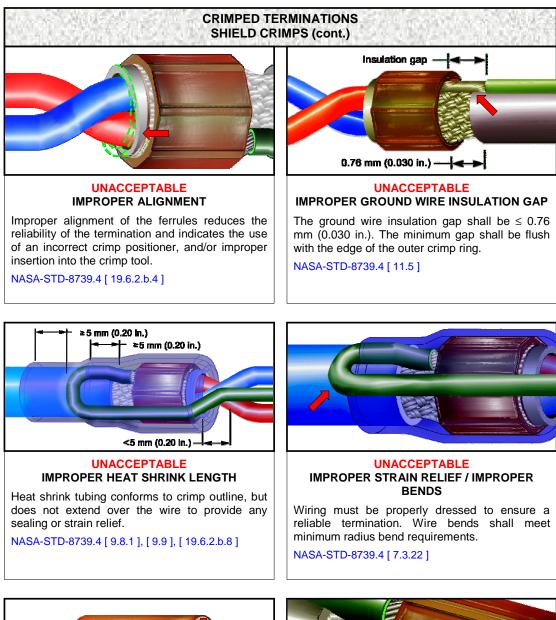
12.46

NASA WORKMANSHIP STANDARDS				
	NATIONAL AERONAUTICS AND SPACE ADMINISTRATION	Released: 03.30.2001	Revision:	Revision Date:
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UNACCEPTABLE INNER FERRULE DAMAGE / DISTORTION

The inner ferrule shall be sized so that the inward distortion caused by the crimping process will not affect the insulated wires it surrounds.

NASA-STD-8739.4 [ 11.5 ]



UNACCEPTABLE NICKED SHIELD STRANDS

Nicked shield strands shall not exceed 10% of the total number of strands.

NASA-STD-8739.4 [ 19.6.2.b.2 ]

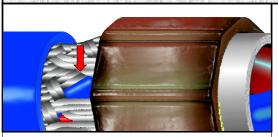
# NASA WORKMANSHIP STANDARDS



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#### CRIMPED TERMINATIONS SHIELD CRIMPS (cont.)



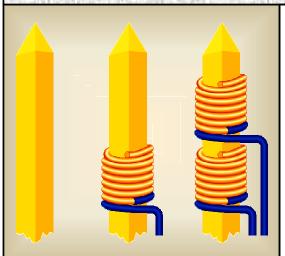
#### UNACCEPTABLE UNEVEN SHIELD COVERAGE

The shield braid shall be dressed to provide uniform coverage and dispersion. Uneven coverage may result in electrical interference in sensitive circuits, and may interfere with the reliability of the crimp assembly.

Best Workmanship Practice

NASA WORKMANSHIP STANDARDS				
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#### DISCRETE WIRING SOLDERLESS WRAPPED ELECTRICAL CONNECTIONS - WIRE WRAP

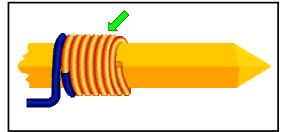


#### WIRE WRAPPING

Solderless wrapped terminations are made by helically wrapping a solid uninsulated wire, around a specially designed termination post, to produce a mechanically and electrically stable connection.

<u>Class A</u>: **Class A** provides improved vibration characteristics, and is the required wrap style for spaceflight hardware applications. This wrap configuration, requires 1/2 to 1-1/2 turns of insulated wire be in contact with a minimum of three (3) corners of the wrappost, in addition to the uninsulated wraps.

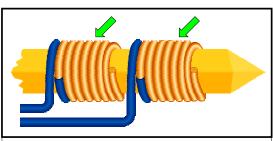
Class B: Class B wraps are prohibited.



ACCEPTABLE CLASS A – SINGLE TERMINATION

The termination has the required number of insulated and uninsulated turns of wire, and is clean and free of foreign material.

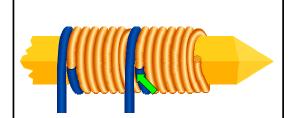
MIL-STD-1130B [ 4.1 ]



ACCEPTABLE CLASS A – MULTIPLE TERMINATIONS

The terminations are properly spaced, with each having the required number of insulated and uninsulated turns of wire, and are clean and free of foreign material.

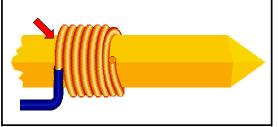
MIL-STD-1130B [ 4.1 ]



#### ACCEPTABLE OVERLAPPED TURNS

The insulated conductor overwrap does not exceed one (1) turn, and the termination wrap is tight.

MIL-STD-1130B [ 5.3.2.1 b ]



UNACCEPTABLE CLASS B

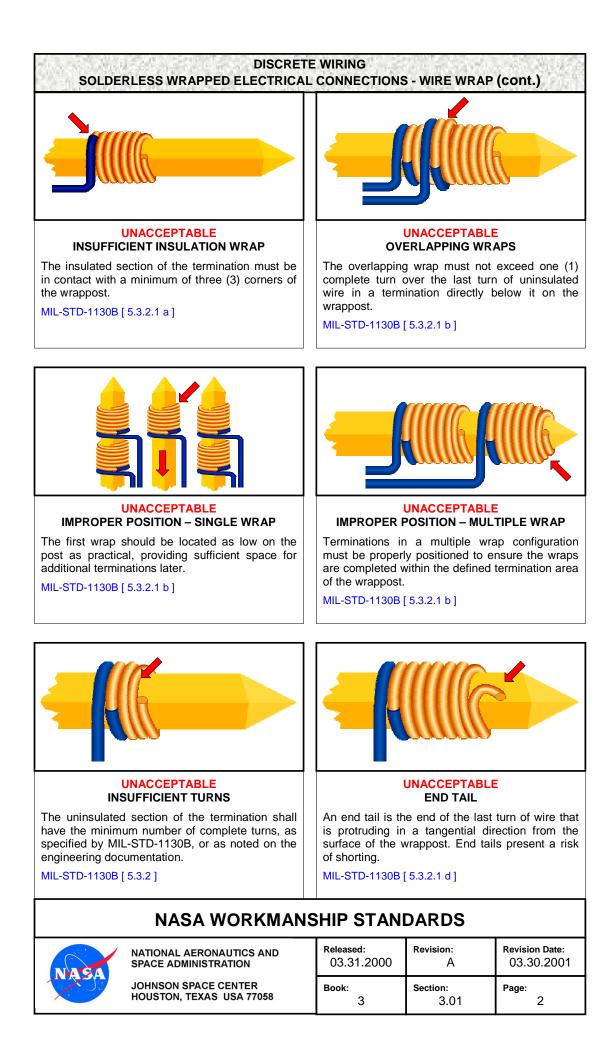
Class B terminations, characterized by the absence of insulated turns, are prohibited. Best Workmanship Practice

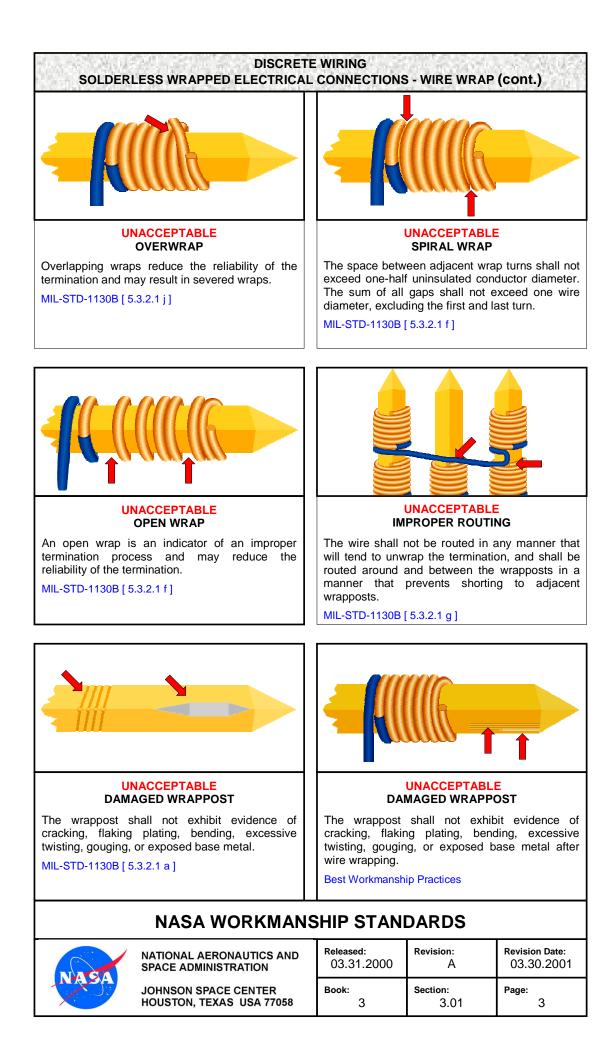
# NASA WORKMANSHIP STANDARDS

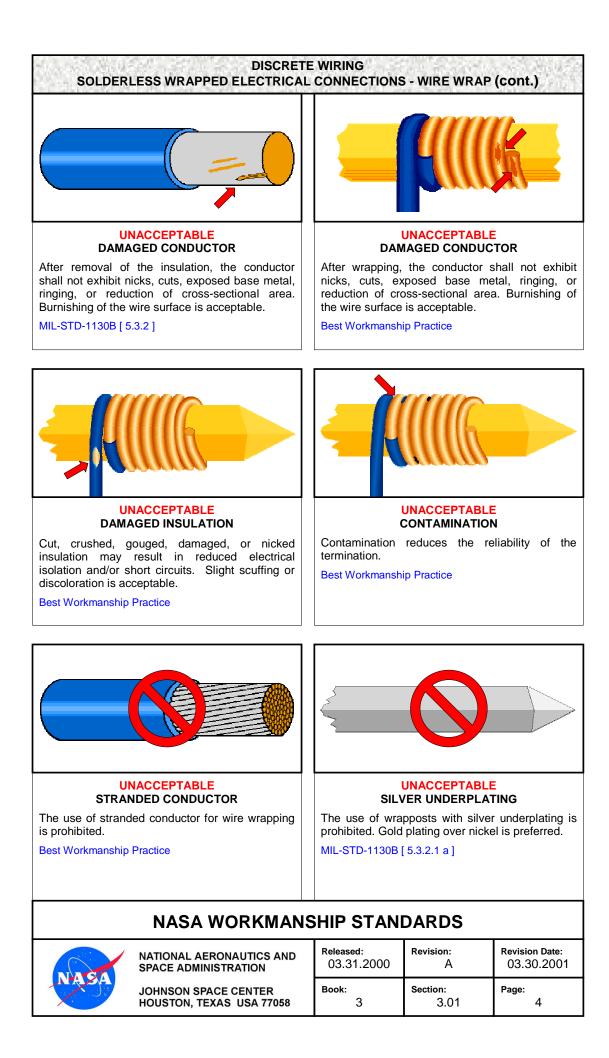


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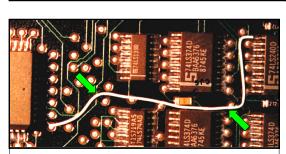






#### DISCRETE WIRING JUMPER WIRES (a.k.a.: haywires) are used to facilitate minor circuit modifications to printed wiring assemblies (PWA), rather than redesign and manufacture a new board. While their use is an accepted practice, <u>the customer must</u> grant approval prior to their use and installation. Jumper wires are usually solid, insulated copper conductor with tin/lead plating (i.e.: wire wrap wire), although jumpers less than 25mm

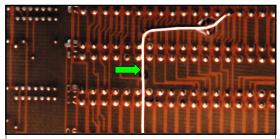
component



PREFERRED COMPONENT TERMINATION SIDE

Wire route is the shortest path. Wire does not pass over or under components, or pass over any land or via used as a test point. Sufficient slack to allow relocation during component replacement.

**Best Workmanship Practice** 



(0.984 in.) may be uninsulated, provided the jumper is not liable to short between lands or

Silver-plated

and/or

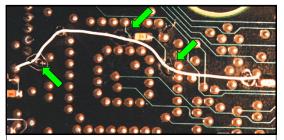
leads.

stranded wire shall not be used.

PREFERRED SOLDER TERMINATION SIDE

Wire route is the shortest path. Wire does not cross component footprints or lands, except where unavoidable. Wire does not pass over any land or via used as a test point.

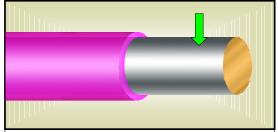
**Best Workmanship Practice** 



MANDATORY STAKING

Jumper wire is staked at intervals specified by engineering documentation. The wire is staked at all changes of direction to restrict movement, and as close to the solder termination as possible.

NASA-STD-8739.1 [ 9.2.4 ]



#### MANDATORY SOLID, INSULATED CONDUCTOR

Jumper wires shall be solid, insulated copper conductor with tin/lead plating (i.e.: wire wrap wire). Stranded, silver-plated wire shall not be used.

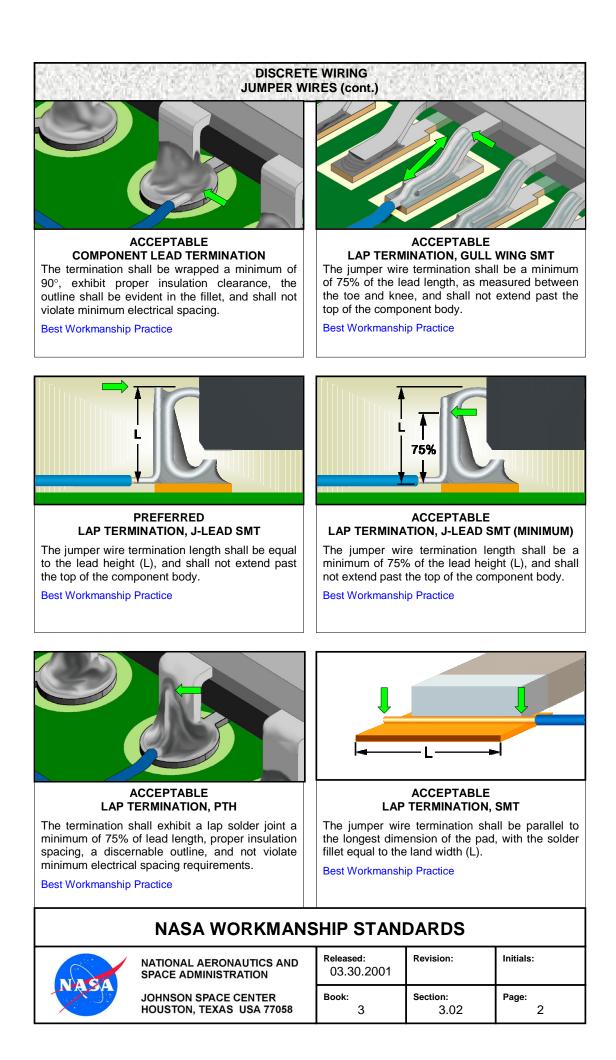
Best Workmanship Practice

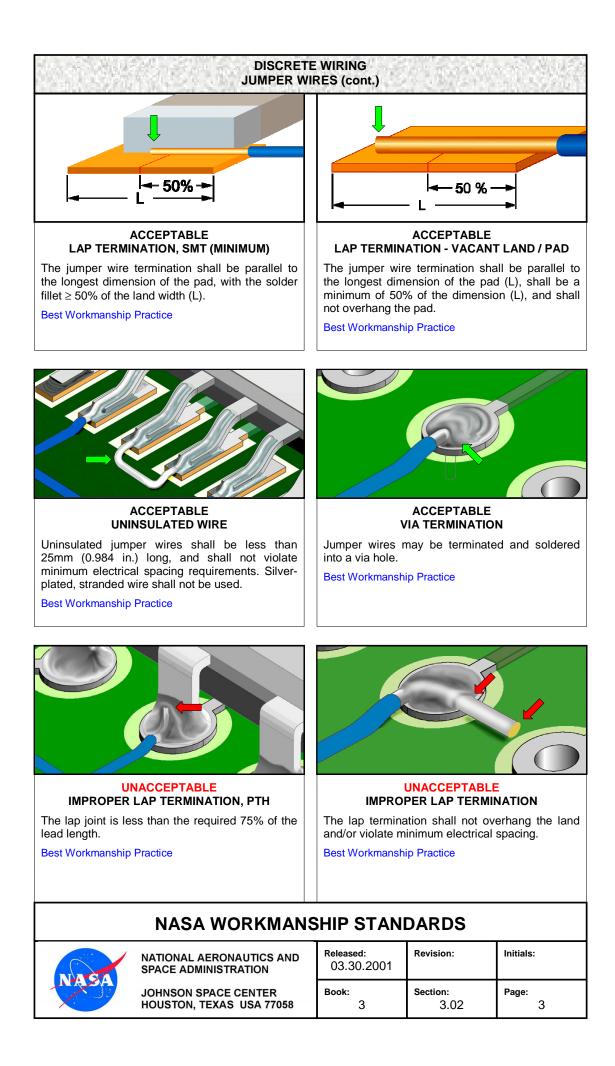
# NASA WORKMANSHIP STANDARDS



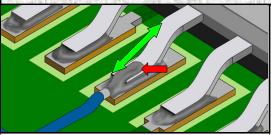
NATIONAL AERONAUTICS AND SPACE ADMINISTRATION

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#### DISCRETE WIRING JUMPER WIRES (cont.)



UNACCEPTABLE IMPROPER LAP TERMINATION GULL WING SMT

The jumper wire termination shall be a minimum of 75% of the lead length (L), as measured between the toe and knee.

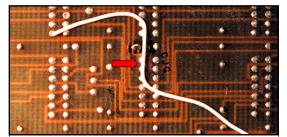
**Best Workmanship Practice** 



#### UNACCEPTABLE IMPROPER LEAD TERMINATION

The termination wrap shall be a minimum of  $90^{\circ}$  and a maximum of  $180^{\circ}$ , with evidence of proper insulation gap, and without overhanging the component termination.

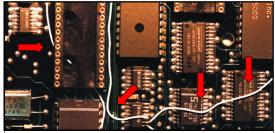
**Best Workmanship Practice** 



UNACCEPTABLE IMPROPER ROUTING (OVER TEST POINTS)

Jumper wires shall not be routed over circuit patterns or vias that are used as test points.

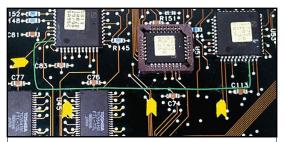
**Best Workmanship Practice** 



UNACCEPTABLE IMPROPER ROUTING (OVER / UNDER COMPONENTS)

Jumper wires shall not be routed over or under components.

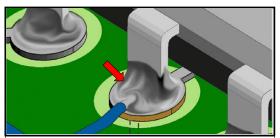
**Best Workmanship Practice** 



#### UNACCEPTABLE IMPROPER STAKING

The jumper wire is not staked as specified. The wire is loose and can extend above the height of adjacent components.

Best Workmanship Practice



#### UNACCEPTABLE IMPROPER TERMINATION (OCCUPIED PTH)

Jumper wires shall not be terminated and soldered into plated-through holes (PTH) occupied by a component lead.

**Best Workmanship Practice** 

# NASA WORKMANSHIP STANDARDS



NATIONAL AERONAUTICS AND SPACE ADMINISTRATION

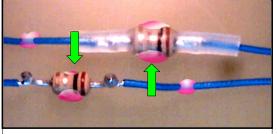
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# DISCRETE WIRING DEADBUGS

#### DEADBUGS

The term "Deadbugs" is an industry nickname for the discrete components added and wired into a printed wiring assembly (PWA) to facilitate active circuit modifications, rather than redesign and manufacture a new board. The nickname comes from their general appearance on the board: upside down, with their termination leads (legs) up in the air – like a dead bug.

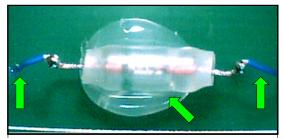
While their use is an accepted practice, the customer must grant approval prior to their use and installation.



PREFERRED AXIAL-LEADED COMPONENT

Component is properly mounted. Lead bends are within limits. Terminations are properly wrapped. The solder joints meet all minimum requirements. Jumper wires have appropriate stress relief.

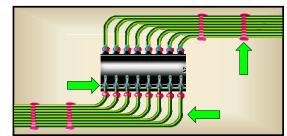
**Best Workmanship Practice** 



PREFERRED GLASS-BODIED COMPONENT

Component is covered with a transparent resilient sleeving, and properly mounted. Lead bends are within limits. Terminations are properly wrapped. The solder joints meet all minimum requirements. Jumper wires have appropriate stress relief.

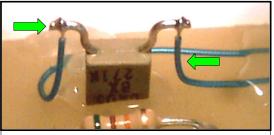
**Best Workmanship Practice** 



#### PREFERRED DUAL-INLINE PACKAGE (DIP)

Component is properly mounted and terminated. Jumper wires are properly terminated, with appropriate stress relief. The solder joints meet all minimum requirements.

Best Workmanship Practice



#### PREFERRED RADIAL-LEADED COMPONENT

Component is properly mounted and terminated. Lead bends are within limits. Terminations are properly wrapped. The solder joints meet all minimum requirements. Jumper wires have appropriate stress relief.

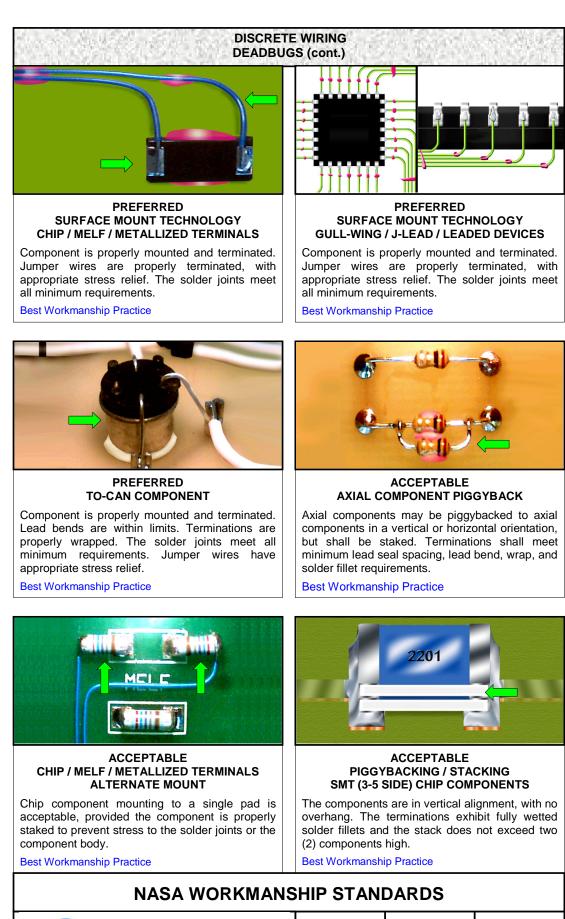
**Best Workmanship Practice** 

# NASA WORKMANSHIP STANDARDS



NATIONAL AERONAUTICS AND SPACE ADMINISTRATION

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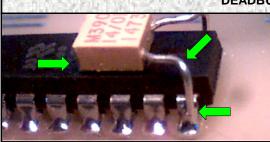




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#### DISCRETE WIRING DEADBUGS (cont.)



#### ACCEPTABLE PIGGYBACKING / STACKING TO ICs AXIAL / RADIAL / SMT COMPONENTS

IC piggybacking is acceptable when space and/or noise requirements prohibit more traditional placement methods. Component leads / jumper wires shall meet minimum bend requirements.

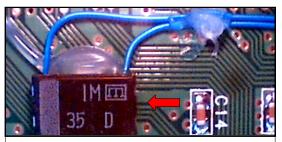
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#### UNACCEPTABLE IMPROPER LEAD DRESS

Dead-bugged components shall be mounted and dressed in a manner that prevents shorting of the leads to the component case (pictured) or to other conductors.

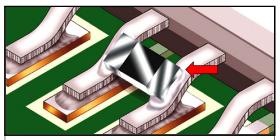
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UNACCEPTABLE IMPROPER MOUNTING ORIENTATION

Components shall be mounted with the leads in an orientation that ensures the terminations meet minimum electrical spacing requirements. As pictured, the component terminals are resting on exposed circuitry and vias.

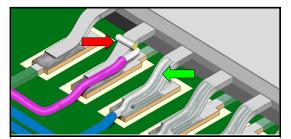
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UNACCEPTABLE IMPROPER MOUNTING SMT COMPONENTS MOUNTED ON LEADS

Chip and MELF devices shall not be directly mounted on component leads of integrated circuit (chip) packages.

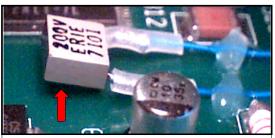
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#### UNACCEPTABLE IMPROPER SOLDER TERMINATION LEADED DEVICES

Jumper wires shall be lap soldered to the device leads. Wrapped terminations place stress on the component lead, and may violate minimum lead-to-lead electrical spacing requirements.

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UNACCEPTABLE IMPROPER STAKING

The component shall be secured with an adhesive material, per engineering documentation.

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# DISCRETE WIRING DEADBUGS (cont.)

#### UNACCEPTABLE IMPROPER TERMINATION WRAP

Jumper wires shall be wrapped at least  $180^{\circ}$  to  $270^{\circ}$  around the component lead prior to soldering, and shall not be located closer than one (1) lead diameter to end of the component lead.

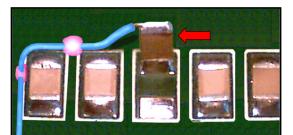
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#### UNACCEPTABLE PIGGY-BACK / STACKING CYLINDRICAL / MELF COMPONENTS

The piggy-backing / stacking of cylindrical / glassbodied / MELF components is not recommended.

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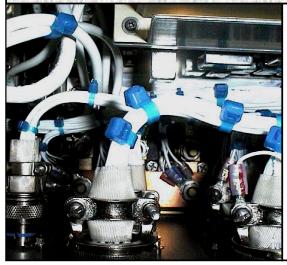
UNACCEPTABLE TOMBSTONED TERMINATION

Deadbugged components shall be mounted parallel to and in contact with the base laminate, or base component (if applicable). Tombstoning places unacceptable stress on the component/solder pad termination.

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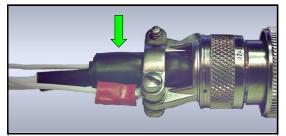
# NASA WORKMANSHIP STANDARDS NATIONAL AERONAUTICS AND SPACE ADMINISTRATION Released: 05.31.2002 Revision: Revision Date: JOHNSON SPACE CENTER HOUSTON, TEXAS USA 77058 Book: 3.03 Section: 3.03 Page: 4

#### CABLE AND HARNESS GENERAL REQUIREMENTS



#### CABLE AND HARNESS GENERAL REQUIREMENTS

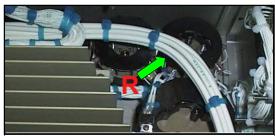
Often the most overlooked and ignored component of any electrical / electronic design, cables and harnesses are essential to the accurate and rapid transmission of data and control signals.



PREFERRED BACKSHELL

Connector backshells shall be potted and molded, or use stress relief boots as required, in accordance with applicable engineering documentation.

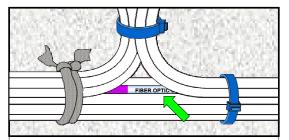
NASA-STD-8739.4 [ 13.1.1.c ]



PREFERRED BEND RADIUS

Cables and harnesses shall not be subjected to bending forces resulting in radii less than the minimum specified for the most sensitive component (i.e.: coaxial, fiber, etc.) in the assembly.

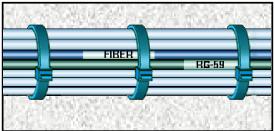
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#### PREFERRED COAXIAL / FIBER OPTIC CABLE LOCATION

Harnesses should be designed so that coaxial / fiber optic cables are located at or near the bundle center to minimize flexure, and to provide additional protection.

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#### PREFERRED COAXIAL / FIBER OPTIC CABLES

Coaxial cables (flexible, semi-rigid, rigid) and fiber optic cables shall exhibit a neatly organized layout, with smooth bends and sufficient stress relief.

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#### CABLE AND HARNESS GENERAL REQUIREMENTS (cont.)



#### PREFERRED CONNECTOR STYLE

Connectors shall be straight, right-angle, or flange-mount. The use of right-angle connectors shall be minimized and restricted to applications where stress-free mounting of the cable assembly can be assured.

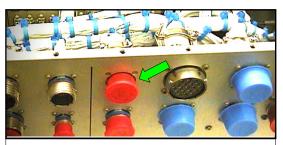
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#### PREFERRED DISCRETE WIRE HARNESSES

Harnesses exhibit a neatly organized layout, with smooth bends and sufficient stress relief. Connector cable clamps and cable straps are properly set.

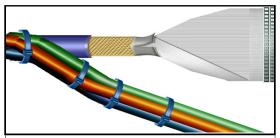
NASA-STD-8739.4 [ 19.6.1.e ]



PREFERRED DUST CAPS

The mating surfaces of all unmated connectors shall be protected by covers during storage, handling, and installation. Connectors on ESD sensitive hardware shall be protected by ESD rated dust caps / covers.

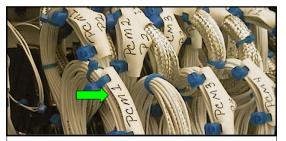
NASA-STD-8739.4 [ 13.1.2 ], [ 16.2.4 ], [ 16.3.3 ]



PREFERRED HYBRID CABLES / HARNESSES

Hybrid cables / harnesses (copper / fiber optic / coaxial conductors) shall be designed to comply with the requirements of the most sensitive and demanding component (typically the fiber optic cable) in the assembly.

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PREFERRED IDENTIFICATION CABLES / HARNESSES

Each cable / harness shall be identified by a permanent label / marking.

NASA-STD-8739.4 [ 14.2.1 ]



#### PREFERRED IDENTIFICATION CONNECTOR

Each connector shall be identified by a permanent label / marking affixed directly to the connector body, or to the cable adjacent to the connector.

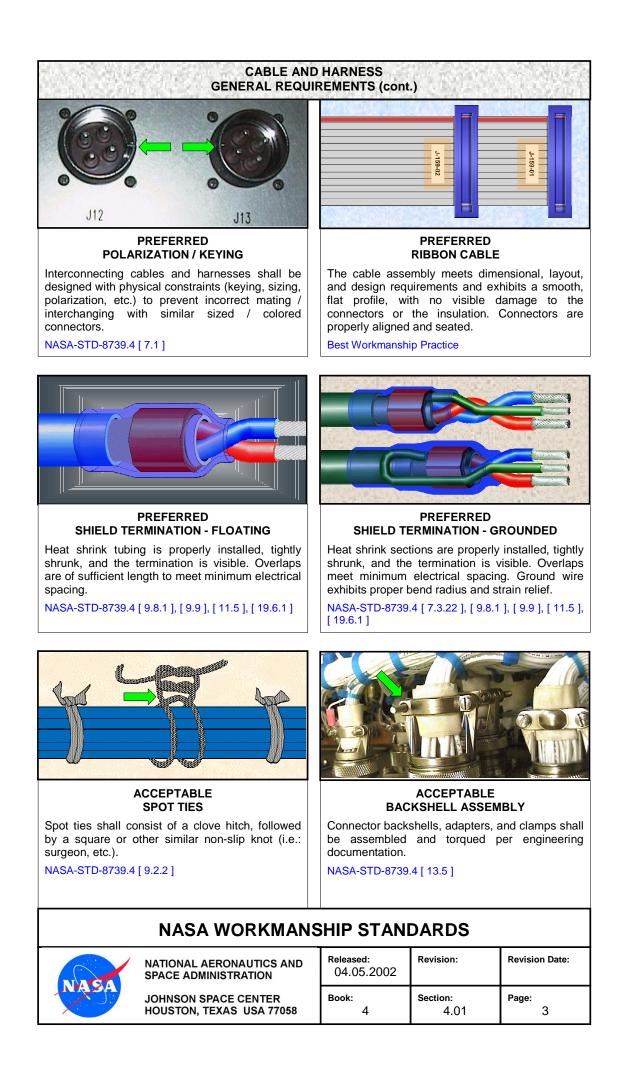
NASA-STD-8739.4 [ 14.2.2 ]

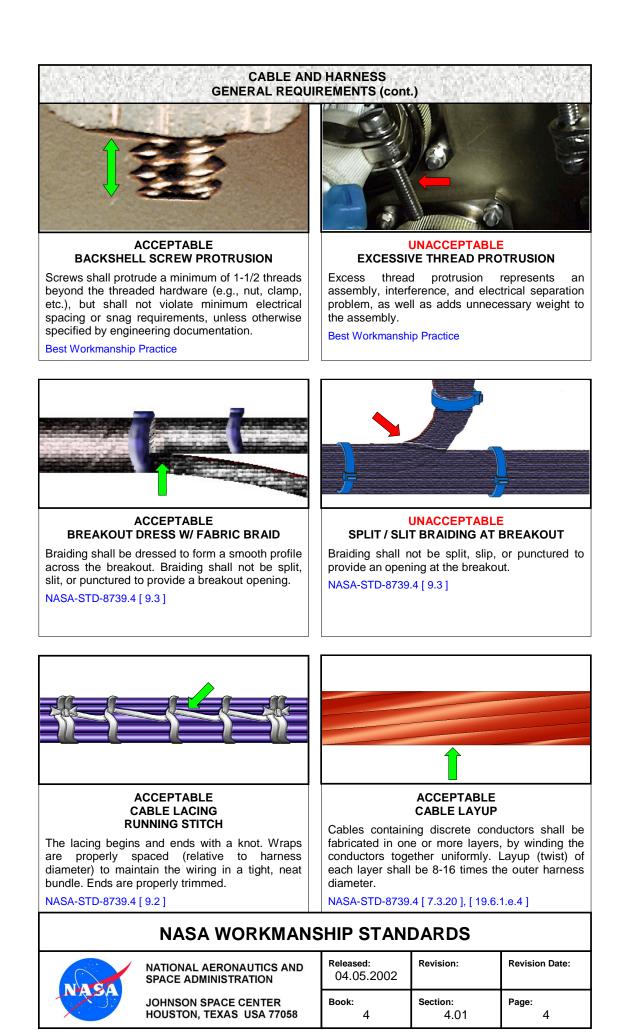
# NASA WORKMANSHIP STANDARDS

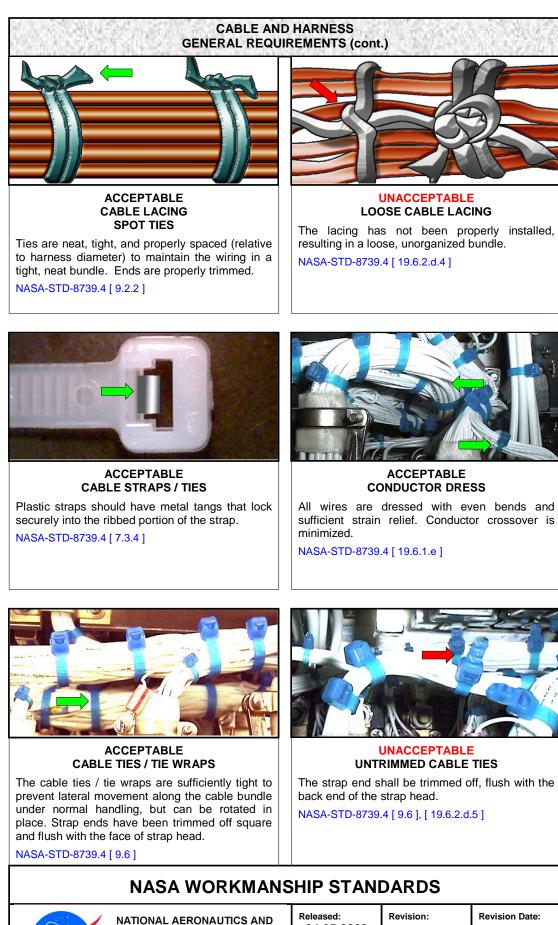


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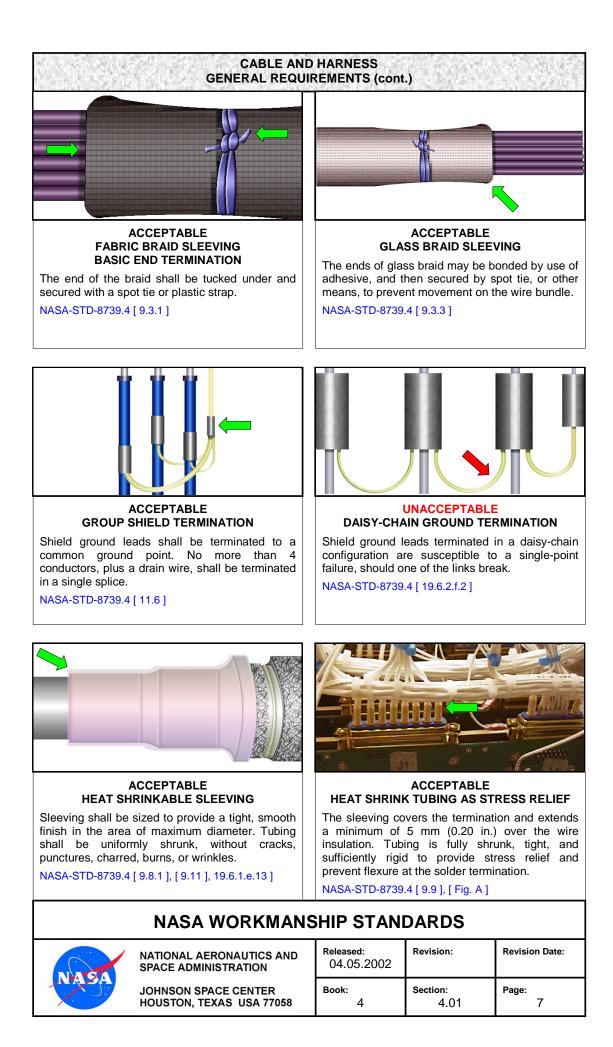


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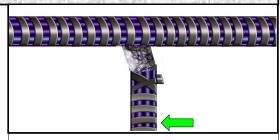






ACCEPTABLE SHIELD / DRAIN WIRE

The shield and drain wire have been properly terminated, per engineering documentation. NASA-STD-8739.4 [ 19.6.1.f.2 ]



ACCEPTABLE SPIRAL WRAP SLEEVING

Spiral wrap shall be tight, uniformly spaced, and shall not overlap. Ends shall be trimmed to eliminate sharp edges.

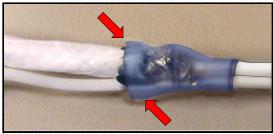
Note: Spiral wrap shall not be used on spacecraft or launch vehicles.

NASA-STD-8739.4 [ 9.5 ]



ACCEPTABLE SOLDER SLEEVE TERMINATION

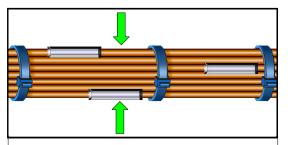
The solder sleeve has been properly installed and tightly shrunk. Strain relief is acceptable. Overlaps are of sufficient length to meet minimum electrical spacing. Solder fillet is visible, fully flowed, and smooth. NASA-STD-8739.4 [ 11.4 ]



**UNACCEPTABLE** SOLDER SLEEVE TERMINATION

The solder sleeve has not been completely shrunk and is improperly positioned, resulting in a poor fit that does not provide a good mechanical grip or seal, and which does not meet minimum overlap requirements.

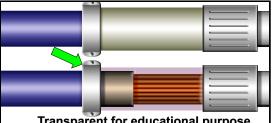
NASA-STD-8739.4 [ 9.8.1 ], [ 9.11 ]



#### ACCEPTABLE SPLICE ASSEMBLY PROFILE

The location of splices shall be staggered to minimize the increase in profile to the harness. Final assembly profile shall not impact form, fit, or function.

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Transparent for educational purpose

#### ACCEPTABLE STRAIN RELIEF

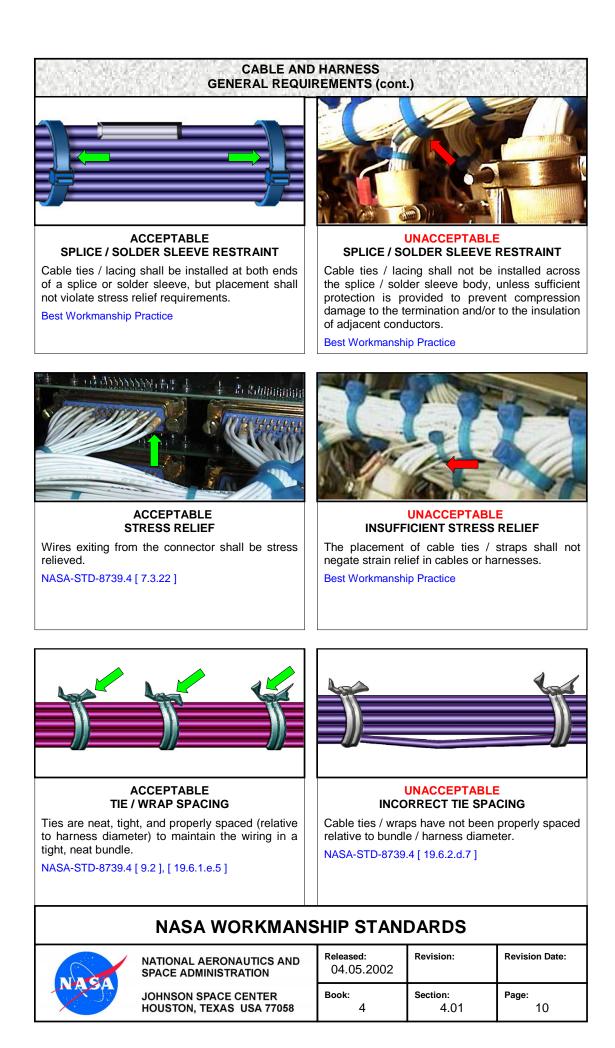
The cable (shielded / unshielded) should be dressed to ensure that the strain relief mechanism transfers structural stresses from the connector to the cable sheath (or strength member) rather than to the individual conductors. **Best Workmanship Practice** 

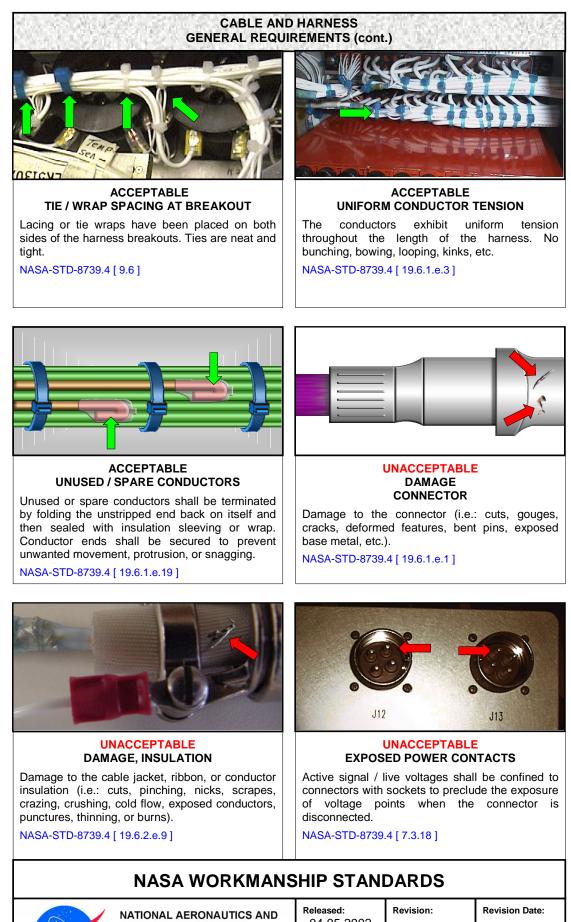
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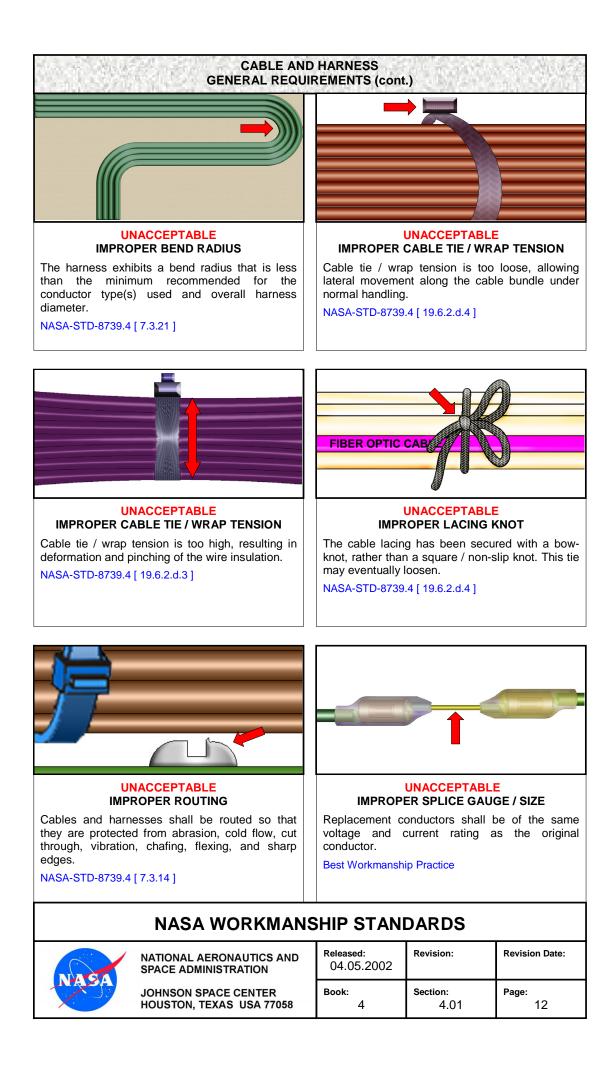


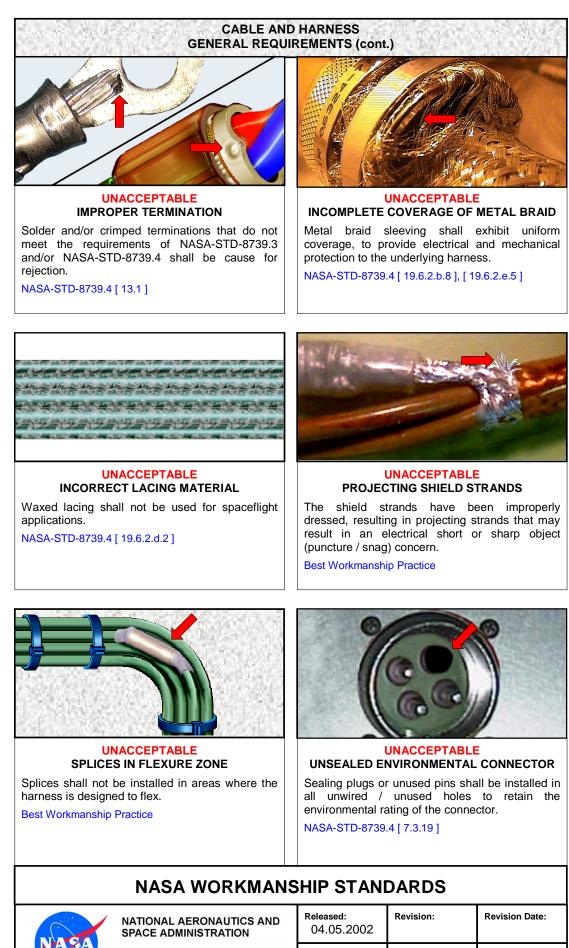




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#### CABLE AND HARNESS GENERAL REQUIREMENTS (cont.)

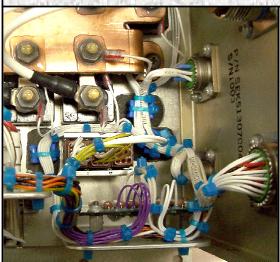
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#### CABLE AND HARNESS DISCRETE CONDUCTOR HARNESSES



#### DISCRETE CONDUCTOR HARNESSES

Discrete conductor harnesses are built to print for specific applications, and are constructed of one or more individually insulated wires, cables, or fiber optics; with or without an overall helical twist; with or without an overall covering, jacket, or metallic braid; with or without breakouts; assembled with two or more electrical termination devices; and engineered as a unit that can be assembled and handled as a single component.

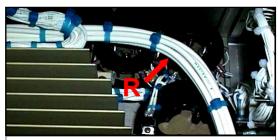
See Section 4.01 "Cable and Harness, General Requirements", for common accept / reject criteria.



PREFERRED GENERAL HARNESSES

Harness and connectors are clean, damage-free, and free of contamination and/or corrosion. Dimensions, layout, and identification meet design requirements.

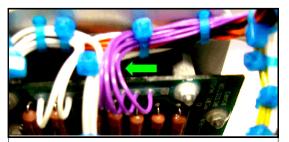
NASA-STD-8739.4 [ 19.6.1.e ]



MANDATORY BEND RADIUS

Cables and harnesses shall not be subjected to bending forces resulting in radii less than the minimum specified for the most sensitive component (i.e.: coaxial, fiber, etc.) in the assembly.

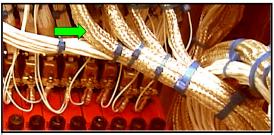
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ACCEPTABLE CONDUCTOR DRESS

All wires are dressed with even bends and sufficient strain relief. Conductor crossover is minimized.

NASA-STD-8739.4 [ 19.6.1.e ]



ACCEPTABLE OVERALL SHIELDING

An overall braided metallic shield provides mechanical and electrical protection (EMI/RFI) to the harness. Metallic shielding shall exhibit a smooth and tight finish, with a uniform distribution of coverage and no projecting strands.

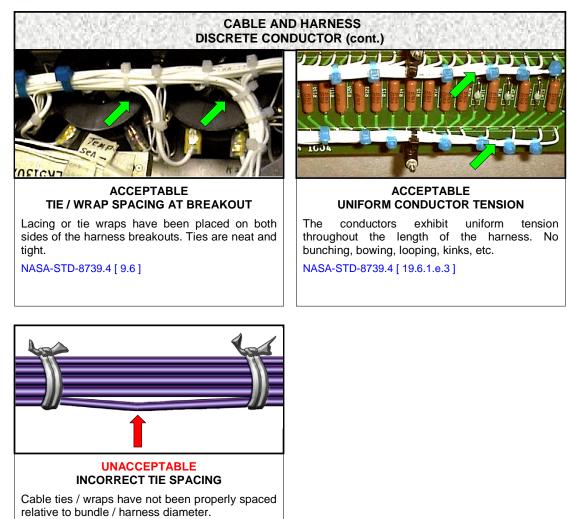
NASA-STD-8739.4 [ 11.1.3 ]

### NASA WORKMANSHIP STANDARDS



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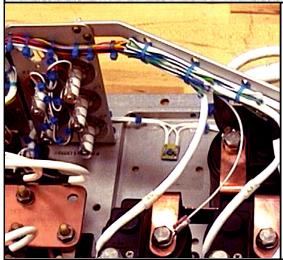
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NASA-STD-8739.4 [ 19.6.2.d.7 ]

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#### CABLE AND HARNESS MULTI-CONDUCTOR



#### MULTI-CONDUCTOR

Multi-conductor cable is an engineered wiring product, typically constructed of two (2) or more individually insulated conductors, bound together by an overall insulation jacket (unshielded); or, bound and wrapped with an overall metallic covering (braid or foil), and covered by an overall insulation jacket (shielded).

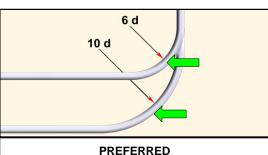
Multiconductor cable is used primarily for the transmission of control and/or data signals.

See Section 4.01 "Cable and Harness, General Requirements", for common accept / reject criteria.



GENERAL REQUIREMENTS

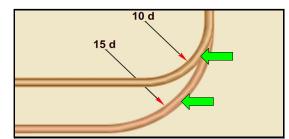
Cable and connectors are clean, damage-free, and free of contamination and/or corrosion. Shields are terminated per engineering requirements. Dimensions, layout, and identification meet design requirements. NASA-STD-8739.4 [19.6.1]



#### BEND RADIUS ( EXCLUDING KAPTON<sup>®</sup> )

Cables insulated with materials other than Kapton<sup>®</sup> shall not be bent less than six (6) outer diameters. The recommended long-term bend radius is ten (10) diameters.

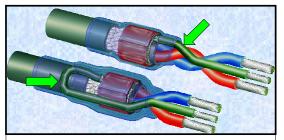
NASA-STD-8739.4 [ 7.3.21 ]



PREFERRED BEND RADIUS (KAPTON<sup>®</sup>)

Kapton<sup>®</sup> insulated cables shall not be bent less than ten (10) outer diameters. The recommended long-term bend radius is fifteen (15) diameters.

NASA-STD-8739.4 [ 7.3.21 ]



ACCEPTABLE SHIELD TERMINATION – CRIMP SLEEVE

Heat shrink sections are properly installed, tightly shrunk, and the termination is visible. Overlaps meet minimum electrical spacing. Ground wire exhibits proper bend radius and strain relief.

NASA-STD-8739.4 [ 7.3.22 ], [ 19.6.1 ]

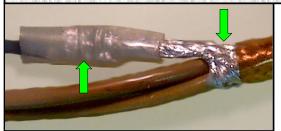
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#### CABLE AND HARNESS MULTI-CONDUCTOR (cont.)



#### ACCEPTABLE SHIELD TERMINATION – LASH SPLICE INTERIM ASSEMBLY

The termination exhibits a fully wetted solder termination. Shield braid is smooth and evenly dressed with no sharp edges or projections. Shrink tubing properly installed and tight.

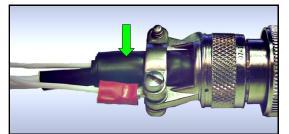
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#### ACCEPTABLE SOLDER SLEEVE TERMINATION

The solder sleeve has been properly installed and tightly shrunk. Strain relief is acceptable. Overlaps are of sufficient length to meet minimum electrical spacing. Solder fillet is visible, fully flowed, and smooth.

NASA-STD-8739.4 [ 11.4 ]



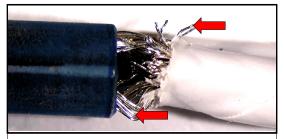
ACCEPTABLE STRAIN RELIEF

The cable (shielded / unshielded) should be dressed to ensure that the strain relief mechanism transfers structural stresses from the connector to the cable sheath (or strength member) rather than to the individual conductors. Best Workmanship Practice

UNACCEPTABLE IMPROPER STRAIN RELIEF

The cable has been dressed in a manner that results in the possible transfer of stress from the connector to the individual conductors, rather than to the cable sheath or stress member.

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#### UNACCEPTABLE PROJECTING SHIELD STRANDS

The shield strands have been improperly dressed, resulting in projecting strands that may result in an electrical short or sharp object (puncture / snag) concern.

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#### CABLE AND HARNESS COAXIAL



#### <u>COAXIAL</u>

Coaxial is an engineered cable product, typically supplied in the form of a central conductor insulated by a dielectric material, held in concentric orientation to a conductive tubing or sheathing that serves both as an EMI/RFI shield and as a return circuit path.

Coaxial systems are available in different technologies, ranging from flexible, insulated cable; to semi-rigid and rigid metallic sheathed.

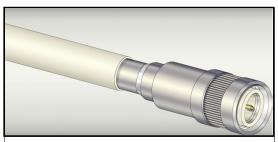
The selection of a particular coaxial cable technology involves the careful consideration of the specific electrical, mechanical, and environmental requirements of the project.



PREFERRED FLEXIBLE CABLE

Cable dimensions and layout meet design requirements, with smooth bends and sufficient stress relief. Connector backshell is properly assembled and torqued. Cable insulation jacket is smooth and continuous, shield properly secured.

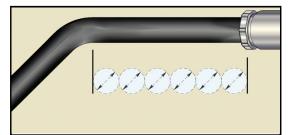
NASA-STD-8739.4 [ 19.6.1.f ]



PREFERRED SEMI-RIGID / RIGID CABLE

Completed cable meets dimensional and layout requirements, with smooth surface, bends, uniform diameter, and sufficient stress relief. Connectors exhibit properly formed solder / weld fillets and are contamination / corrosion-free.

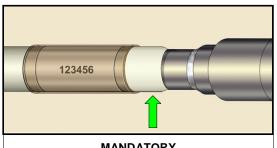
NASA-STD-8739.4 [ 19.6.1.f ]



#### MANDATORY CABLE TERMINUS ALIGNMENT

A minimum straight length of six (6) cable diameters is required at each finished cable end to allow for clearance and strain relief, unless specified otherwise in the engineering documentation.

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#### MANDATORY COATING / FINISH

In applications requiring the cable assembly to be coated or painted, the finish shall be applied to the outer sheath only, and shall stop at least 5 mm (0.20 in.) from the back of the connector. The connector shall not be coated or painted.

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# NASA WORKMANSHIP STANDARDS



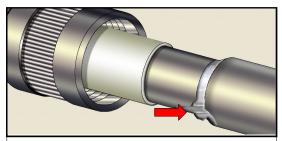
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# **CABLE AND HARNESS** COAXIAL (cont.) MANDATORY MANDATORY FLOATING NUTS MINIMUM BEND RADIUS Semi-rigid / rigid cable assemblies shall be Coaxial cables shall not be bent below the designed with connectors with retractable (noncaptive/floating) coupling nuts, reducing the possibility that the cable assembly will be in a

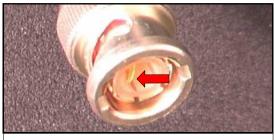
minimum recommended inside bend radius (6 diameters for flexible, 2 diameters for semi-rigid and rigid). state of tension / torsion during connector mating.

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**UNACCEPTABLE EXCESS / IMPROPER SOLDER** 

The solder termination between the connector and the rigid / semi-rigid cable sheath shall exhibit a fully wetted, concave, smooth, and continuous fillet which extends completely around the termination periphery.



**UNACCEPTABLE IMPROPER ASSEMBLY CENTER CONTACT(S)** 

Center contact location / orientation does not meet requirements for proper mating.

NASA-STD-8739.4 [ 19.6.2.f.3 ]

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#### **UNACCEPTABLE IMPROPER ASSEMBLY** CONNECTOR

The connector has not been assembled per the manufacturer's or engineering documentation. The connector body has been crimped by the center pin crimp tool, crushing the dielectric.

NASA-STD-8739.4 [ 19.6.2.f ]



#### **UNACCEPTABLE IMPROPER BEND RADIUS**

The cable has been bent below the minimum radius recommended, resulting in ripples and stretching in the cable sheath and possible coldflow of the dielectric, resulting in increased attenuation and/or shorting.

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# NASA WORKMANSHIP STANDARDS

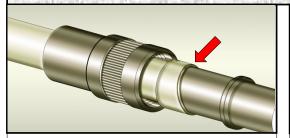


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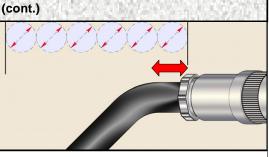
#### CABLE AND HARNESS COAXIAL (cont.)



#### UNACCEPTABLE IMPROPER COATING / PAINT

The coating has been improperly applied, resulting in interference during assembly and mating.

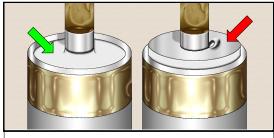
**Best Workmanship Practice** 



#### UNACCEPTABLE IMPROPER TERMINUS SPACING

The termination exhibits an improper minimum straight section length between the connector body and start of nearest bend. This may impede assembly / mating, reduce strain relief, or increase cable impedance.

**Best Workmanship Practice** 



UNACCEPTABLE PROTRUDING DIELECTRIC

Care shall be exercised to minimize the protrusion or melting of the dielectric as a result of overheating during tinning and soldering operations.

**Best Workmanship Practice** 

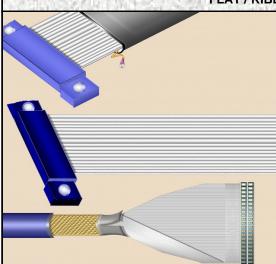
# NATIONAL AERONAUTICS AND SPACE ADMINISTRATION Released: 04.05.2002 Revision: Revision Date: JOHNSON SPACE CENTER HOUSTON, TEXAS USA 77058 Book: 4 Section: 4.04 Page: 3

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CABLE AND HARNESS COAXIAL (cont.)

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#### CABLE AND HARNESS FLAT / RIBBON CABLE

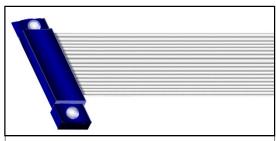


## FLAT / RIBBON CABLE

Flat cable is a multi-conductor cable comprised of individually insulated, solid conductors, which are mechanically bonded in a parallel (flat) orientation.

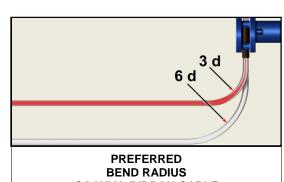
Ribbon cable is a multi-conductor cable, comprised of individually insulated, stranded conductors, which are mechanically bonded to each other in a parallel (flat) orientation.

Both cable architectures result in a highly flexible, compact, and robust cable, allowing mass termination of the conductors to highdensity connectors by the insulation displacement contact (IDC) process.



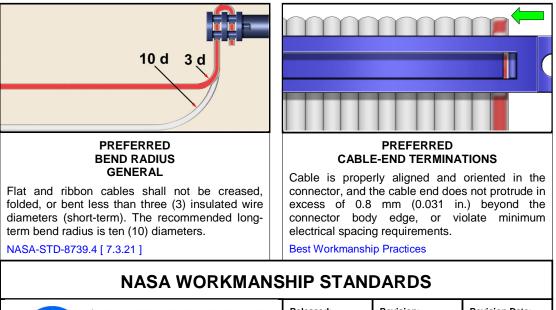
**GENERAL REQUIREMENTS** 

The cable assembly meets dimensional, layout, and design requirements. Conductors are properly aligned to respective termination pins and properly seated. The assembly exhibits a smooth, flat profile, with no visible damage to the connectors or the insulation.



**COAXIAL RIBBON CABLE** The bend radius for coaxial ribbon cables shall not be less than six (6) insulated wire diameters short-term, ten (10) diameters long-term.

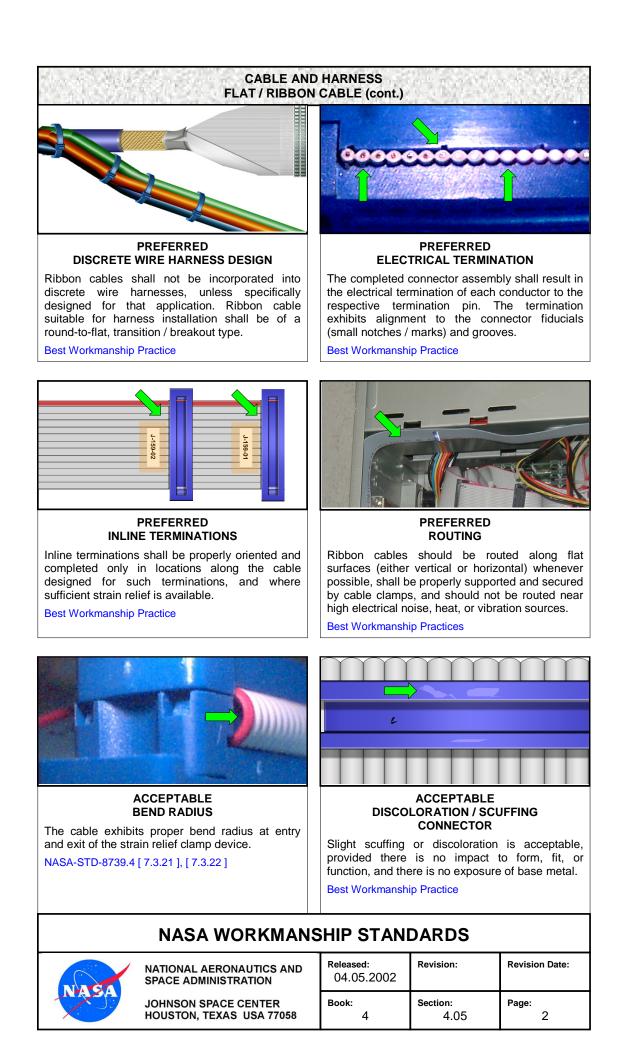
NASA-STD-8739.4 [ 7.3.21 ]

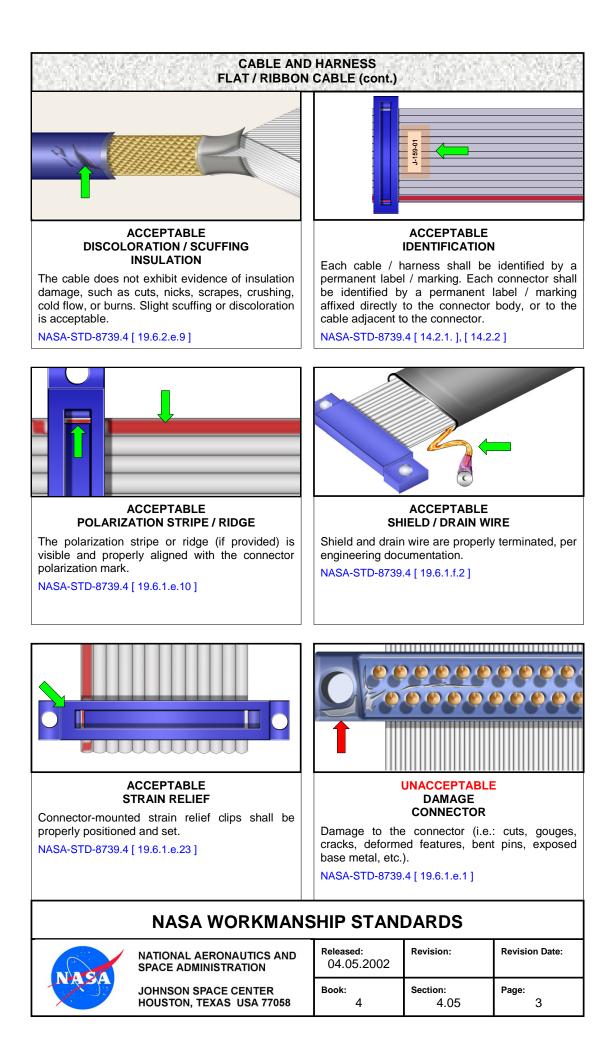


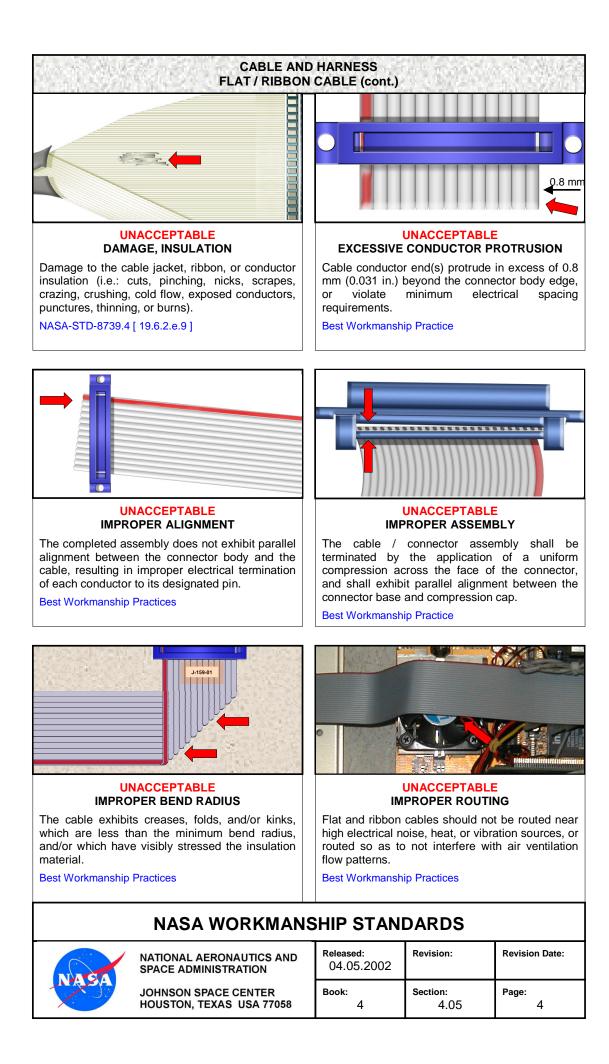


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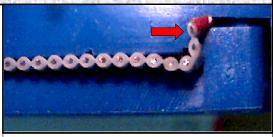
#### CABLE AND HARNESS FLAT / RIBBON CABLE (cont.)



#### UNACCEPTABLE IMPROPER STRAIN RELIEF

Wires exiting from connectors shall be stress relieved. Connector strain relief clamps shall be properly set.

NASA-STD-8739.4 [7.3.22]



#### UNACCEPTABLE IMPROPER TERMINATION

The completed connector assembly shall result in the electrical termination of each conductor to the respective termination pin. The termination shall exhibit alignment to the connector fiducials (small notches / marks) and grooves.

**Best Workmanship Practice** 



MISSING COMPONENTS

Missing connector parts ( i.e.: compression cap, strain relief clip, polarizing key, etc.) shall be cause for rejection.

NASA-STD-8739.4 [ 19.6.1.e.17 ]

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## CABLE AND HARNESS FLAT / RIBBON CABLE (cont.)

3.2.3

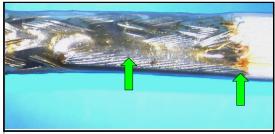
NASA WORKMANSHIP STANDARDS				
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# CABLE AND HARNESS SOLDER SLEEVES SOLDER SLEEVES SOLDER SLEEVES Solder sleeves are primarily used to attach a ground wire (lead) to the shielding braid of a shielded cable by means of a shrinkable tubing assembly having an integral solder preform. Solder sleeves are also used to splice two or more conductors together in a parallel configuration. See Section 4.01 "Cable and Harness, General Requirements", and Section 6.01 "Through-Hole Soldering, General Requirements", for common accept / reject criteria.



#### PREFERRED

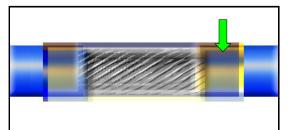
Solder sleeve has been properly installed and tightly shrunk. Strain relief is acceptable. Overlaps are of sufficient length to meet minimum electrical spacing. Solder fillet is visible, fully flowed, and smooth.



#### ACCEPTABLE CLOSE-UP VIEW

The termination exhibits proper solder flow and complete wetting. There is evidence of a complete fillet between the ground wire and the shield. Individual strands are discernable. Minor flux entrapment is acceptable.

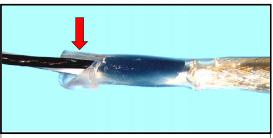
NASA-STD-8739.4 [ 19.6.1.b ], [ 19.6.1.g ]



#### ACCEPTABLE DISCOLORATION

The solder sleeve may exhibit slight discoloration resulting from the heating process. The sleeve shall not exhibit any damage.

NASA-STD-8739.4 [ 19.6.1.g.3 ]



#### UNACCEPTABLE DAMAGED SLEEVE

Solder sleeves shall be free of cracks, cuts, crushing, gouges, punctures, and charred, melted or burned areas. Slight scuffing or discoloration is acceptable.

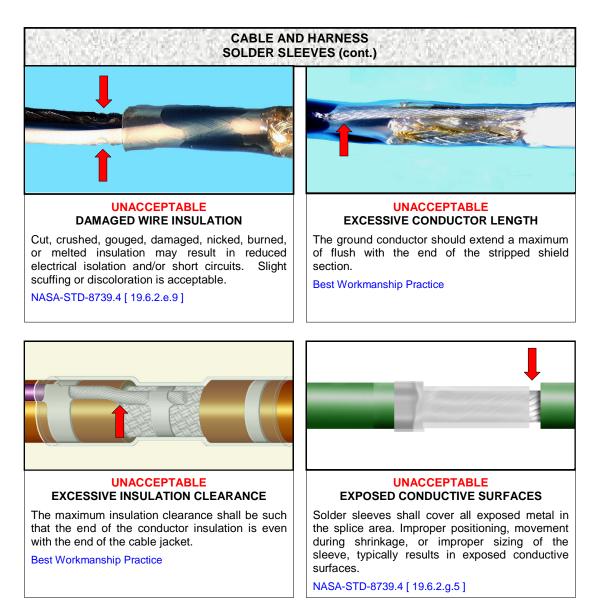
NASA-STD-8739.4 [ 19.6.1.g.3 ], [ 19.6.2.g.6 ]

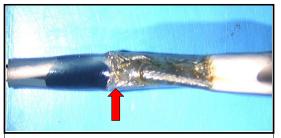
# NASA WORKMANSHIP STANDARDS



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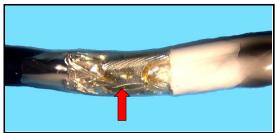




#### UNACCEPTABLE IMPROPER LOCATION

Solder sleeves should be installed, such that the solder preform is approximately centered on the stripped section of the conductors, ensuring proper sealing and strain relief.

Best Workmanship Practice



#### UNACCEPTABLE INCOMPLETE FILLET

The termination shall exhibit a complete, fully wetted fillet along both sides of the interface between the two conductors.

NASA-STD-8739.4 [ 19.6.2.g.2 ], [ 19.6.2.g.3 ]

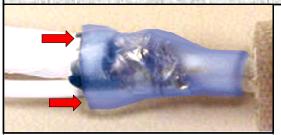
# NASA WORKMANSHIP STANDARDS



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#### CABLE AND HARNESS SOLDER SLEEVES (cont.)



## UNACCEPTABLE INCOMPLETE SHRINKAGE

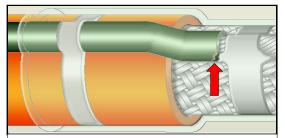
The solder sleeve shall be completely shrunk to provide a tight fit and proper stress relief. NASA-STD-8739.4 [ 9.10 ], [ 11.4 ], [ 19.6.1.b.6 ]



#### UNACCEPTABLE INCOMPLETE SOLDER FLOW

The solder preform has not melted and properly flowed. Typically this is caused by insufficient heat, insufficient dwell time during the shrinkage process, or use of an infra-red (IR) heat source.

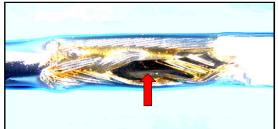
NASA-STD-8739.4 [ 19.6.2.g ]



UNACCEPTABLE INSUFFICIENT INSULATION CLEARANCE

The insulation shall not be imbedded in the solder joint. The contour of the conductor shall not be obscured at the termination end of the insulation.

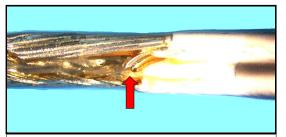
NASA-STD-8739.4 [ 10.1.7.a.1 ]



UNACCEPTABLE SEPARATED SHIELD STRANDS

Birdcaging or separation of the shield stranding may introduce unwanted electrical noise into the system, and may interfere with the proper installation of the solder sleeve.

NASA-STD-8739.4 [ 19.6.1.e.16 ]



#### UNACCEPTABLE SEVERED / PROTRUDING STRANDS

Conductors exhibiting severed strands shall not be used. Severed wire strands may protrude through the solder sleeve, creating a shorting and reliability risk.

NASA-STD-8739.3 [ 7.2.3 ] NASA-STD-8739.4 [ 19.6.2.a.2 ], [ 19.6.2.g.7 ]



#### UNACCEPTABLE TERMINATION NOT VISIBLE

The solder sleeve is opaque, prohibiting visual inspection of the termination. The solder sleeve shall be transparent or translucent to allow inspection.

NASA-STD-8739.4 [ 19.6.2.g.1 ]

# NASA WORKMANSHIP STANDARDS

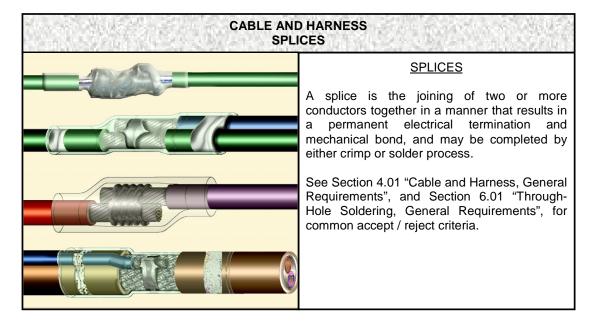


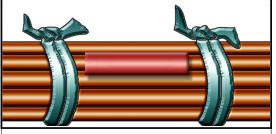
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## CABLE AND HARNESS SOLDER SLEEVES (cont.)

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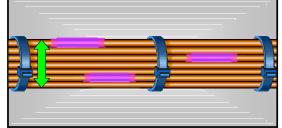




PREFERRED GENERAL REQUIREMENTS

The splice exhibits a smooth profile, proper strain relief, and is located in an area of the harness not subjected to flexure.

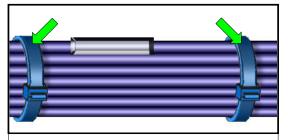
Best Workmanship Practice



PREFERRED SPLICE ASSEMBLY PROFILE

The location of splices shall be staggered to minimize the increase in profile to the harness. Final assembly profile shall not impact form, fit, or function.

Best Workmanship Practice



#### PREFERRED SPLICE RESTRAINT

Cable ties / lacing shall be installed at both ends of a splice or solder sleeve, but placement shall not violate stress relief requirements.

Best Workmanship Practice



#### ACCEPTABLE CRIMP SPLICE – BUTT / INLINE

The contact has been deformed only by tool indenters. Indents are symmetrical and centered on the crimp barrel. No exposed base metal or other damage. Wire strand ends are visible. Proper insulation spacing (C).

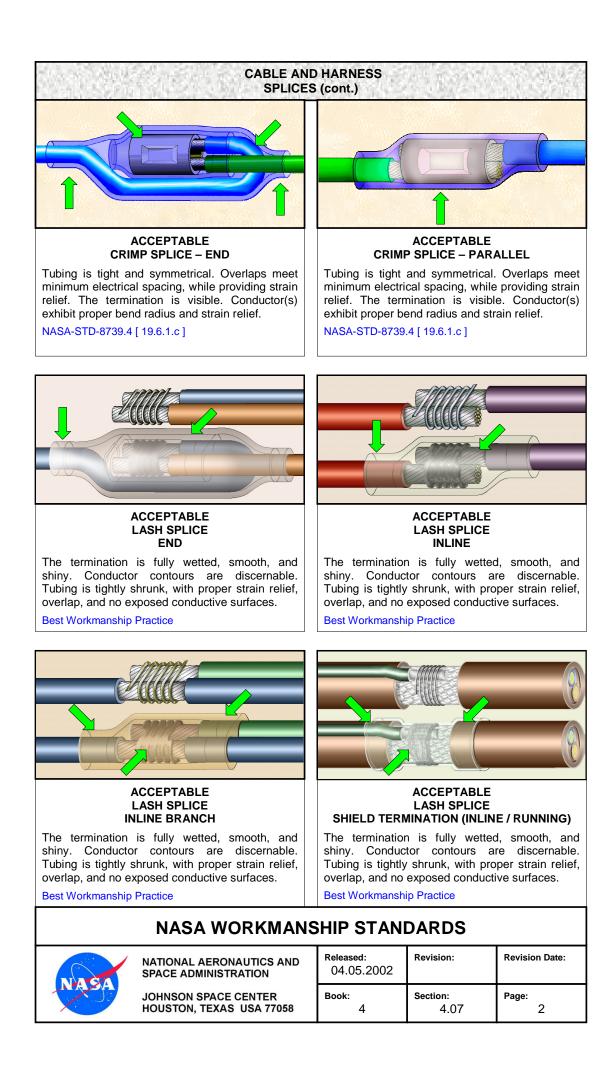
NASA-STD-8739.4 [ 19.6.1.c ]

# NASA WORKMANSHIP STANDARDS



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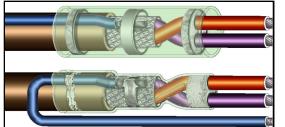
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The termination is fully wetted, smooth, and shiny. Conductor contours are discernable. Tubing is tightly shrunk, with proper strain relief, overlap, and no exposed conductive surfaces.

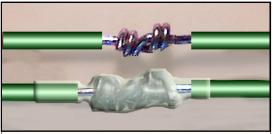
Best Workmanship Practice



#### ACCEPTABLE SOLDER SLEEVE SPLICE SHIELD TERMINATION (TRADITIONAL)

The termination is fully wetted, smooth, and shiny. Conductor contours are discernable. Tubing is tightly shrunk, with proper strain relief, overlap, and no exposed conductive surfaces.

NASA-STD-8739.4 [ 11.4 ], [ 19.6.1 ]



shiny. Conductor contours are discernable.

Tubing is tightly shrunk, with proper strain relief,

overlap, and no exposed conductive surfaces.

NASA-STD-8739.4 [ 11.4 ], [ 19.6.1 ]

#### ACCEPTABLE WESTERN UNION / LINEMAN SPLICE

The termination is fully wetted, smooth, and shiny. Tubing is tightly shrunk, with proper strain relief, overlap, and no exposed conductive surfaces. Western Union splices are used for solid conductors

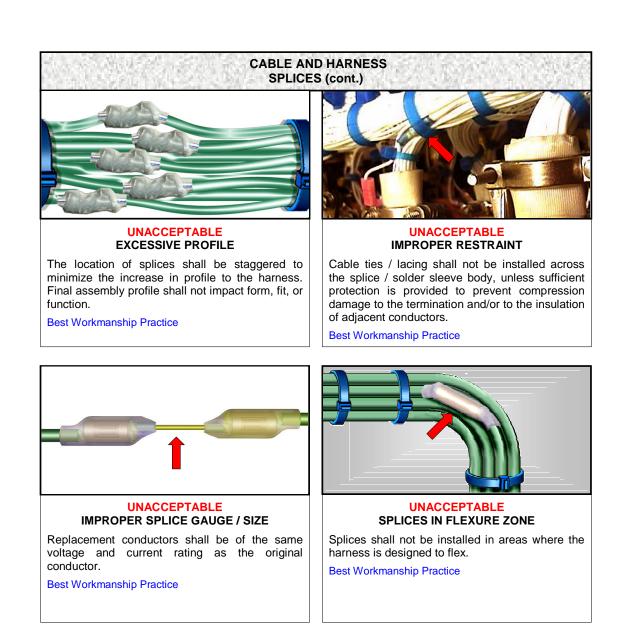
NASA-STD-8739.3 [ 13.6 ]

# NASA WORKMANSHIP STANDARDS

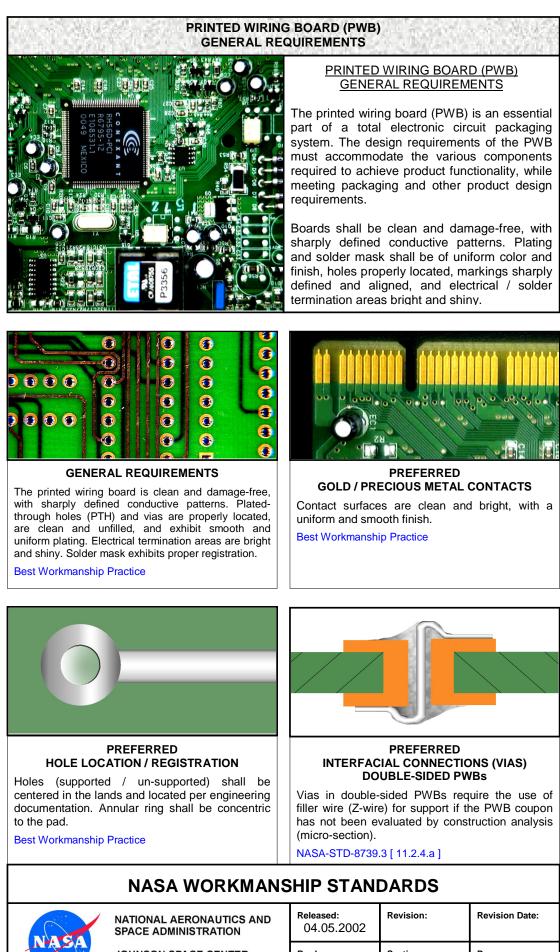


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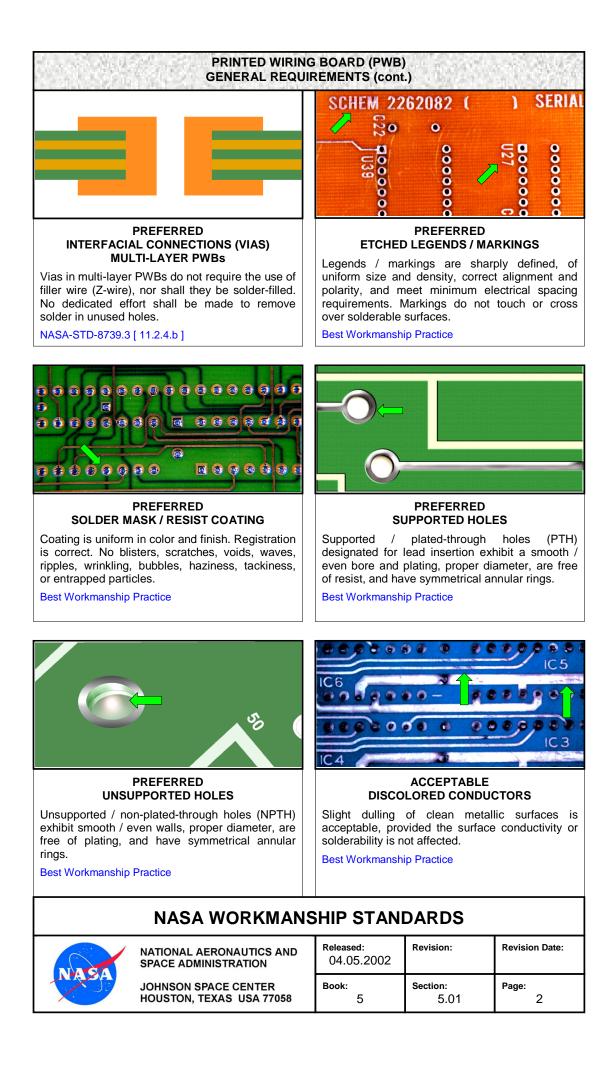
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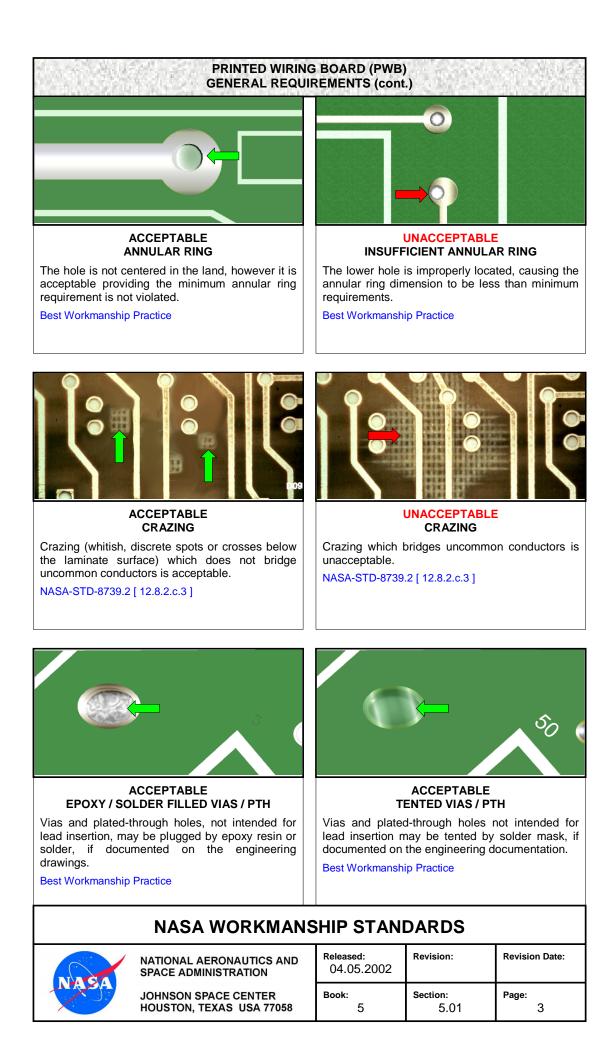


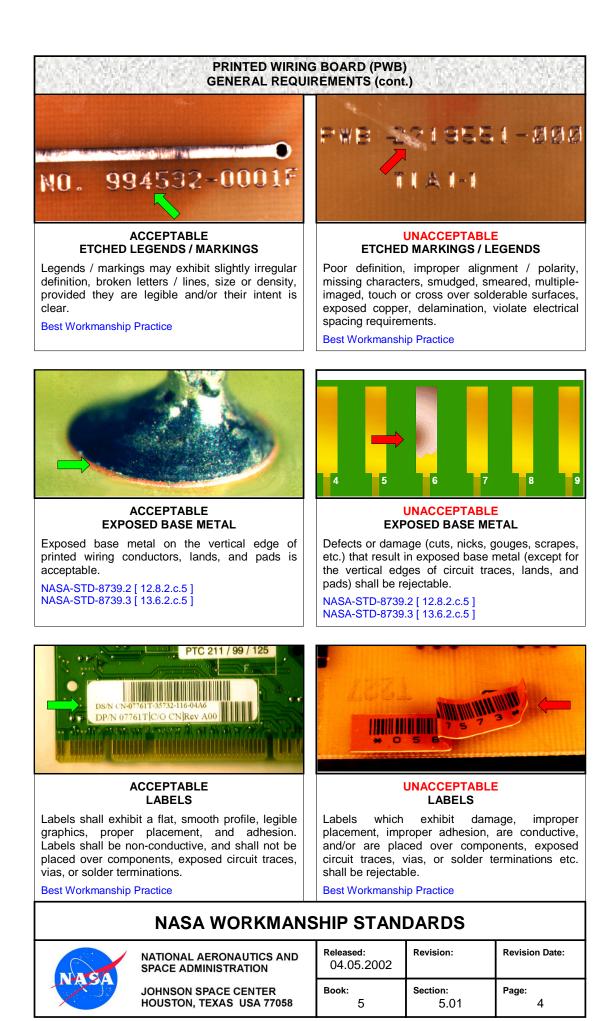
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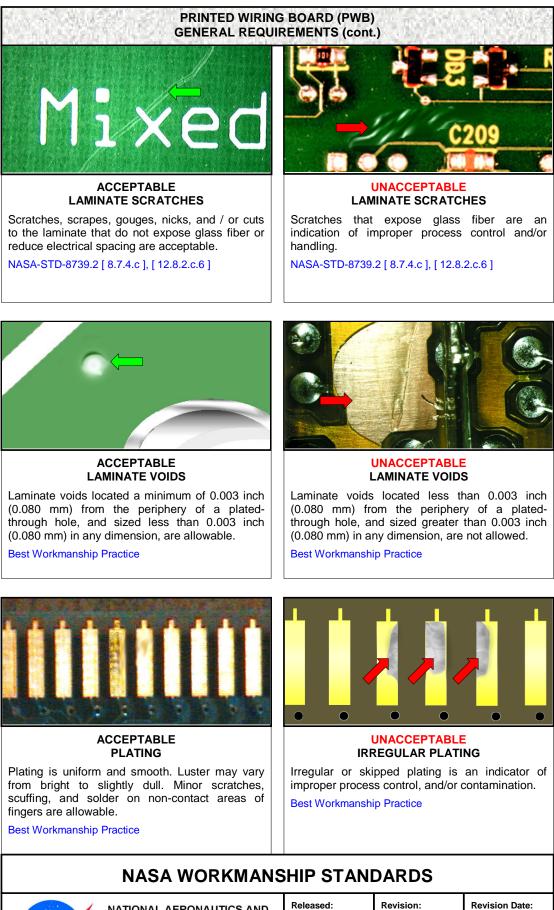


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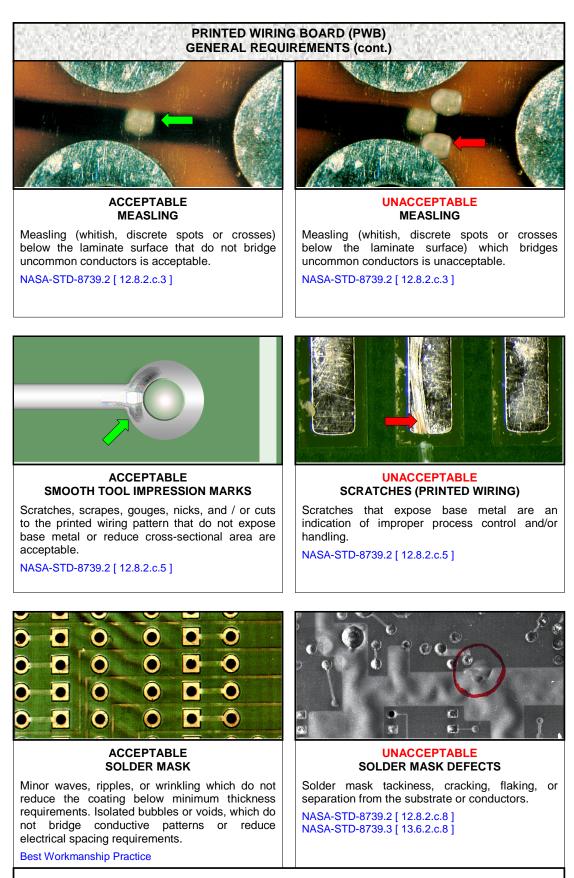






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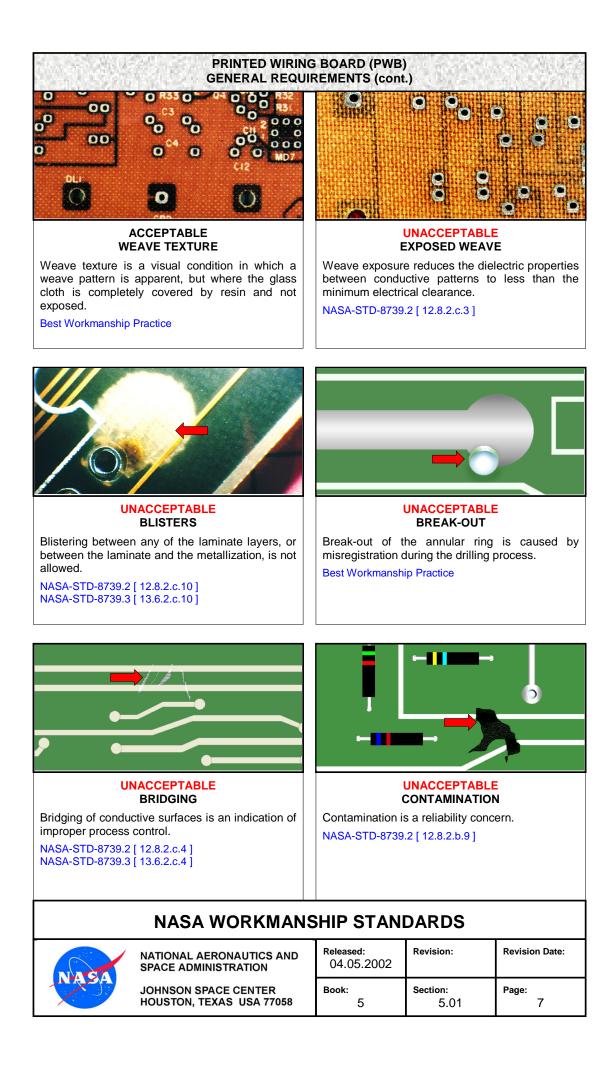


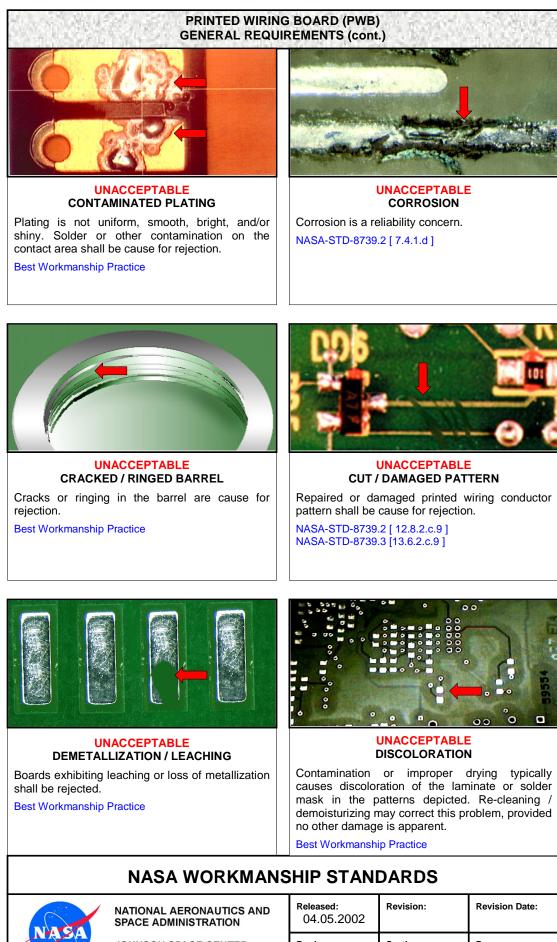
# NASA WORKMANSHIP STANDARDS



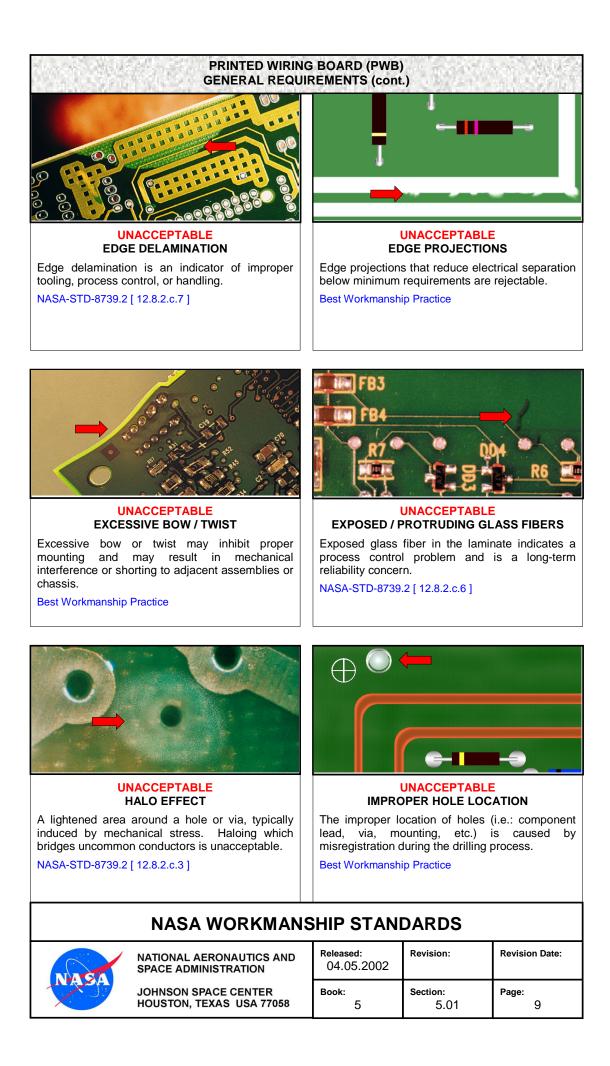
NATIONAL AERONAUTICS AND SPACE ADMINISTRATION

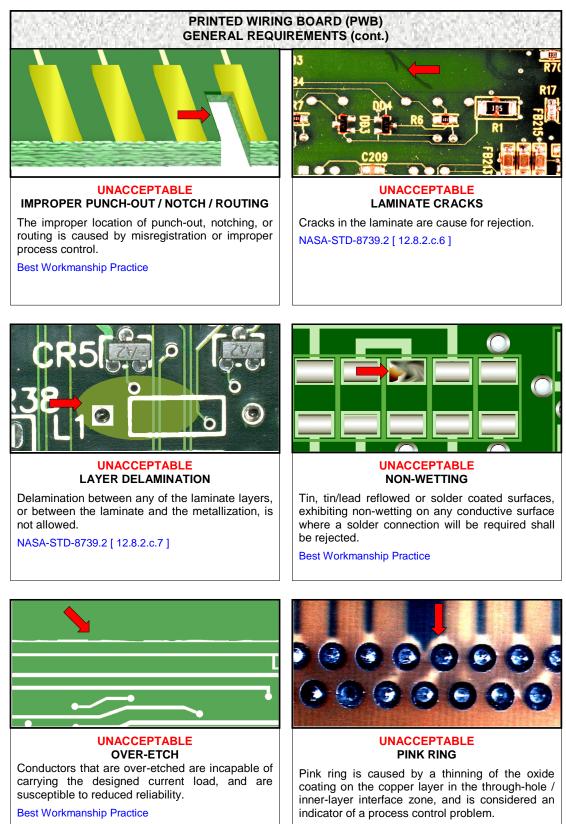
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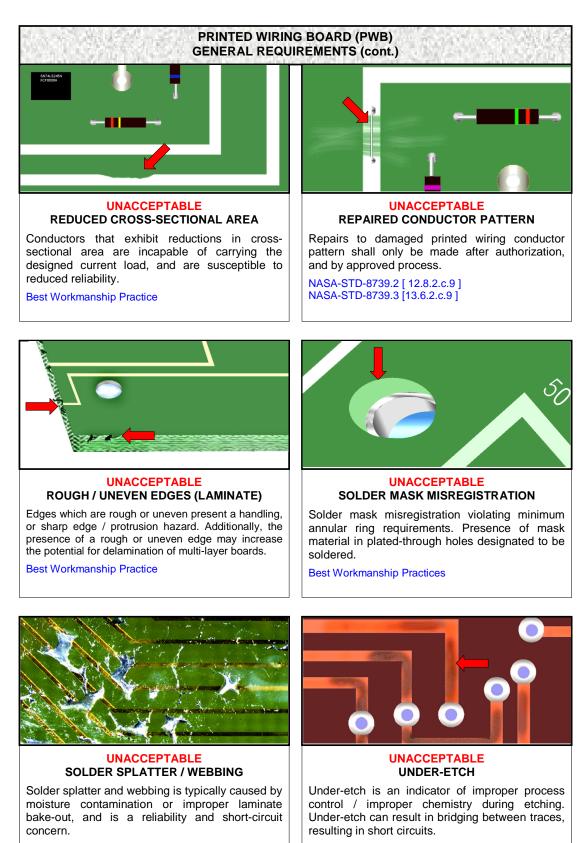
**Best Workmanship Practice** 

# NASA WORKMANSHIP STANDARDS



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NASA-STD-8739.2 [ 12.8.2.c.4 ]



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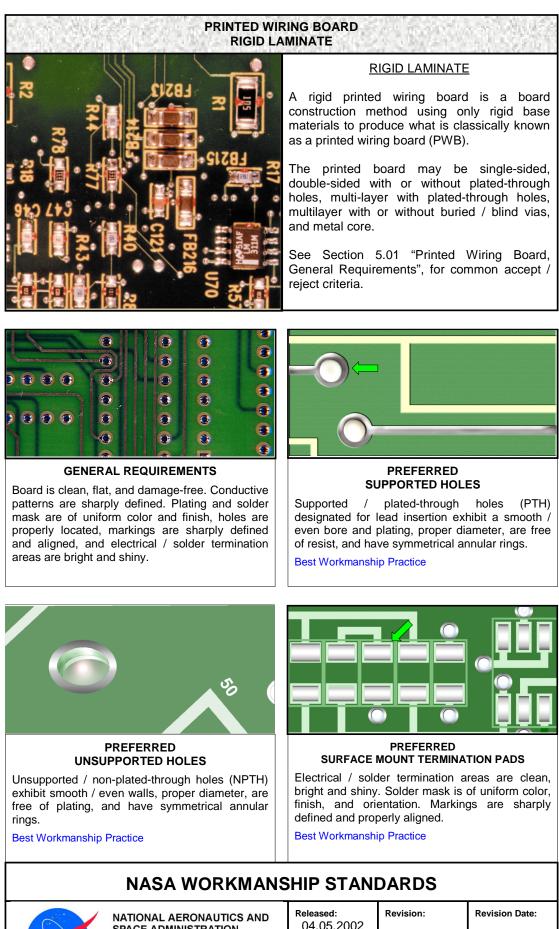


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## PRINTED WIRING BOARD (PWB) GENERAL REQUIREMENTS (cont.)

NASA WORKMANSHIP STANDARDS				
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#### PRINTED WIRING BOARD RIGID LAMINATE (cont.)

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NASA WORKMANSHIP STANDA				
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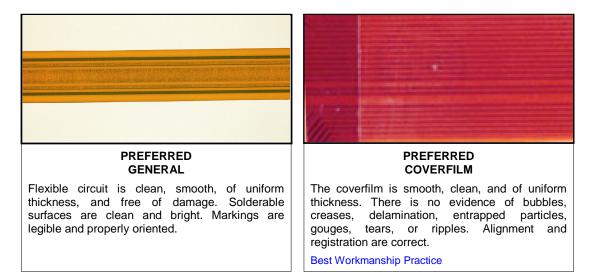
#### PRINTED WIRING BOARD FLEXIBLE LAMINATE

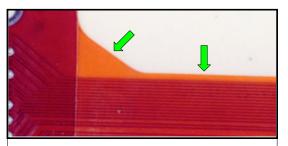


#### FLEXIBLE LAMINATE

The thin films used in flexible circuitry offer significant weight and space savings over traditional rigid designs, and allow the development of printed boards that can be bent and folded in three-dimensional (3-D) configurations. Flexible boards may be single, double, or multi-layer; may contain throughhole, surface mount, or mixed technology; and, can be constructed wholly of flex or a combination of both flex and rigid (see rigid-flex, section 5.04).

See Section 5.01 "Printed Wiring Board, General Requirements", for common accept / reject criteria.

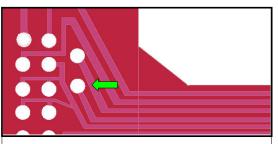




PREFERRED TRIMMED EDGE

The trimmed edge shall be free of burrs, nicks, delamination, or tears. Minimum edge to conductor spacing shall be maintained.

Best Workmanship Practice



#### ACCEPTABLE COVERFILM REGISTRATION

The coverfilm is aligned and registered within engineering specification. All annular ring cutouts are centered, and there is no evidence of unwanted material on land areas.

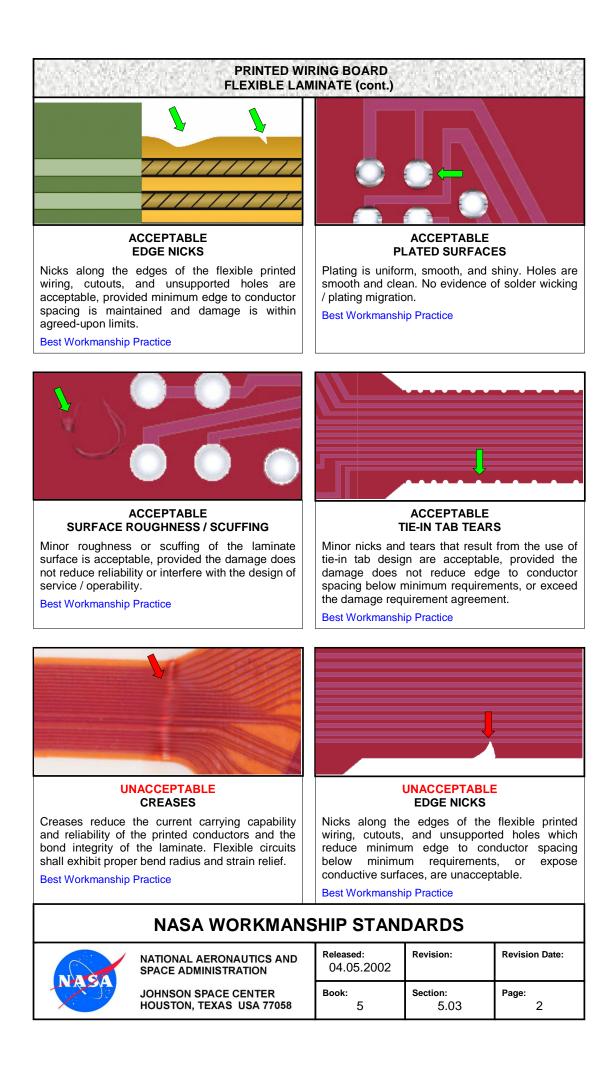
Best Workmanship Practice

# NASA WORKMANSHIP STANDARDS

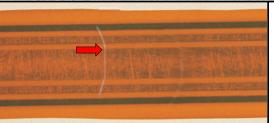


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## PRINTED WIRING BOARD FLEXIBLE LAMINATE (cont.)



### UNACCEPTABLE FOREIGN MATERIAL IN COVERCOAT

Foreign material under the covercoat represents a contamination and reliability concern. Best Workmanship Practice



#### UNACCEPTABLE PHYSICAL DAMAGE

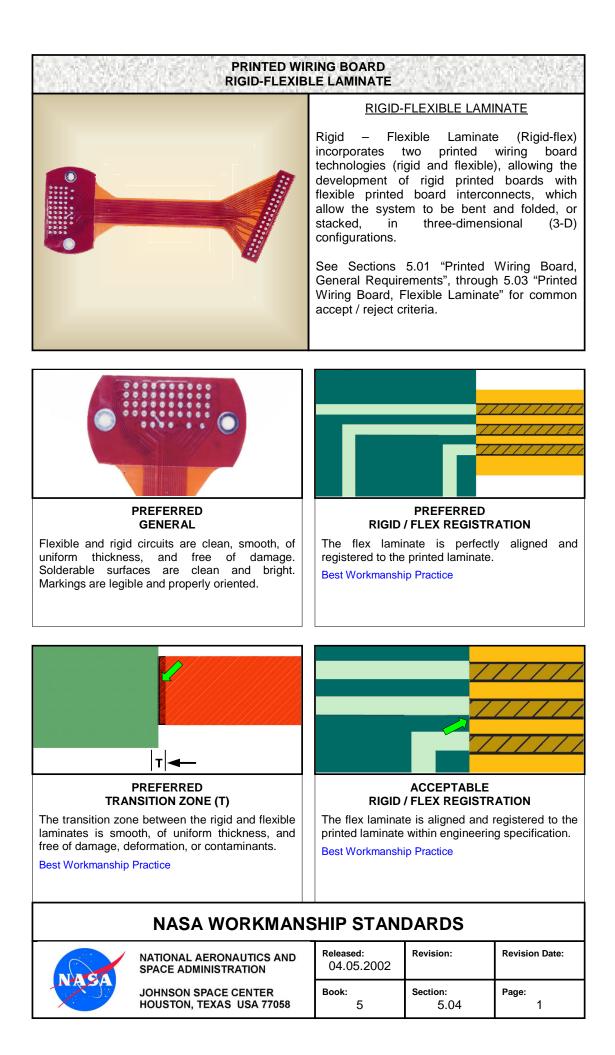
Cuts, nicks, gouges, tears, or other physical damage that result in exposed circuitry or reduce electrical separation below minimum requirements are unacceptable.

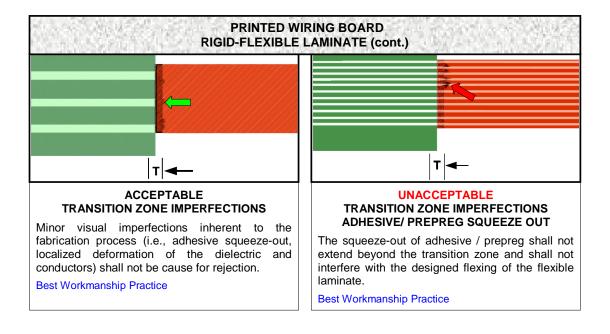
Best Workmanship Practice

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## PRINTED WIRING BOARD FLEXIBLE LAMINATE (cont.)

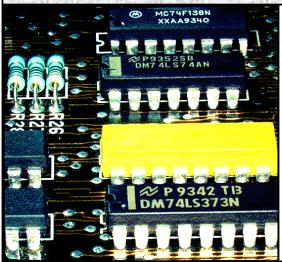
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### THROUGH-HOLE SOLDERING GENERAL REQUIREMENTS



### THROUGH-HOLE SOLDERING GENERAL REQUIREMENTS

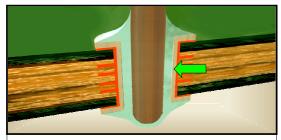
Discrete components are the backbone of the electronics world, consisting of individually packaged, leaded devices, highly integrated circuits (IC), interconnects, terminators, switches, etc.

While discretes are rapidly being displaced by the smaller-form surface mount technology (SMT) package, the discrete component is still widely in use, especially in extreme environmental applications where the SMT device will not perform reliably and/or is unavailable.



**GENERAL REQUIREMENTS** 

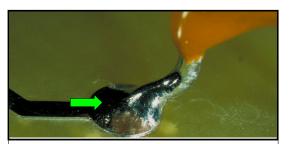
Components are installed per engineering documentation, and are parallel to, and in contact with, the board surface. Component and board markings are clear and legible. Component leads exhibit proper bend radii, and stress relief. Solder fillets are smooth and shiny, with concave profiles.



PREFERRED 100% SOLDER FILL (PTH)

Populated plated through holes (PTH) should exhibit a vertical solder fill of 100%, with a fully formed fillet on the solder side, and evidence of 100% wetting on the component side lead, barrel, and pad.

**Best Workmanship Practice** 



### PREFERRED PWB COMPONENT SIDE FILLET (PTH)

The solder joint surface is smooth, nonporous and undisturbed, with a finish varying from satin to bright. The fillet completely wets all elements to the periphery of the connection and is concave.

NASA-STD-8739.3 [ 13.6.1.f.2 ]



PREFERRED SOLDER SIDE FILLETS (PTH / NPTH)

The solder joint surfaces are smooth, nonporous and undisturbed, with a finish varying from satin to bright. The fillet completely wets all elements of the connection and is concave.

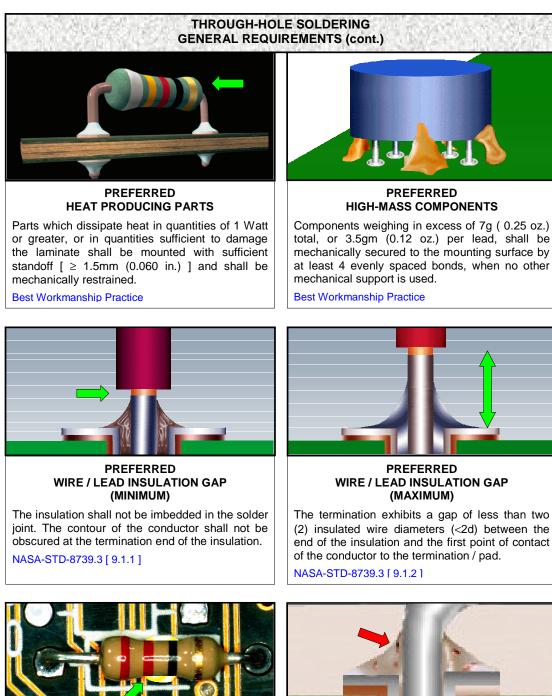
NASA-STD-8739.3 [ 13.6.1.f.1 ]

# NASA WORKMANSHIP STANDARDS



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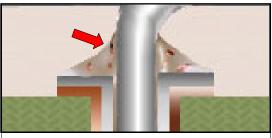
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ACCEPTABLE ADHESIVES

Adhesives may be used to temporarily hold discrete components in position during wave or reflow soldering. Adhesives shall not interfere with soldering, and residues shall be removed following soldering operations.

**Best Workmanship Practice** 



**UNACCEPTABLE** ADHESIVE INCLUSION

Adhesive material in the solder joint shall be cause for rejection.

NASA-STD-8739.3 [ 13.6.2.b.10 ]

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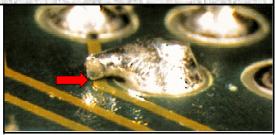
### THROUGH-HOLE SOLDERING GENERAL REQUIREMENTS (cont.)



### ACCEPTABLE CLINCHED LEAD TERMINATION

Conductor / lead ends may be clinched, with the clinched length at least ½ the largest solder pad dimension, bent in the direction of the longest pad dimension. Clinched leads shall not violate minimum electrical spacing requirements.

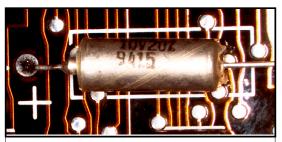
NASA-STD-8739.3 [ 8.5.2 ]



### UNACCEPTABLE IMPROPERLY CLINCHED LEAD

Component leads shall not be clinched toward an electrically uncommon conductor.

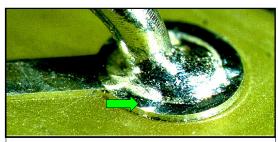
NASA-STD-8739.3 [ 13.6.2.a.20 ]



ACCEPTABLE CONDUCTIVE CASE PARTS

Parts having conductive cases, which are mounted over printed conductors or which are in close proximity to other conductive materials shall be separated by insulation of suitable thickness, or shall have an insulating jacket / sleeve.

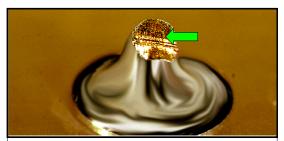
NASA-STD-8739.3 [ 8.1.2.b ]



ACCEPTABLE DEWETTING

Slight solder dewetting around the periphery of the component side termination pad shall not be cause for rejection, provided the termination exhibits flow-through and bonding of the lead / conductor to the termination pad.

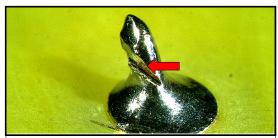
NASA-STD-8739.3 [ 11.2.3.c ]



ACCEPTABLE EXPOSED BASE METAL

Exposed ends of leads on straight-through terminations shall not be cause for rejection if the PWA is to be conformally coated.

NASA-STD-8739.3 [ 13.6.1.k ]



### UNACCEPTABLE EXPOSED BASE METAL

Defects or damage (cuts, nicks, gouges, scrapes, etc.) that result in exposed base metal (except for the vertical edges of circuit traces, lands, and pads) shall be rejectable.

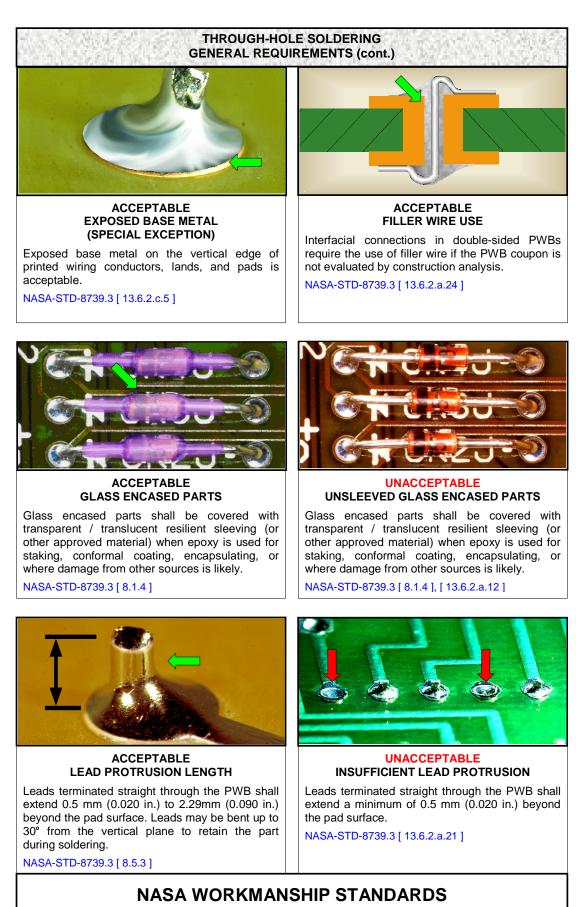
NASA-STD-8739.3 [ 13.6.2.a.8 ], [ 13.6.2.c.5 ]

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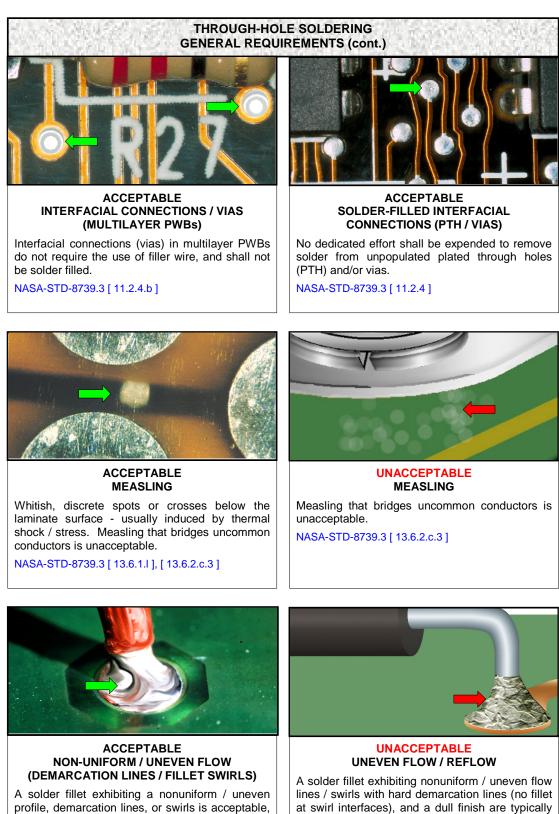
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provided the fillet is shiny and there is evidence of complete wetting with smooth fillets at the swirls.

**Best Workmanship Practice** 

caused by an inadequate / uneven application of heat during the fillet formation.

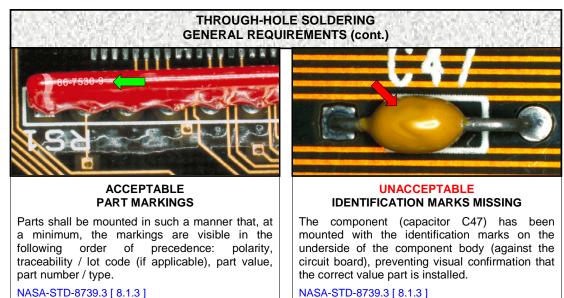
**Best Workmanship Practice** 

# NASA WORKMANSHIP STANDARDS

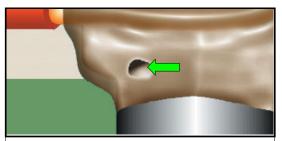


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NASA-STD-8739.3 [ 8.1.3 ]



ACCEPTABLE PITS

A solder pit is acceptable, provided the bottom of the cavity can be seen from all angles of vision. **Best Workmanship Practice** 



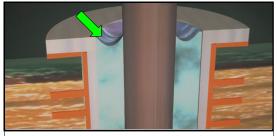
ACCEPTABLE SHRINK TUBING (TRANSLUCENT / TRANSPARENT)

Shrink tubing installed over components and/or soldered terminations shall be transparent (or translucent) to allow visual inspection. NASA-STD-8739.3 [ 8.1.4 ]

### ACCEPTABLE SMOOTH TOOL IMPRESSION MARKS

Smooth tool impression marks (slight cuts, nicks, scratches or scrapes) on the conductor surface, which do not expose base metal or reduce crosssectional area are acceptable.

NASA-STD-8739.3 [ 8.1.6.d ]



ACCEPTABLE SOLDER FILLET RECESS / SHRINKBACK

A slight recessing or shrinkback of the solder into the PTH below the solder pad is acceptable, providing the lead and pad exhibit wetting and the shrinkback is slight.

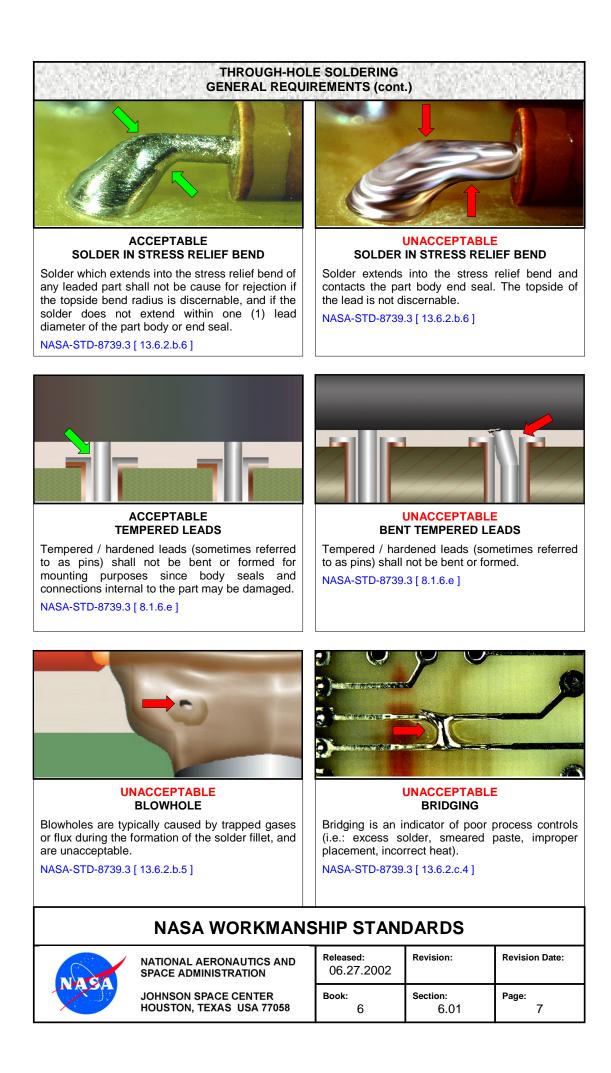
NASA-STD-8739.3 [ 13.6.1.f.2 ]

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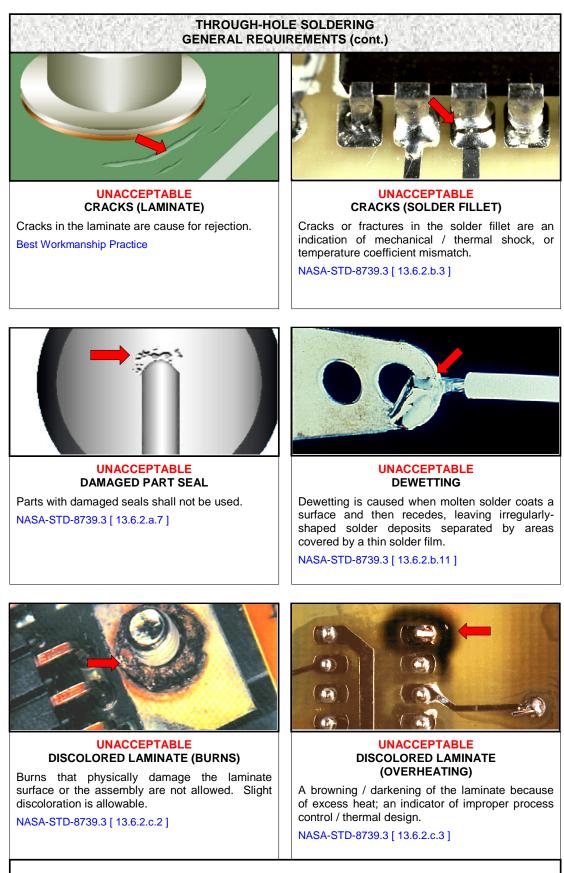


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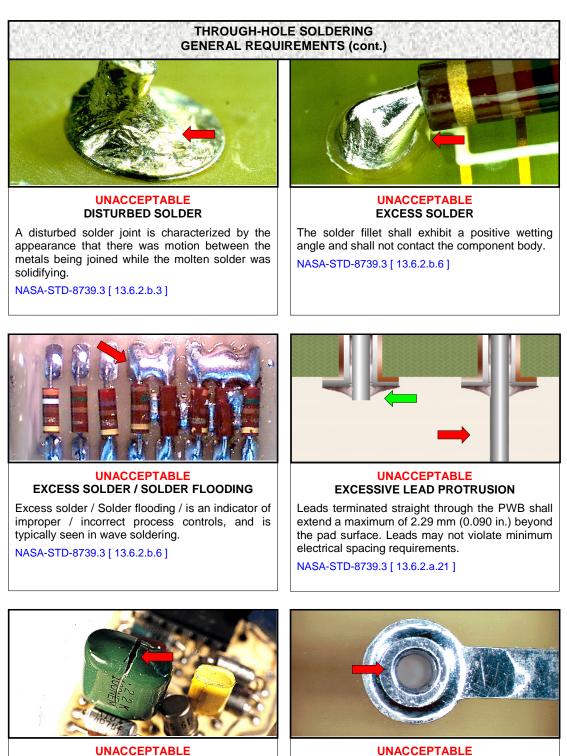


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### UNACCEPTABLE EXPOSED DIE / CIRCUIT ELEMENTS

The unprotected exposure of die or circuit elements is not allowed unless specified in the engineering documentation.

NASA-STD-8739.3 [ 13.6.2.a.7 ]

**EYELETS** Eyelets shall not be used for interfacial terminations.

**Best Workmanship Practice** 

# NASA WORKMANSHIP STANDARDS



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### UNACCEPTABLE FLUX RESIDUE

Flux residue indicates improper / incomplete cleaning.

NASA-STD-8739.3 [ 13.6.2.b.10 ]



UNACCEPTABLE FLUX SPLATTER

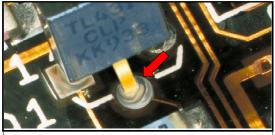
Flux splatter is an indication of an improper process parameter (heat / moisture). NASA-STD-8739.3 [ 13.6.2.b.8 ]



UNACCEPTABLE FRACTURED SOLDER

A fractured solder joint is an indication that the joint has been subjected to extreme mechanical shock. A crack in an "as-received" assembly is unusual and cause for concern.

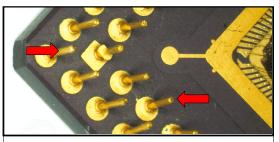
NASA-STD-8739.3 [ 13.6.2.b.3 ]



UNACCEPTABLE GOLD INTERMETALLIC

Gold intermetallic is characterized by evidence of golden colored streaks in the solder fillets of gold plated leads that have not been properly tinned. Gold intermetallic can severely embrittle a solder joint.

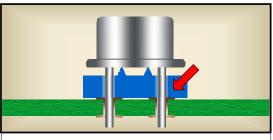
**Best Workmanship Practice** 



### UNACCEPTABLE GOLD PLATING

Gold plated surfaces that will become a part of the finished solder connection shall be tinned prior to soldering to remove the gold plating.

NASA-STD-8739.3 [ 7.2.5.c ], [ 13.6.2.a.3 ]



### UNACCEPTABLE HOLE OBSTRUCTION

The mounting pad has been installed upside down. Parts shall not be mounted such that they obstruct solder flow to the component-side termination area (pad), or prevent cleaning and inspection.

NASA-STD-8739.3 [ 8.4.4 ]

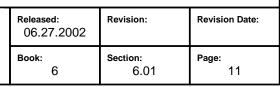
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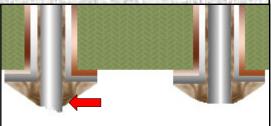
### THROUGH-HOLE SOLDERING GENERAL REQUIREMENTS (cont.)



### UNACCEPTABLE IMPROPER LEAD BENDING

The minimum distance from the part body / seal to the start of the bend shall be 2 lead diameters for round leads and 0.5 mm (0.020 in.) for ribbon leads. The bend radius shall not be less than one lead diameter (1d) or ribbon thickness (1 t).

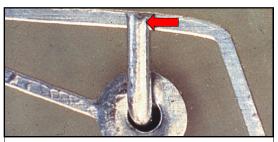
NASA-STD-8739.3 [ 8.1.6.a ]



#### UNACCEPTABLE IMPROPER LEAD CUTTING

Leads shall be cut per engineering documentation and by methods, which do not impart stress to the lead seal or internal terminations.

NASA-STD-8739.3 [ 8.1.6.a ]



UNACCEPTABLE IMPROPER LEAD LENGTH

The clinched lead extends beyond the pad edge in excess of allowed limits and is bent over an uncommon electrical conductor.

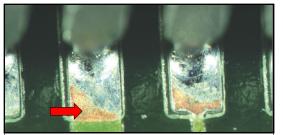
NASA-STD-8739.3 [ 13.6.2.a.20 ]



UNACCEPTABLE IMPROPER ORIENTATION

Parts shall be mounted parallel to the laminate surface, right side up, and aligned to the lands within design and engineering specifications.

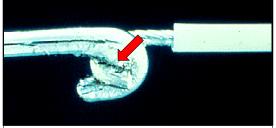
NASA-STD-8739.3 [ 13.6.2.a.5 ]



UNACCEPTABLE IMPROPER TINNING

Tinned surfaces, which are to become part of the solder termination, shall exhibit 100% coverage.

NASA-STD-8739.3 [ 7.2.6 ], [ 13.6.2.a.3 ]



### UNACCEPTABLE INSUFFICIENT SOLDER

Insufficient solder is an indicator of improper process control, and may result in reduced reliability.

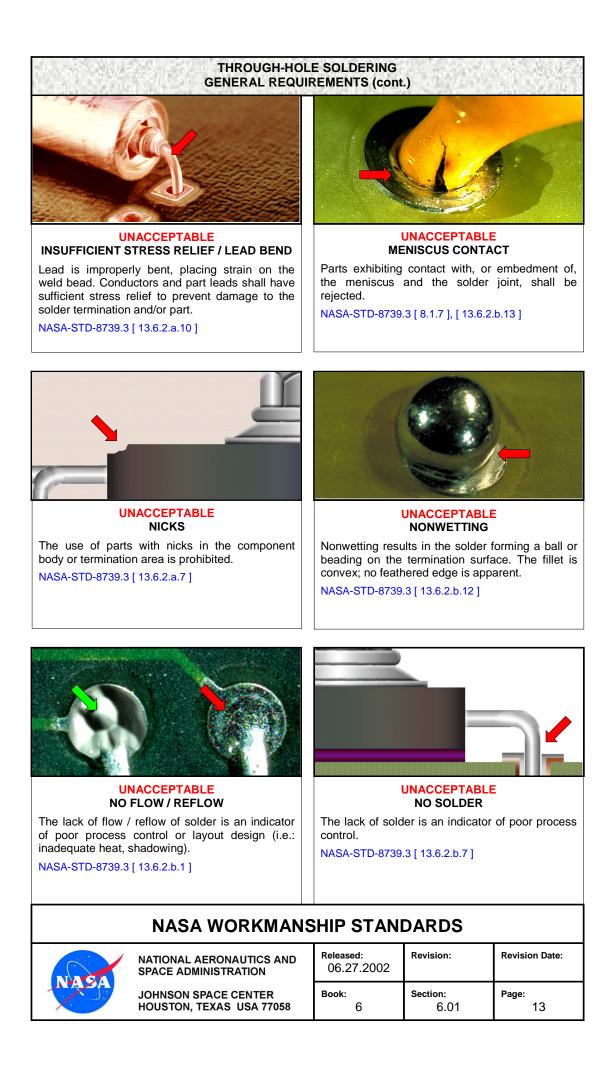
NASA-STD-8739.3 [ 13.6.2.b.7 ]

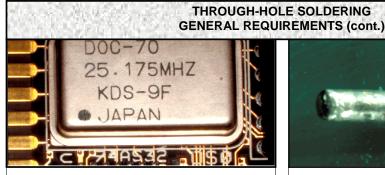
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### UNACCEPTABLE OBSCURED SOLDER TERMINATIONS

The placement of a part, which obscures the inspectability of another part's terminations, is unacceptable, unless interim inspection is performed (part depicted is mounted over previously installed surface mount components). NASA-STD-8739.3 [ 13.6.2.a.23 ]



### UNACCEPTABLE OPENS / VOIDS

Cavities (opens / voids) reduce the circumferential wetting of lead and barrel, land coverage, and vertical solder fill below minimum acceptable requirements.

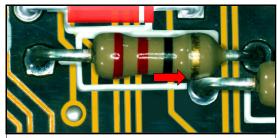
NASA-STD-8739.3 [ 13.6.2.b.5 ]



UNACCEPTABLE OVERHEATED SOLDER

Overheated solder has a dull, gray, frosty, and/or crystallized appearance and is the result of excessive exposure to heat.

NASA-STD-8739.3 [ 13.6.2.b.2 ]



UNACCEPTABLE PART BODY CONTACT

Part bodies shall not be in contact with soldered terminations. The spacing between components is below recommended values, resulting in contact between the resistor body and the lead, which may eventually result in a short circuit.

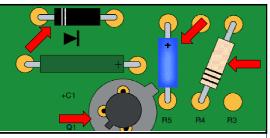
NASA-STD-8739.3 [ 8.1.7 ], [ 13.6.2.b.13 ]



### UNACCEPTABLE PART LEADS USED AS TERMINALS

Part leads shall not be used as terminals, unless the part lead is designed to function as a terminal.

NASA-STD-8739.3 [ 13.6.2.a.18 ]



### UNACCEPTABLE PART MISALIGNMENT

Part misalignment is an indicator of improper process control.

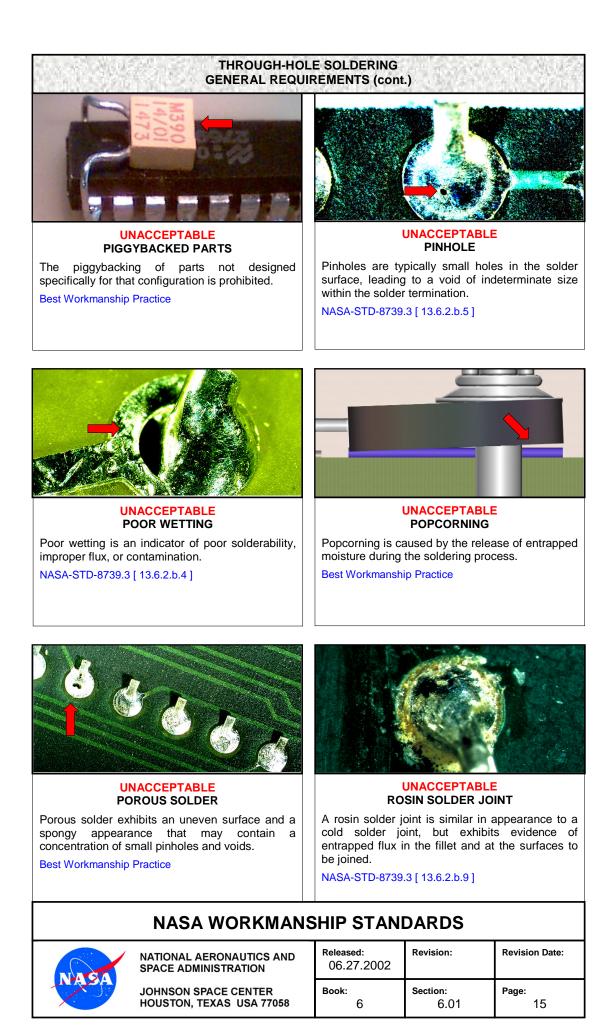
NASA-STD-8739.3 [ 13.6.2.a.5 ]

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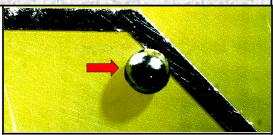
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### THROUGH-HOLE SOLDERING GENERAL REQUIREMENTS (cont.)



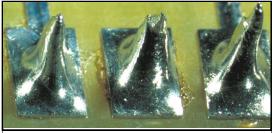
UNACCEPTABLE SCRATCHES (SOLDER FILLET) Scratches in the solder are prohibited. NASA-STD-8739.3 [ 13.6.2.b.3 ]



### UNACCEPTABLE SOLDER BALLS

Solder balls are considered a contaminant, and are an indication of improper process control (inadequate preheat), and/or the use of outdated solder/flux.

NASA-STD-8739.3 [ 13.6.2.b.10 ]



UNACCEPTABLE SOLDER PEAKS, ICICLES, SHARP EDGES

Solder peaks, icicles, and/or sharp edges are an indicator of an improper process parameter and are a reliability and short-circuit concern.

NASA-STD-8739.3 [ 13.6.2.c.4 ]



UNACCEPTABLE SOLDER SKIPS

Solder skip is the random non-formation of solder fillets, and is an indicator of poor process control. Solder skip may be caused by insufficient solder, contamination, non-solderability (oxide), improper flux, solder thieving, etc.

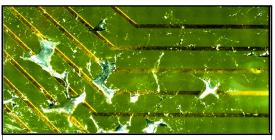
NASA-STD-8739.3 [ 13.6.2.b.7 ]



UNACCEPTABLE SOLDER SLIVERS

Solder slivers are an indication of improper process control.

NASA-STD-8739.3 [ 13.6.2.c.4 ]



UNACCEPTABLE SOLDER SPLATTER

Solder splatter is typically caused by moisture contamination and is an indicator of poor process control.

NASA-STD-8739.3 [ 13.6.2.b.8 ]

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# THROUGH-HOLE SOLDERING **GENERAL REQUIREMENTS (cont.)** UNACCEPTABLE UNACCEPTABLE SOLDER WEBBING SPLICED CONDUCTORS / LEADS Webbing is an indication of improper process Splices shall not be used to repair broken or control. damaged conductors or part leads. NASA-STD-8739.3 [ 13.6.2.c.4 ] NASA-STD-8739.3 [ 8.1.8 ], [ 13.6.2.a.16 ] **UNACCEPTABLE** UNACCEPTABLE WHISKER VOIDS

Voids are an indication of improper process control, and are typically caused by insufficient solder, solder wicking / thieving, or contamination. NASA-STD-8739.3 [ 13.6.2.b.5 ]

Best Workmanship Practice

A whisker is a slender needle-shaped metallic

growth between a conductor and a land.

Typically the result of mechanical stresses in high

tin compounds, it is a reliability concern.

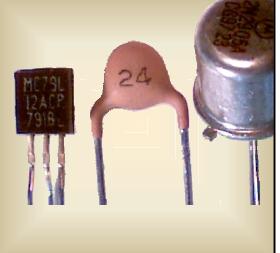
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### THROUGH-HOLE SOLDERING GENERAL REQUIREMENTS (cont.)

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### THROUGH-HOLE SOLDERING PREPARATION OF CONDUCTORS

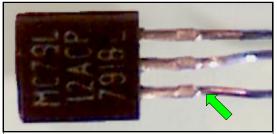


### PREPARATION OF CONDUCTORS

The quality of solder terminations can be correlated to the preparation of the conductors prior to soldering.

Solderability can be significantly improved by the pre-tinning and thorough cleaning of all surfaces designated to be part of the completed solder termination. Pre-forming of component leads and other conductors reduces stresses in the solder joint and component body.

See Section 6.01 "Through-Hole Soldering, General Requirements", for common accept / reject criteria.



PREFERRED COMPONENT LEADS

The component's leads have been tinned, formed, and cleaned per engineering requirements. Gold plating has been removed. The spacing and radius of bends are within requirements. There is no mechanical damage to the component leads or body.



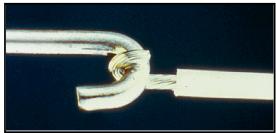
PREFERRED CONDUCTORS / WIRE

The conductors have been stripped, tinned, formed, and cleaned per engineering requirements. There is no mechanical damage to the conductor or insulation, no reduced crosssection, and individual strands are discernable.



### PREFERRED TERMINATION AREAS / PWB

Termination areas have been tinned with hotcoated tin-lead solder or hot reflowed electrodeposited tin-lead solder prior to mounting of the parts. Gold plating has been removed.



### PREFERRED TERMINATIONS / MISCELLANEOUS

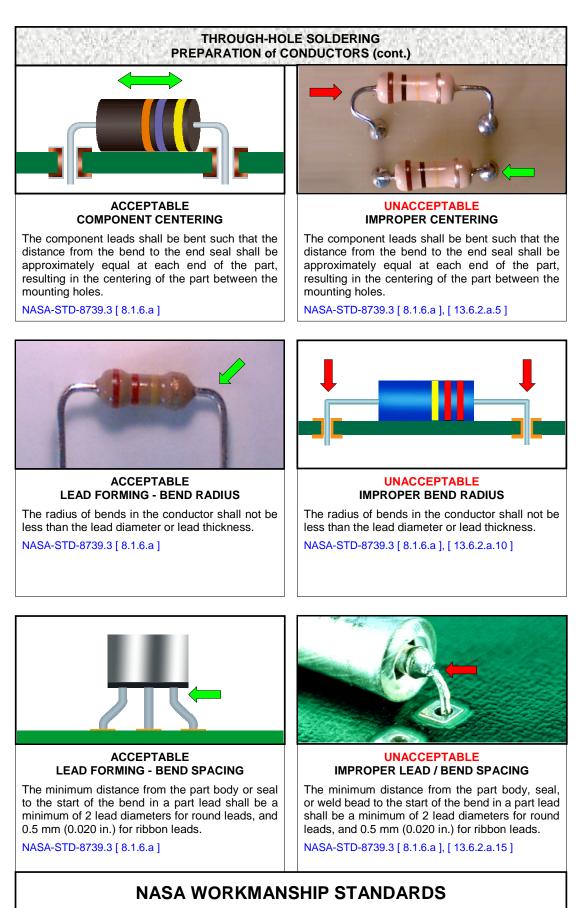
The terminations have been properly tinned, formed, and cleaned in preparation for solder termination. The preparation of simple terminations, such as the hook and conductor termination shown, is just as important as more complex terminations.

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# THROUGH-HOLE SOLDERING **PREPARATION of CONDUCTORS (cont.)** ACCEPTABLE **UNACCEPTABLE LEAD FORMING - SMOOTH TOOL MARKS REDUCED CROSS-SECTIONAL AREA** Smooth tool impression marks resulting from tool Part leads and other conductors that have holding forces are acceptable, provided they do deformation / damage resulting in a reduced not expose base metal or reduce cross-sectional cross-sectional area shall not be used. area. NASA-STD-8739.3 [ 7.2.3 ], [ 8.1.6.d ] NASA-STD-8739.4 [ 10.1.3 ] NASA-STD-8739.3 [7.2.3], [8.1.6.d]



ACCEPTABLE PREFORMING / SIZING

Part leads shall be formed so that they may be installed into the holes in the PWB without excessive deformation that can stress the part body or end seals. All leads should be tinned and formed prior to mounting.

NASA-STD-8739.3 [ 8.1.6.b ], [ 8.1.6.c ]



UNACCEPTABLE IMPROPER PREFORMING / SIZING Part leads shall be formed so that they may be

excessive deformation that can stress the part body or end seals.

NASA-STD-8739.3 [ 8.1.6.b ], [ 8.1.6.c ]



ACCEPTABLE TINNING – COVERAGE

The portion of stranded or solid conductors, or part leads shall be solder tinned and cleaned prior to attachment. The solder shall completely wet the conductor and exhibit 100% coverage.

NASA-STD-8739.3 [ 7.2.5 ], [ 7.2.6 ] NASA-STD-8739.4 [ 10.1.5 ]



### UNACCEPTABLE IMPROPER TINNING (COVERAGE)

The solder shall completely wet the conductor and shall exhibit 100% coverage.

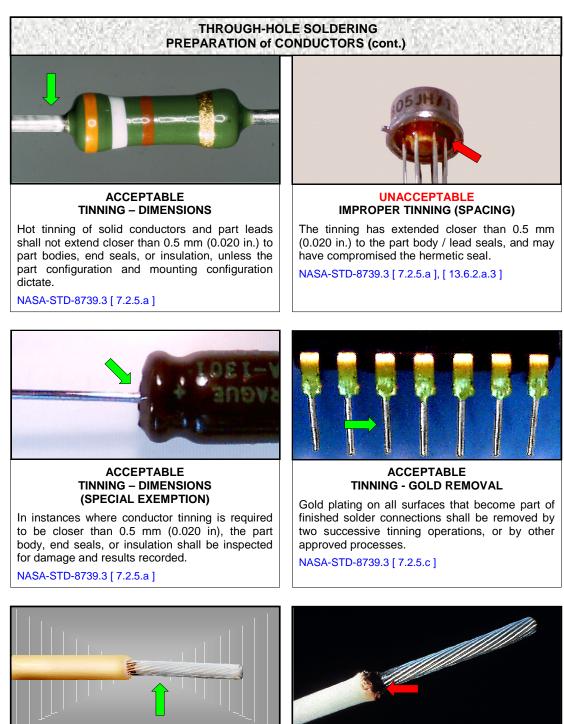
NASA-STD-8739.3 [ 7.2.5 ], [ 7.2.6 ], [ 13.6.2.a.3 ] NASA-STD-8739.4 [ 10.1.5 ]

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### ACCEPTABLE **TINNING - STRANDED WIRE**

The solder shall completely wet the conductor, penetrate to the inner strands, and exhibit 100% coverage. Wire strands shall be distinguishable. Wicking of flux or solder shall be minimized.

NASA-STD-8739.3 [7.2.5], [7.2.6] NASA-STD-8739.4 [ 10.1.5 ]



### **UNACCEPTABLE** DAMAGED INSULATION

After stripping and tinning, the conductor insulation shall not exhibit any damage, such as nicks, cuts, or charring. Conductors with damaged insulation shall not be used.

NASA-STD-8739.3 [ 13.6.2.a.1 ] NASA-STD-8739.4 [ 19.6.2.a.2 ]

# NASA WORKMANSHIP STANDARDS



NATIONAL AERONAUTICS AND SPACE ADMINISTRATION

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### THROUGH-HOLE SOLDERING PREPARATION OF CONDUCTORS (cont.)



ACCEPTABLE TINNING - TERMINALS / SOLDER CUPS

Terminals and solder cups shall be solder tinned, examined for damage, and cleaned prior to the attachment of conductors.

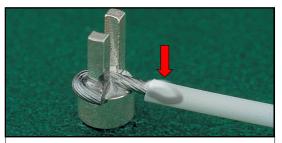
NASA-STD-8739.3 [ 7.2.5 ], [ 7.3.1 ], [ 7.3.2 ]



### UNACCEPTABLE DAMAGED CONDUCTOR – GENERAL

After removal of the conductor insulation and/or lead forming, the conductor shall not be nicked, cut, or scraped to the point that base metal is exposed.

NASA-STD-8739.3 [ 13.6.2.a.8 ] NASA-STD-8739.4 [ 19.6.2.a.1 ]



UNACCEPTABLE EXCESSIVE WICKING

The use of flux and the solder-tinning operation shall be controlled to limit wicking under the insulation.

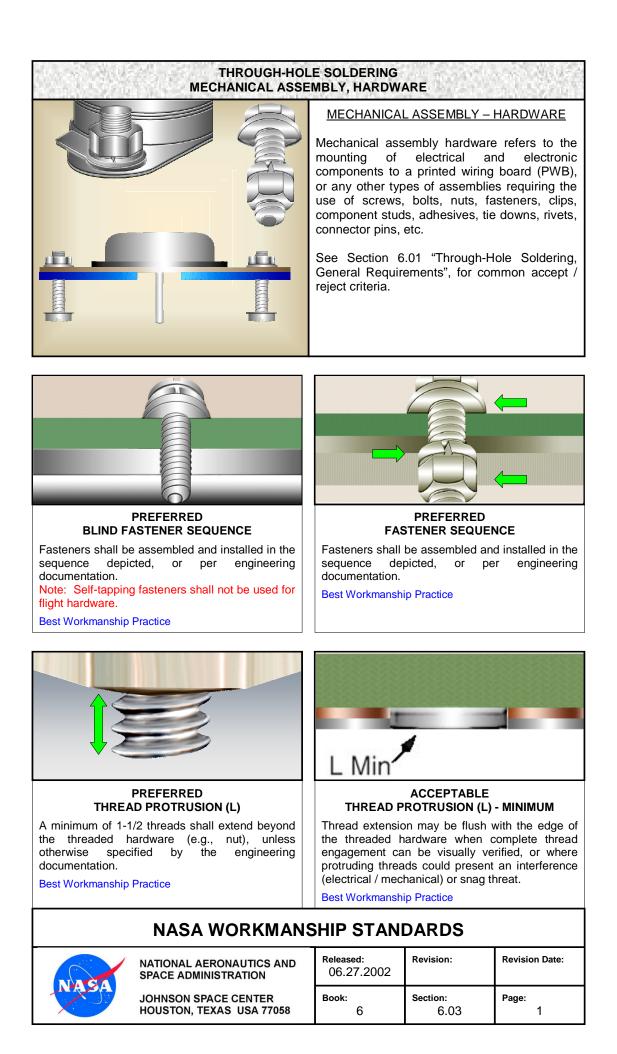
NASA-STD-8739.3 [ 7.2.5 ] NASA-STD-8739.4 [ 10.1.5 ]

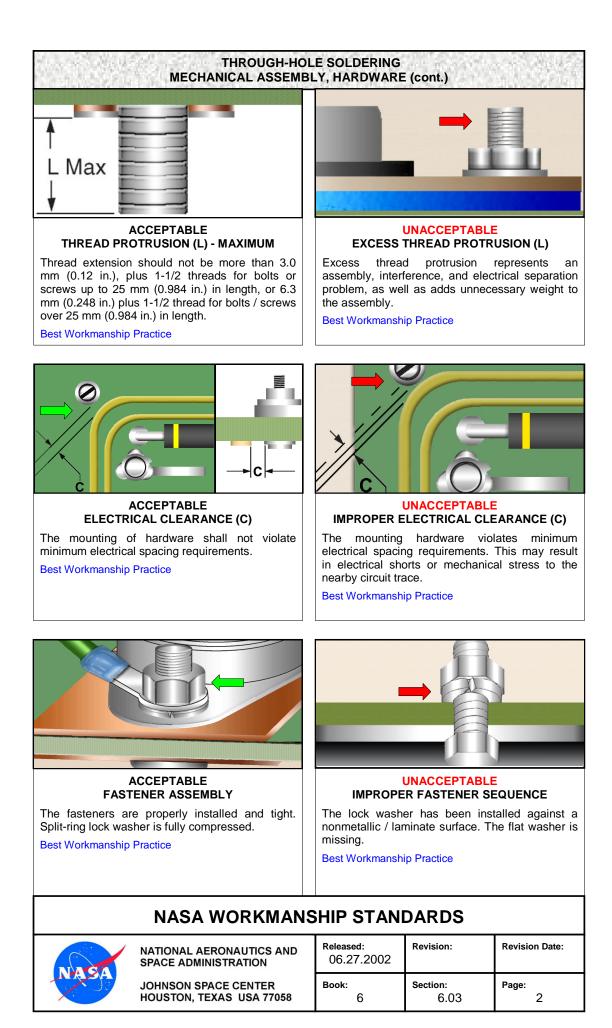
### NASA WORKMANSHIP STANDARDS NATIONAL AERONAUTICS AND SPACE ADMINISTRATION Released: 06.27.2002 Revision: Revision Date: JOHNSON SPACE CENTER HOUSTON, TEXAS USA 77058 Book: Section: 6.02 Page: 5

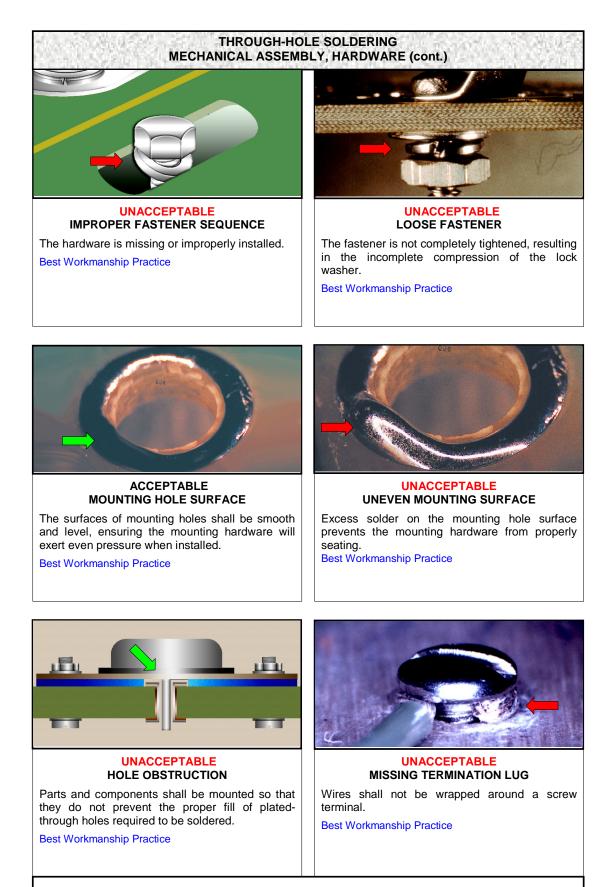
### THROUGH-HOLE SOLDERING PREPARATION OF CONDUCTORS (cont.)

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# NASA WORKMANSHIP STANDARDS



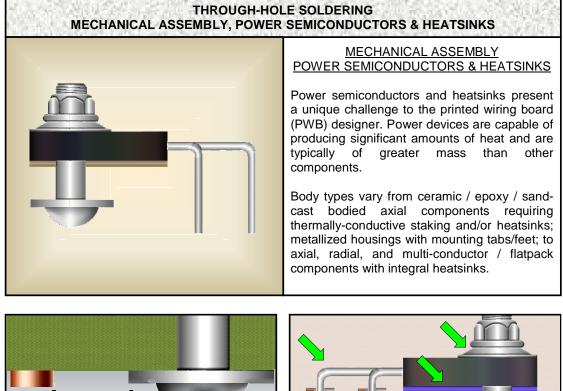
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### THROUGH-HOLE SOLDERING MECHANICAL ASSEMBLY, HARDWARE (cont.)

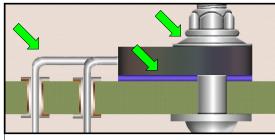
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ACCEPTABLE **ELECTRICAL CLEARANCE (C)** 

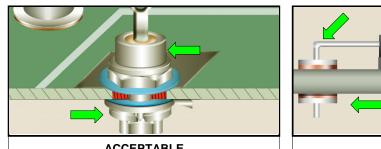
The mounting of hardware shall not violate minimum electrical spacing requirements. **Best Workmanship Practice** 



ACCEPTABLE INTEGRAL HEATSINK COMPONENTS

Component and mounting hardware are installed per engineering documentation. Heatsink body and dielectric are undamaged, and in full contact with no air gaps.

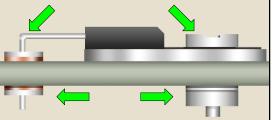
**Best Workmanship Practice** 



### ACCEPTABLE POWER DIODE INSTALLATION

Component and mounting hardware are properly installed.

**Best Workmanship Practice** 



### ACCEPTABLE POWER TAB INSTALLATION

Component and mounting hardware are properly installed.

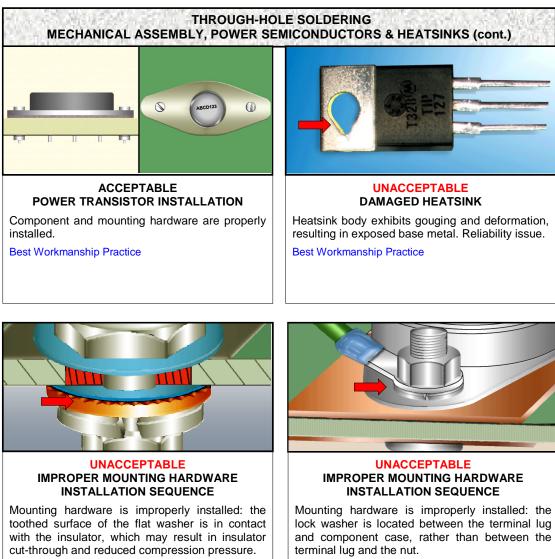
Best Workmanship Practice

# NASA WORKMANSHIP STANDARDS



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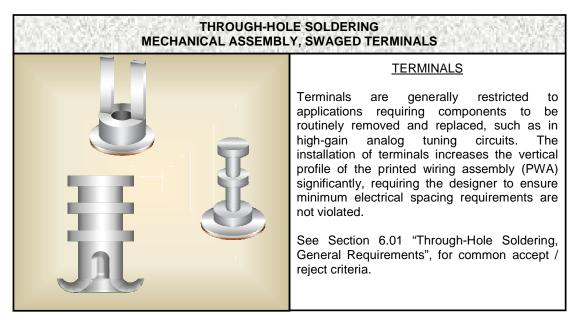


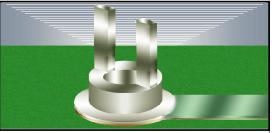
Best Workmanship Practice

lock washer is located between the terminal lug and component case, rather than between the

**Best Workmanship Practice** 

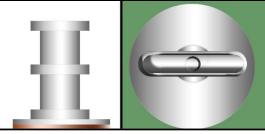
NASA WORKMANSHIP STANDARDS				
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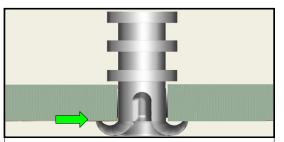
PREFERRED BIFURCATED TERMINAL

The terminal is properly set, aligned, and straight. Tines are straight. No exposed base metal. Flange is swaged sufficiently tight to prevent Zaxis movement, while allowing finger force twisting for adjustment. No damage to the PWB.



PREFERRED ELLIPTICAL FUNNEL SWAGE

The flange is uniformly shaped and concentric to the hole or termination pad. Strain / stress marks are minimum, no splits or cracks. Flange is swaged sufficiently tight to prevent Z-axis movement, while allowing finger force twisting for adjustment. No damage to the PWB.



### PREFERRED ROLL FLANGE SWAGE

The flange is uniformly rolled and concentric to the hole or termination pad. Strain / stress marks are minimum, no splits or cracks. Flange is swaged sufficiently tight to prevent Z-axis movement, while allowing finger force twisting for adjustment. No damage to the PWB.



### PREFERRED TURRET TERMINAL

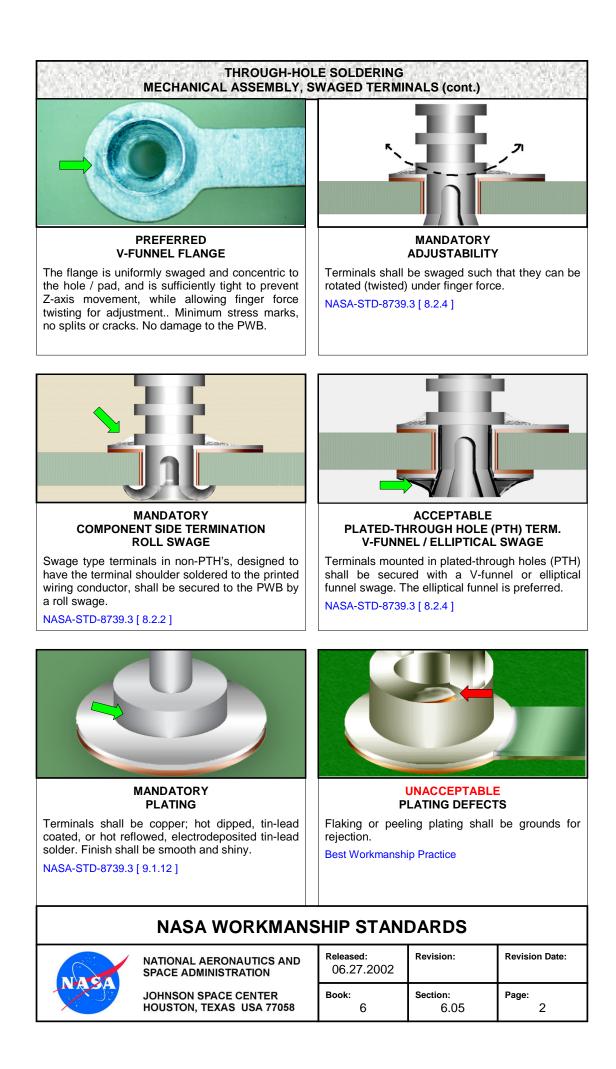
The terminal is properly set and straight. No exposed base metal. Flange is swaged sufficiently tight to prevent Z-axis movement, while allowing finger force twisting for adjustment. No damage to the PWB.

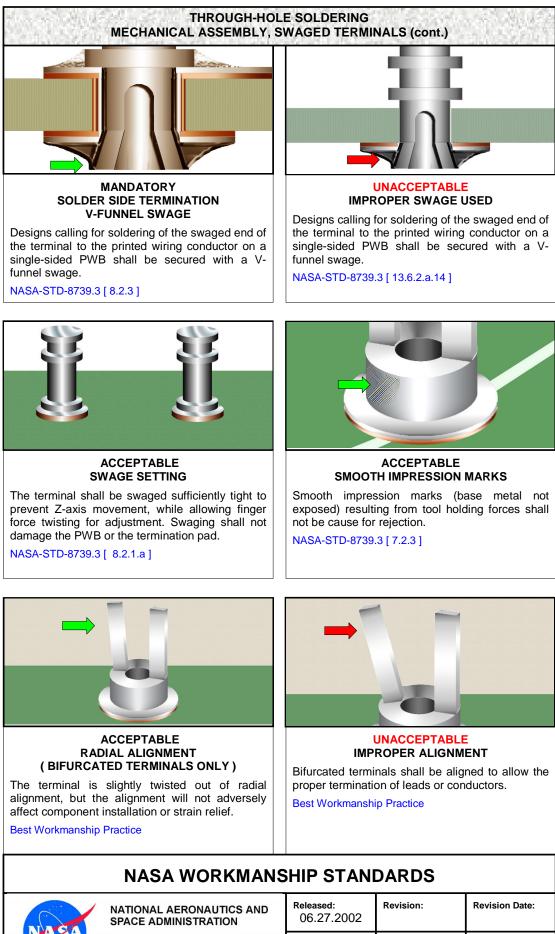
# NASA WORKMANSHIP STANDARDS



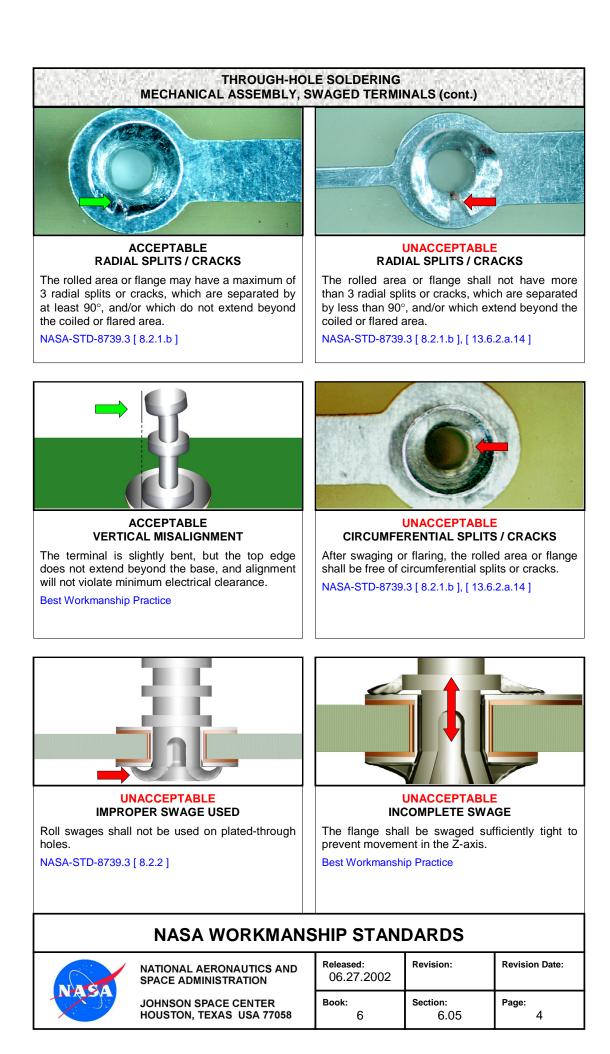
NATIONAL AERONAUTICS AND SPACE ADMINISTRATION

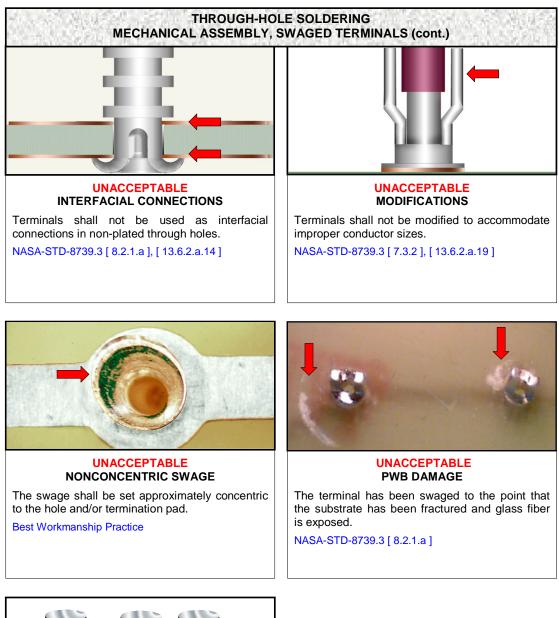
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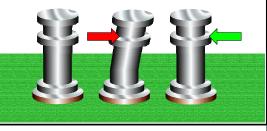




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### UNACCEPTABLE TERMINAL DAMAGE

Terminals exhibiting physical damage (i.e.: nicks, gouging, bent / missing tines, reduced cross-section, etc.) shall be rejected.

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# NASA WORKMANSHIP STANDARDS



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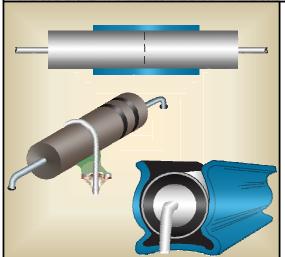
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# THROUGH-HOLE SOLDERING MECHANICAL ASSEMBLY, SWAGED TERMINALS (cont.)

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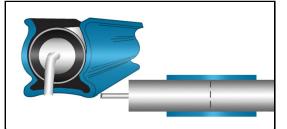
### THROUGH-HOLE SOLDERING MECHANICAL ASSEMBLY - COMPONENT SUPPORT, MECHANICAL



### MECHANICAL ASSEMBLY COMPONENT SUPPORT, MECHANICAL

Components weighing 7 grams (0.25 oz.) total, or 3.5 grams (0.12 oz.) per lead, shall be provided mechanical support, and be bonded to the mounting surface to prevent vibration damage and to improve thermal management. Mechanical support (i.e.: fasteners, throughbolts, clips, etc.) can be used to satisfy this requirement, especially in applications where polymeric staking and bonding methods would not provide satisfactory results.

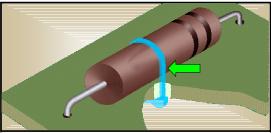
See Section 6.01 "Through-Hole Soldering, General Requirements", for common accept / reject criteria.



ACCEPTABLE **AXIAL COMPONENT CLIP** 

Component is properly inserted in the clip, and leads exhibit proper bend radius and strain relief. Spacing between lands and uninsulated component body meet or exceed minimum electrical clearance.

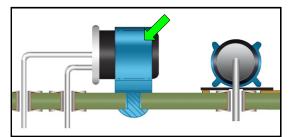
**Best Workmanship Practice** 



ACCEPTABLE CABLE TIE HOLD DOWN

The cable tie is approximately centered, smoothly dressed, and is holding the component firmly in place without deforming the case. The component does not exhibit any damage. Not recommended for high heat environments.

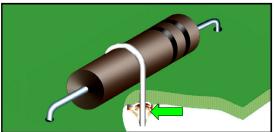
**Best Workmanship Practice** 



#### ACCEPTABLE **RADIAL COMPONENT CLIP**

Component is properly inserted in the clip, and leads exhibit proper bend radius and strain relief. Spacing between lands and the uninsulated component body or clip meet or exceed minimum electrical clearance.

**Best Workmanship Practice** 



#### ACCEPTABLE WIRE HOLD DOWN

The hold down wire is approximately centered, smoothly dressed, does not violate minimum electrical clearance requirements, and is holding the component firmly in place. The component does not exhibit any damage.

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**Best Workmanship Practice** 

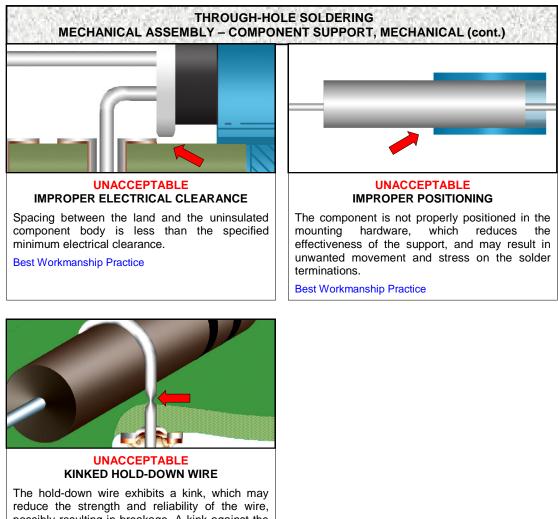
# NASA WORKMANSHIP STANDARDS



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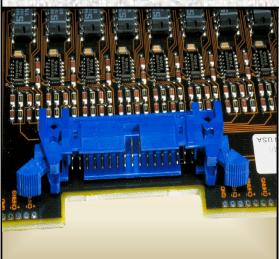


The hold-down wire exhibits a kink, which may reduce the strength and reliability of the wire, possibly resulting in breakage. A kink against the component body may result in component damage or failure.

**Best Workmanship Practice** 

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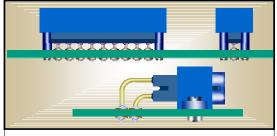
# THROUGH-HOLE SOLDERING SOLDERED CONNECTORS



### SOLDERED CONNECTORS

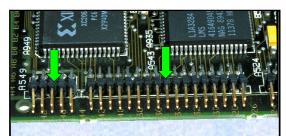
Board-mounted connectors provide an electrical / mechanical solution to the problem of providing reliable input and output of power and signals to the printed wiring assembly (PWA). While the incorporation of connectors into a PWA may increase reliability, bulk, and weight concerns, their use facilitates testing and field replacement, without the hardwiring of harnesses and cables to the PWA.

Connectors are considered to be components, and the mounting and soldering requirements are identical to those imposed on other through-hole components.



PREFERRED GENERAL REQUIREMENTS

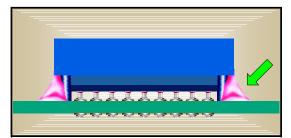
The connector body shall be parallel to and in full contact with the mounting surface. Standoff steps on all leads shall rest on the lands, and lead protrusion shall meet requirements. Mounting features (i.e.: board lock tabs or fasteners) shall be fully inserted and set.



PREFERRED KEYING

Connectors should be keyed to prevent incorrect mating / interchanging with similar sized / colored connectors.

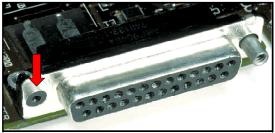
**Best Workmanship Practice** 



#### ACCEPTABLE ALTERNATIVE MECHANICAL MOUNTING

Connectors not supplied with a locking tab or fastener system shall be secured with staking compound. Staking compound shall not be applied over conductive surfaces.

Best Workmanship Practice



UNACCEPTABLE MISSING MOUNTING / CONNECTING HARDWARE

Missing mounting / connecting hardware can interfere with the proper mating of the connector.

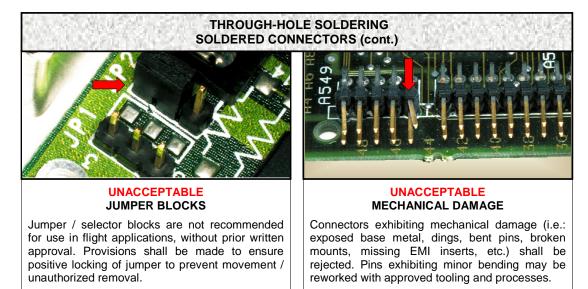
**Best Workmanship Practice** 

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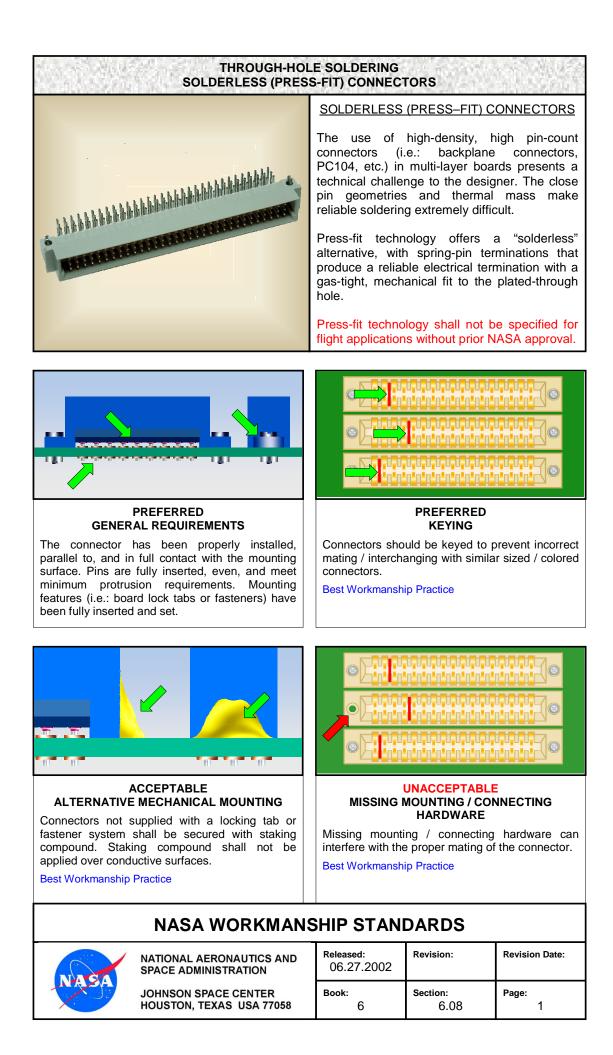
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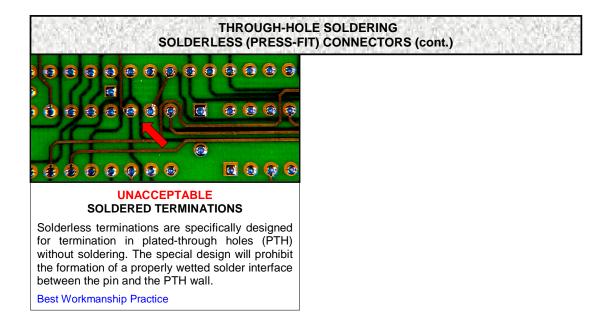


**Best Workmanship Practice** 

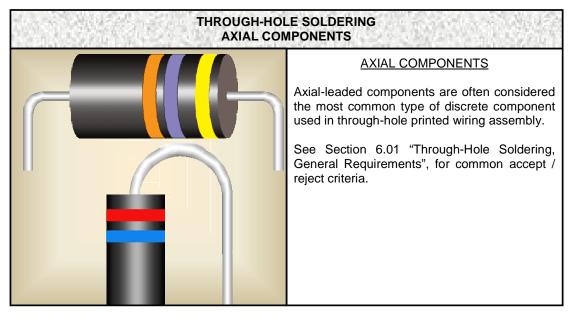
**Best Workmanship Practice** 

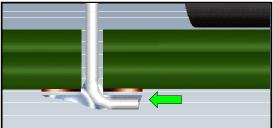
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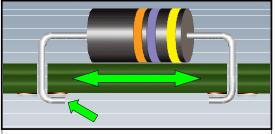




#### PREFERRED HORIZONTAL MOUNTING IN NPTH COMPLETED ASSEMBLY

The component terminations are completely wetted. The solder fillets are smooth, nonporous, undisturbed, exhibit a concave profile, and extend to the edge of the termination pad.

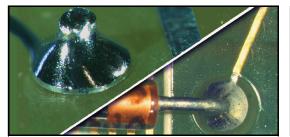
NASA-STD-8739.3 [ 13.6.1 ]



#### PREFERRED HORIZONTAL MOUNTING IN NPTH **INTERIM ASSEMBLY**

Parts shall be parallel to, and in full contact with, the mounting surface, and centered between the termination holes. Leads shall be terminated with an off-the-pad-lap solder joint.

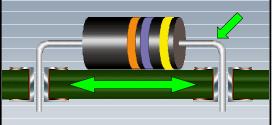
NASA-STD-8739.3 [ 8.4.2.a ]



#### PREFERRED HORIZONTAL MOUNTING IN PTH COMPLETED ASSEMBLY

The component terminations on both sides of the board are completely wetted. The solder fillets are smooth, nonporous, undisturbed, exhibit a concave profile, and extend to the edge of the termination pad.

NASA-STD-8739.3 [ 13.6.1.f ]



PREFERRED HORIZONTAL MOUNTING IN PTH **INTERIM ASSEMBLY** 

Parts shall be parallel to, and in full contact with, the mounting surface, and approximately centered between the termination holes. Leads exhibit proper stress relief bends and spacing.

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NASA-STD-8739.3 [ 8.4.2.a ]

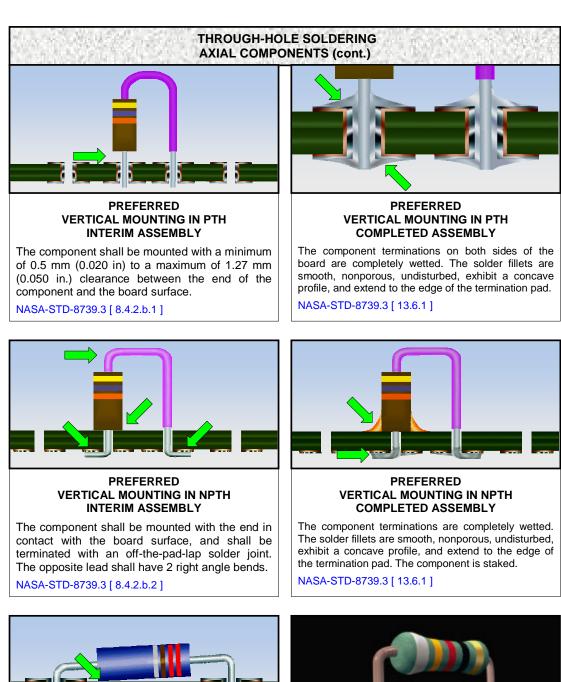
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ACCEPTABLE ANGULARITY

Angularity shall not exceed 0.68 mm (0.025 in.), provided part of the component is in contact with the board, and the angularity does not violate minimum electrical spacing or lead protrusion requirements.

**Best Workmanship Practice** 



### ACCEPTABLE **HEAT PRODUCING PARTS**

Parts which dissipate heat in quantities of 1 Watt or greater, or in quantities sufficient to damage the laminate shall be mounted with sufficient standoff [ ≥ 1.5mm (0.060 in.) ] and shall be mechanically restrained.

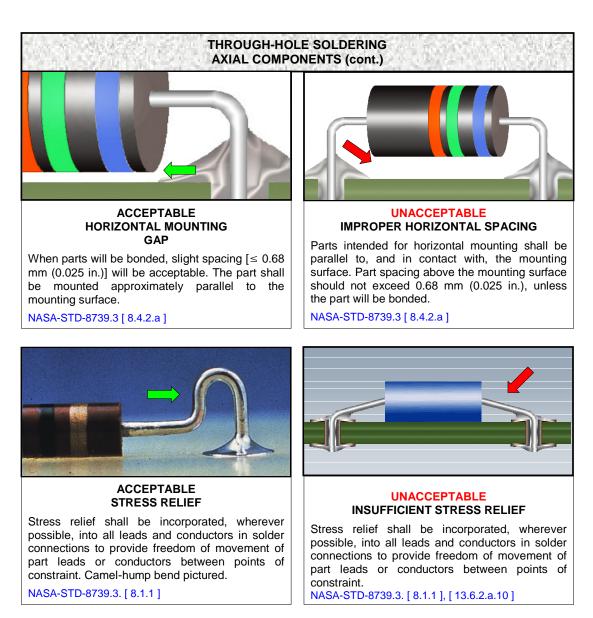
**Best Workmanship Practice** 

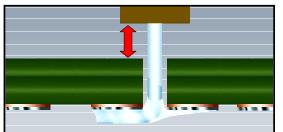
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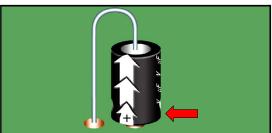




### UNACCEPTABLE IMPROPER VERTICAL MOUNTING NON-PLATED-THROUGH HOLE (NPTH)

The component has been mounted with a space between the component end and the board surface, eliminating any mechanical support to the part or solder joint.

NASA-STD-8739.3 [ 13.6.2.a.6 ]



#### UNACCEPTABLE IMPROPER VERTICAL MOUNTING PLATED THROUGH HOLE (PTH)

The component has been mounted with the end of the component in contact with the platedthrough-hole (PTH). This will result in solder contact with the part body meniscus.

NASA-STD-8739.3 [8.4.2.b.1 ], [ 13.6.2.a.6 ]

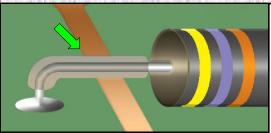
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# THROUGH-HOLE SOLDERING AXIAL COMPONENTS (cont.)



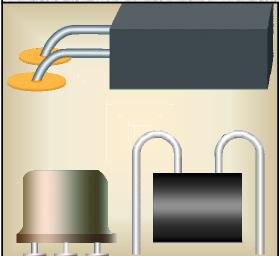
### ACCEPTABLE LEADS CROSSING EXPOSED CONDUCTORS

Leads crossing exposed conductors shall be sleeved with non-conductive sleeving or shrink tubing. Tubing shall be trimmed to meet insulation spacing requirements. Transparent / translucent material is recommended.

**Best Workmanship Practice** 

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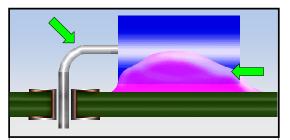
# THROUGH-HOLE SOLDERING RADIAL COMPONENTS



### RADIAL COMPONENTS

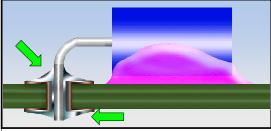
Radial-leaded components are often used in the packaging of capacitors and transistor TO cans. Radials differ from axials in that the leads exit from a common side of the component, rather from opposite ends or sides. This gives the radial a vertical profile and a smaller printed wiring footprint.

See Section 6.01 "Through-Hole Soldering, General Requirements", for common accept / reject criteria.



PREFERRED HORIZONTAL MOUNTING IN PTH INTERIM ASSEMBLY

The component body is in flat contact with, and bonded to the board surface. Component leads exhibit proper bend radius and bend spacing.

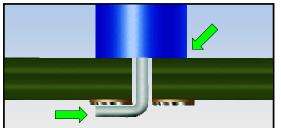


### PREFERRED HORIZONTAL MOUNTING IN PTH COMPLETED ASSEMBLY

The completed assembly exhibits proper solder fillet formation on both the component and solder sides of the printed wiring board (PWB).

NASA-STD-8739.3 [ 13.6.1 ]

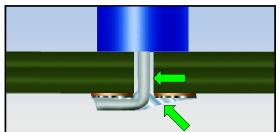




#### PREFERRED VERTICAL MOUNT IN NPTH INTERIM ASSEMBLY

The component has been installed perpendicular to, and the base parallel to, the board. The component may be mounted on the PWB surface and terminated with an off-the-pad lap joint.

NASA-STD-8739.3 [ 8.4.3 ]



#### PREFERRED VERTICAL MOUNT IN NPTH COMPLETED ASSEMBLY

The component leads have been properly soldered. Solder has not wicked up through the non-plated-through-hole (NPTH) and contacted the lead seal / meniscus.

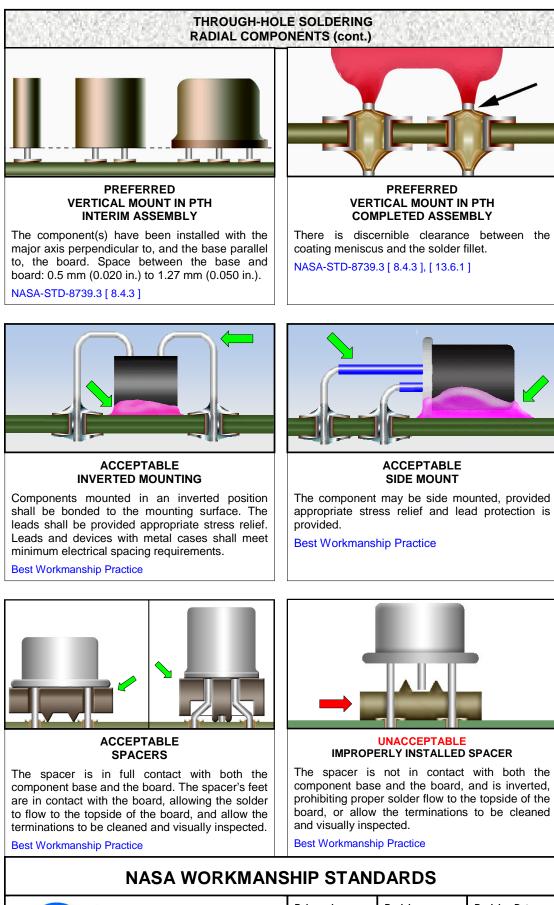
NASA-STD-8739.3 [ 8.4.3 ]

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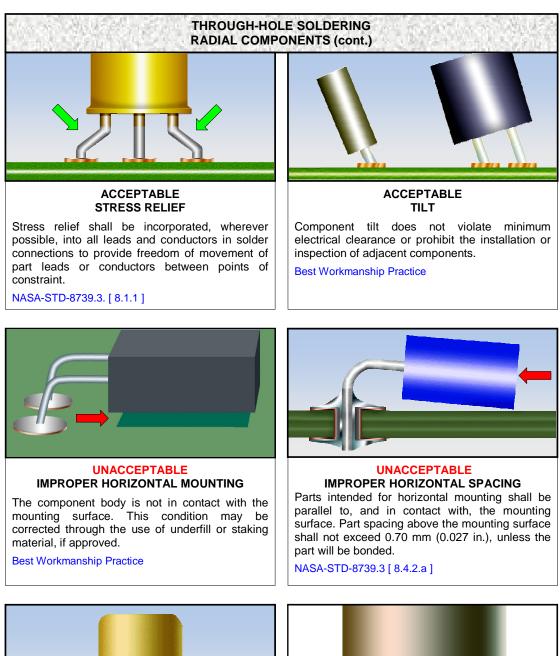
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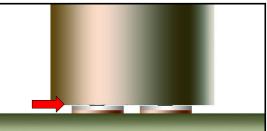




### UNACCEPTABLE IMPROPER VERTICAL SPACING NON-PLATED-THROUGH HOLE (NPTH)

While the component has not been mounted with the end in contact with the board surface, the leads have been terminated with an off-the-padlap solder joint.

NASA-STD-8739.3 [ 13.6.2.a.6 ]



#### UNACCEPTABLE IMPROPER VERTICAL SPACING PLATED THROUGH HOLE (PTH)

The component has been mounted with the end of the component in contact with the platedthrough-hole (PTH). This will result in solder contact with the part body meniscus.

NASA-STD-8739.3 [8.4.2.b.1 ], [ 13.6.2.a.6 ]

# NASA WORKMANSHIP STANDARDS



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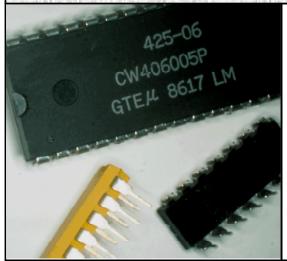
# THROUGH-HOLE SOLDERING RADIAL COMPONENTS (cont.)

1018

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### THROUGH-HOLE SOLDERING COMPONENT INSTALLATION – DUAL IN-LINE PACKAGES (DIPS)



### COMPONENT INSTALLATION DUAL IN-LINE PACKAGES (DIPS)

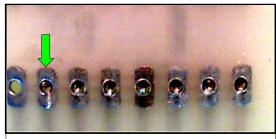
Dual In-Line Packages (DIPS) are the component body type most associated with printed wiring assemblies (PWA) using through-hole technology. The DIP body can be either plastic or ceramic with between 6 to 64 leads.

See Section 6.01 "Through-Hole Soldering, General Requirements", for common accept / reject criteria.



PREFERRED

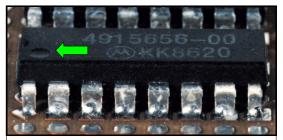
The component has been properly oriented and all leads are fully inserted in the termination holes with the lead standoff step in contact with the lands. The component body is undamaged and part markings are legible and visible on top of component body.



ACCEPTABLE PARTIALLY CLINCHED LEADS

The corner leads may be partially clinched outward from the chip body's longitudinal axis to temporarily secure the component. Clinching shall not violate minimum electrical spacing requirements, or adversely affect solderability.

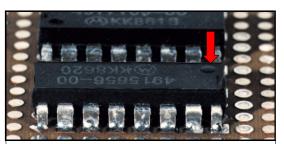
Best Workmanship Practice



### ACCEPTABLE ORIENTATION / POLARITY

The component has been properly installed. The locator chip's notch / dimple, which identifies pin 1, is lined up with the silkscreen pattern. A square-shaped solder pad on the printed wiring pattern may also be used to identify pin 1.

NASA-STD-8739.3 [ 8.1.3 ]



### UNACCEPTABLE IMPROPER ORIENTATION / POLARITY

The DIP has been installed backwards. The locator notch / dimple, which identifies pin 1 of the chip, should be lined up to the silkscreen and/or conductive pattern marks.

NASA-STD-8739.3 [ 13.6.2.a.5 ]

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# **THROUGH-HOLE SOLDERING** COMPONENT INSTALLATION - DUAL IN-LINE PACKAGES (DIPS) (cont.) ACCEPTABLE **UNACCEPTABLE** TILT EXCESS TILT The component exhibits minor tilting, but the tilt Excess part tilt causes the leads to not meet does not reduce lead protrusion below acceptable minimum protrusion requirements. Excess tilt minimums, cause the component body to exceed may cause the part to exceed maximum height height requirements, or violate minimum electrical requirements, or result in violation of minimum spacing requirements. electrical clearance requirements. NASA-STD-8739.3 [ 8.1 ], [ 13.6.1 ] NASA-STD-8739.3 [ 13.6.2.a.21 ] **UNACCEPTABLE UNACCEPTABLE IMPROPER ORIENTATION / OFFSET BENT / CURLED LEAD** The lead has been smashed into the pad surface, The component has been incorrectly installed, with the chip offset with respect to the intended preventing proper insertion. This may be caused by improper lead planarity, an improperly bent termination pattern. This failure is typically lead, or a solder-plugged hole. caused by insertion of the chip leads into the bypass capacitor mounting holes. NASA-STD-8739.3 [ 13.6.2.a.7 ], [ 13.6.2.a.21 ] NASA-STD-8739.3 [ 13.6.2.a.5 ]

### UNACCEPTABLE PISTONED LEAD

The lead has been displaced vertically (pistoned) during insertion. This may be caused by improper lead planarity, an improperly bent lead, or a solder-plugged hole.

NASA-STD-8739.3 [ 13.6.2.a.7 ], [ 13.6.2.a.21 ]

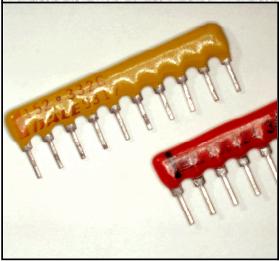
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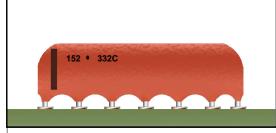
### THROUGH-HOLE SOLDERING SINGLE IN-LINE PACKAGE / SIP



### SINGLE IN-LINE PACKAGE (SIP)

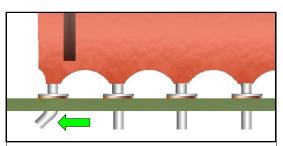
Single In-Line Package (SIP) components have a flat body oriented vertically to the printed wiring board and a single row of pins or leads. Most small-form SIPs are parallel-array devices of common value components (i.e.: diode, resistor arrays). Large-form SIPs are usually hybrid circuits (i.e. timers, oscillators, etc.). The SIP body can be either plastic or ceramic with between 4 to 64 leads.

See Section 6.01 "Through-Hole Soldering, General Requirements", for common accept / reject criteria.



#### PREFERRED

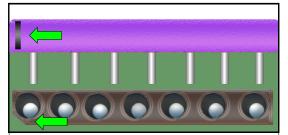
The component has been properly oriented and all leads are fully inserted in the termination holes with the lead standoff step in contact with the lands. The component body is undamaged and part markings are legible and visible.



ACCEPTABLE PARTIALLY CLINCHED LEADS

The end leads may be partially clinched to temporarily secure the component. Clinching shall not violate minimum electrical spacing requirements, or adversely affect solderability.

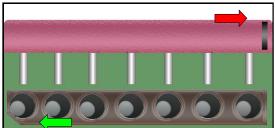
Best Workmanship Practice



### ACCEPTABLE ORIENTATION / POLARITY

The component has been properly installed. The chip's notch / paint stripe, which identifies pin 1, is lined up with the silkscreen pattern. A square-shaped solder pad on the printed wiring pattern may also be used to identify pin 1.

NASA-STD-8739.3 [ 8.1 ]



### UNACCEPTABLE IMPROPER ORIENTATION / POLARITY

The SIP has been installed backwards. The locator notch / dimple, which identifies pin 1 of the chip, should be lined up to the silkscreen and/or conductive pattern marks.

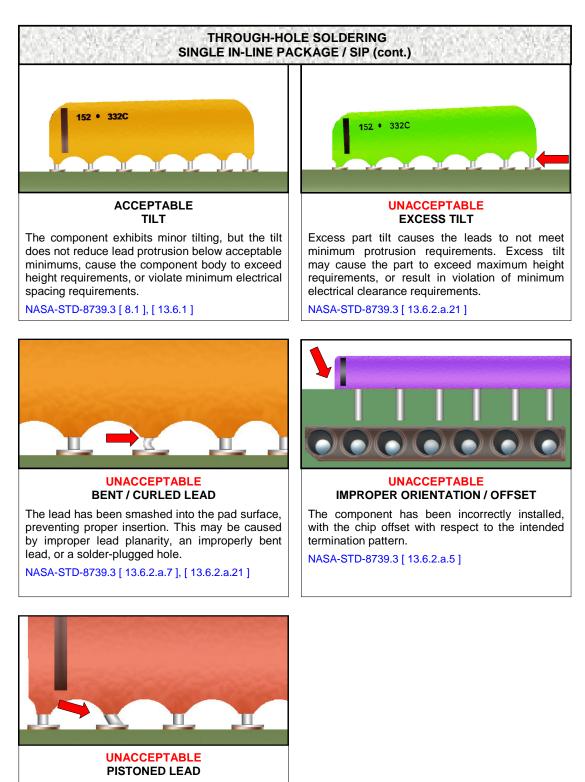
NASA-STD-8739.3 [ 13.6.2.a.5 ]

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The lead has been displaced vertically (pistoned) during insertion. This may be caused by improper lead planarity, an improperly bent lead, or a solder-plugged hole.

NASA-STD-8739.3 [ 13.6.2.a.7 ], [ 13.6.2.a.21 ]

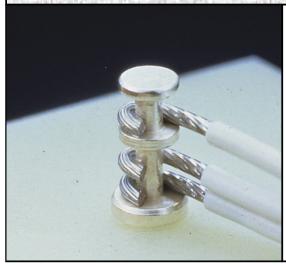
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# THROUGH-HOLE SOLDERING TERMINALS



TERMINALS

The proper installation and soldering of wires and component leads to terminals is important to the overall electrical and mechanical reliability of the termination. Particular attention should be paid to routing and stress relief.

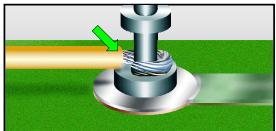
See Section 6.01 "Through-Hole Soldering, General Requirements", for common accept / reject criteria.



GENERAL REQUIREMENTS INSULATION GAP

The insulation gap (referenced from the first point of contact of the conductor to the terminal) shall be less than two (2) wire diameters, but shall not be imbedded in the solder joint. The wire contour shall be visible at the end of the insulation.

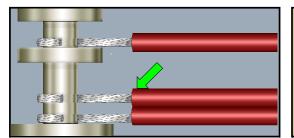
NASA-STD-8739.3 [ 9.1.1 ], [ 9.1.2 ]



GENERAL REQUIREMENTS INSULATION GAP (SPECIAL EXCEPTION)

When characteristic impedance or other circuit parameters may be affected (i.e.: high-voltage, high-frequency terminations, etc.), the insulation clearance requirements may be modified. All variations shall be documented.

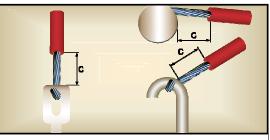
NASA-STD-8739.3 [ 9.1.4 ]



GENERAL REQUIREMANTS INSULATION GAP MULTIPLE TERMINATIONS

Conductor insulation clearances are not required to be equal for applications involving the termination of multiple (common) conductors to a terminal in parallel orientation.

NASA-STD-8739.3 [ 9.1.3 ]



UNACCEPTABLE IMPROPER INSULATION GAP (C)

The insulation gap (referenced from the first point of contact of the conductor to the terminal) is greater than two (2) wire diameters. Excessive insulation gap may present a birdcaging or shorting risk.

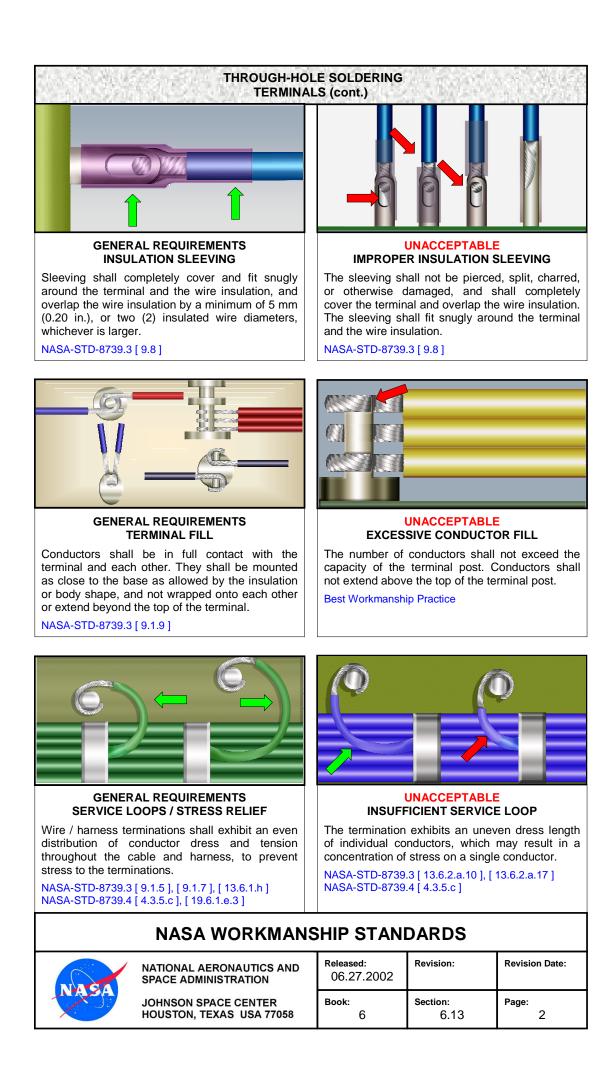
NASA-STD-8739.3 [ 13.6.2.a.2 ]

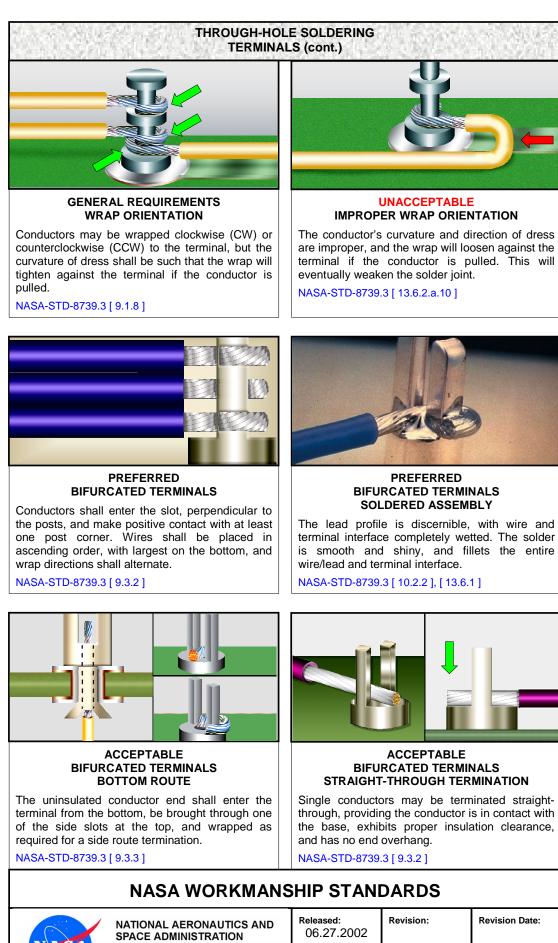
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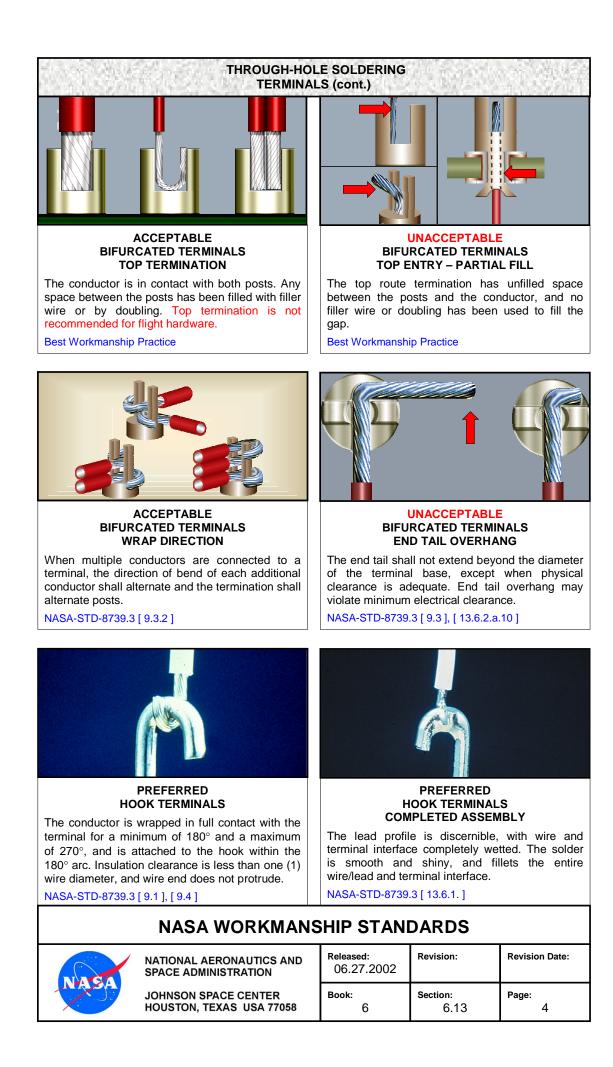
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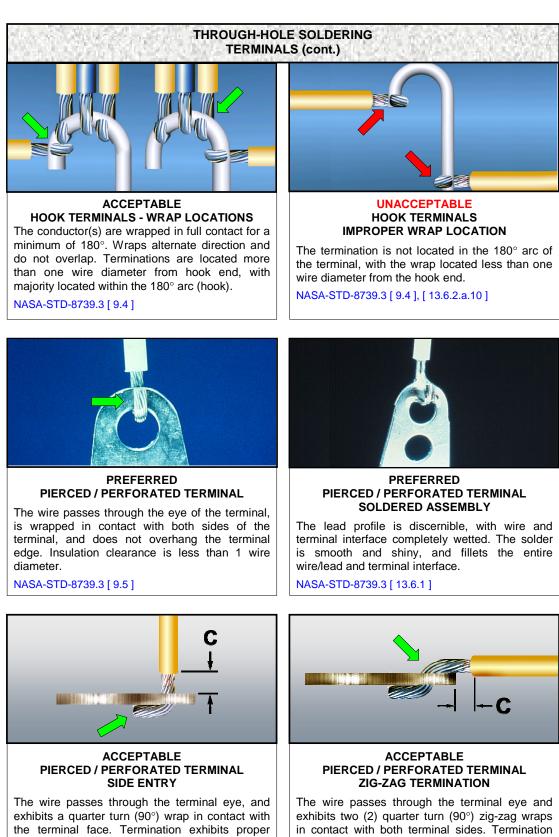




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insulation clearance (C). NASA-STD-8739.3 [ 9.5 ]

NASA-STD-8739.3 [ 9.5 ]

exhibits proper insulation clearance (C).

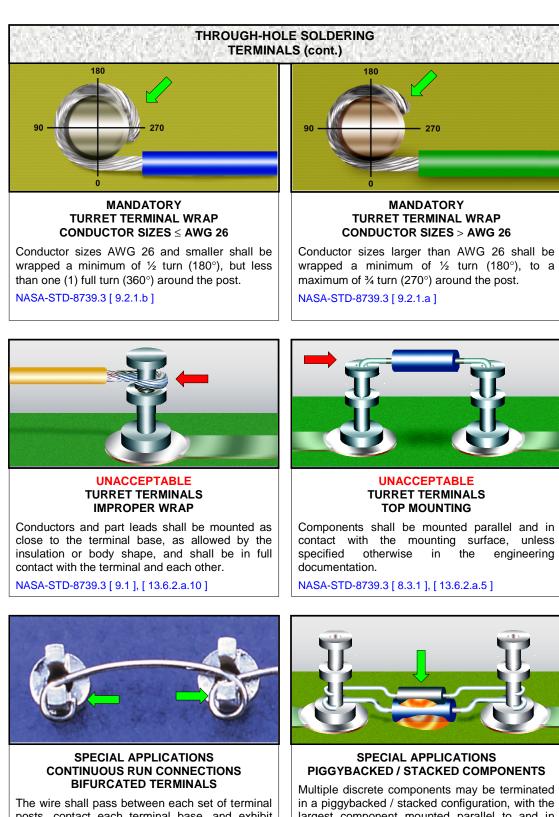
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posts, contact each terminal base, and exhibit stress relief. The wire ends shall be attached to the first and last terminal with a 90° to 180° wrap. NASA-STD-8739.3 [ 9.3.4 ]

largest component mounted parallel to and in contact with the mounting surface. All components shall be stress-relieved and staked.

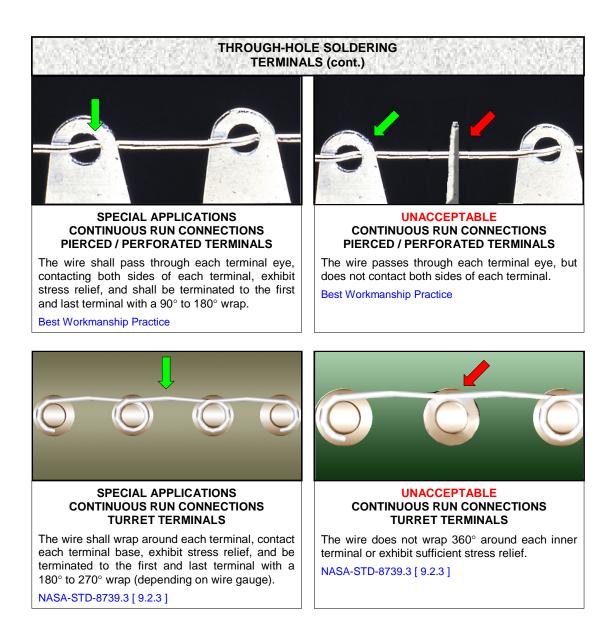
**Best Workmanship Practice** 

# NASA WORKMANSHIP STANDARDS



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# THROUGH-HOLE SOLDERING SOLDER CUPS

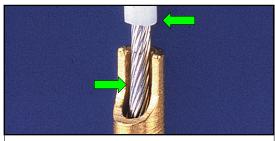


### SOLDER CUPS

Solder cup terminals are primarily designed for the in-line solder termination of conductors. This style of terminal is principally designed as a precision-machined pin for insertion into connector bodies.

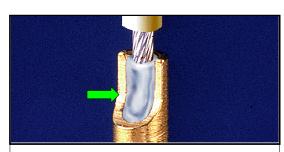
Variations include connectors in which the solder cup pin is captive in the connector body (i.e.: hermetic connectors), or printed wiring board mounted terminals designed for discrete wire terminations.

See Section 6.01 "Through-Hole Soldering, General Requirements", for common accept / reject criteria.



PREFERRED INTERIM ASSEMBLY

The wire has been inserted straight into the cup, is in contact the back wall for the full depth of the cup, and bottoms in the cup. The assembly exhibits proper insulation gap and the cup interior has been pretinned.

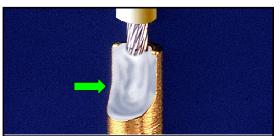


PREFERRED COMPLETED ASSEMBLY

The solder shall form a fillet between the conductor and the cup entry slot, and shall follow the contour of the cup opening.

NASA-STD-8739.3 [ 10.2.3 ]

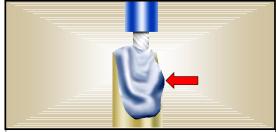
NASA-STD-8739.3 [ 9.6 ]



ACCEPTABLE MAXIMUM SOLDER

The solder quantity is the maximum acceptable, but does not spill over (exceed the diameter of the cup), or exhibit a convex profile.

NASA-STD-8739.3 [ 10.2.3.b ]



#### UNACCEPTABLE EXCESS SOLDER

The solder does follow the contour of the cup opening and spills over (exceeds the diameter of the cup) with a convex profile.

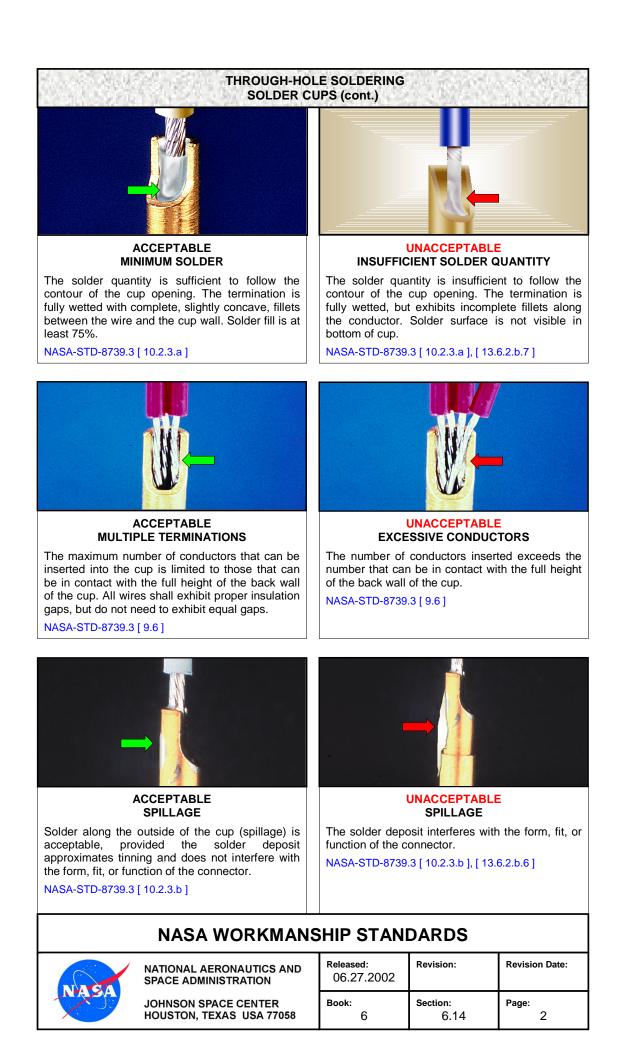
NASA-STD-8739.3 [ 10.2.3.a ], [ 13.6.2.b.6 ]

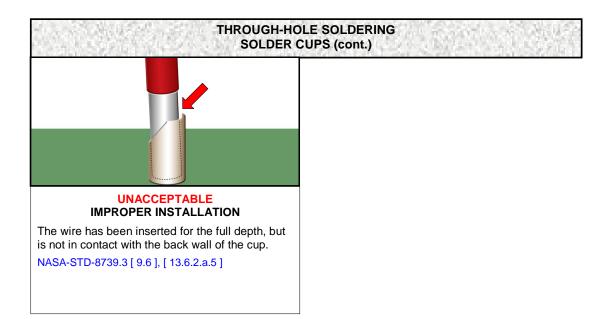
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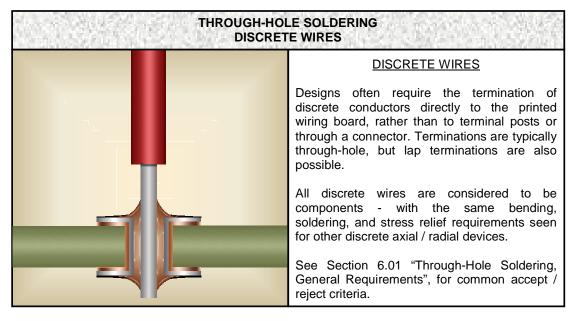


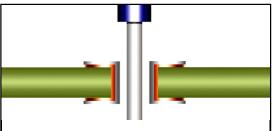
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# THROUGH-HOLE SOLDERING SOLDER CUPS (cont.)

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PREFERRED THROUGH-HOLE TERMINATION INTERIM ASSEMBLY

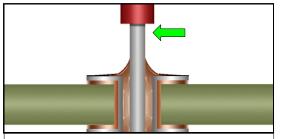
The conductor enters the hole, perpendicular to the board surface, and exhibits proper insulation clearance and lead protrusion. The wire end may be clinched to aid assembly.

NASA-STD-8739.3 [ 8.4 ]

PREFERRED THROUGH-HOLE TERMINATION FINAL ASSEMBLY

The termination exhibits proper insulation clearance and lead protrusion. The termination is fully wetted, with complete fillet formation on both sides of the board.

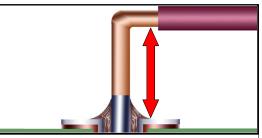
NASA-STD-8739.3 [ 8.4 ]



ACCEPTABLE INSULATION GAP

The insulation gap (referenced from the first point of contact of the conductor to the terminal) shall be less than two (2) wire diameters, but shall not be imbedded in the solder joint. The wire contour shall be visible at the end of the insulation.

NASA-STD-8739.3 [ 9.1.1 ], [ 9.1.2 ]



### UNACCEPTABLE EXCESSIVE INSULATION GAP

The insulation gap exceeds the maximum of two (2) insulated wire diameters, and may present a shorting or birdcaging potential.

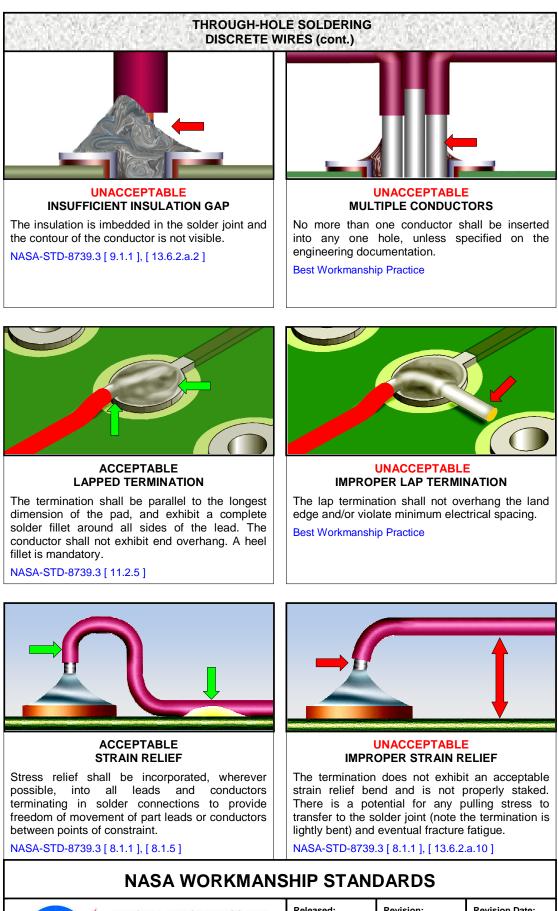
NASA-STD-8739.3 [ 9.1.1 ], [ 13.6.2.a.2 ]

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# THROUGH-HOLE SOLDERING SERVICE LEAD SPLICES



# SERVICE LEAD SPLICES

Design applications may require the termination of a discrete leaded component in a non-standard configuration, where the components are not terminated in the manner originally designed for that package type, the component's leads are being used as terminals, and/or where the termination method is not addressed in the NASA standards.

These terminations impose stress relief requirements on the solder joint and the component lead seals that must be addressed to ensure reliable operation.



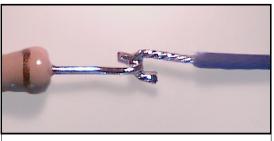
PREFERRED END SPLICE

The end splice is a version of the lash splice, where the conductor ends are laid side by side, wrapped, soldered, and then insulated with shrink tubing. For inline configurations, the splice section may be bent back against the larger conductor(s) and sleeved for strain-relief.



PREFERRED GROUND LEAD / DRAIN WIRE SPLICE

The lash splice can be used to attach a ground lead / drain wire to a shielded cable in instances where a solder sleeve is impractical or too bulky. Assembly rating: Easy / Moderate



PREFERRED HOOK SPLICE

This splice is typically used to terminate discrete, leaded components in "daisy-chain" configurations. The termination has good tensile properties, but is prone to solder joint fatigue if repeatedly flexed. Assembly rating: Easy



# PREFERRED

A lap splice is a non-structural splice, where the component lead end and the conductor end are soldered in parallel, overlapping contact to each other. The splice may be simple or complex, and is more compact than a solder sleeve. Assembly rating: Easy

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### THROUGH-HOLE SOLDERING SERVICE LEAD SPLICES (cont.)



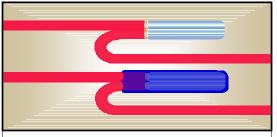
#### PREFERRED LASH SPLICE

A lash splice is a structural splice, consisting of a lap splice with a single strand overlash winding mechanically binding the lead and conductor together. The splice may be simple or complex, and is more compact than a solder sleeve. Assembly rating: Easy / Moderate



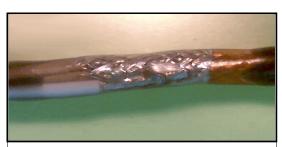
#### PREFERRED LINEMAN / WESTERN UNION SPLICE

This splice is best suited for the termination of wire and cable, but can be used for the termination of discrete component leads. The splice is suitable for situations where the termination may be subjected to tensile loading. Assembly rating: Difficult



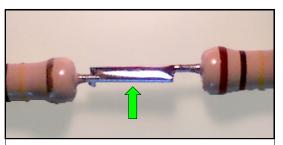
PREFERRED MAGNET WIRE SPLICE

After stripping and preparing for soldering, the ends of the wires shall be twisted together a minimum of three turns in an end slice configuration, soldered, and insulated. The splice section shall be bent back against the larger conductor and strain-relieved.



PREFERRED SOLDER SLEEVES

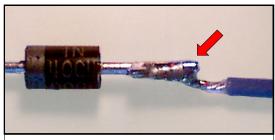
Originally developed for the termination of cable shield drain wires, solder sleeves produce a onestep, insulated, and sealed splice. Solder sleeves are typically larger than that achievable with a lap or lash splice.



### ACCEPTABLE ANGULAR ALIGNMENT ( LAP / LASH )

Angular misalignment of less than 2 lead diameters (measured at the conductor ends of the splice section) is allowable, provided there are no protruding or sharp edges.

**Best Workmanship Practice** 



UNACCEPTABLE LEAD / CONDUCTOR MISALIGNMENT

Angular misalignment in excess of 2 lead diameters (measured at the conductor ends of the splice section) produces a mechanically weak solder joint, with protruding ends or sharp edges.

**Best Workmanship Practice** 

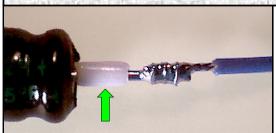
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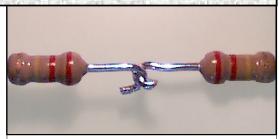
## THROUGH-HOLE SOLDERING SERVICE LEAD SPLICES (cont.)



## ACCEPTABLE COMPONENT LEAD INSULATION

The component lead shall be sleeved with tubing, between the lead seal / weld bead to within 2 lead diameters of the solder joint (if applicable).

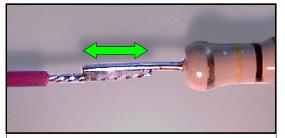
**Best Workmanship Practice** 



#### ACCEPTABLE DAISY-CHAIN

The components exhibit acceptable solder terminations and are sleeved to provide strain relief.

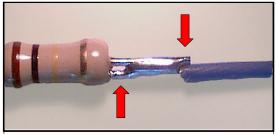
**Best Workmanship Practice** 



ACCEPTABLE CONDUCTOR OVERLAP ( LAP / LASH )

The conductors in the splice section shall be in parallel, overlapping contact to each other. The soldered section shall be a minimum of 5-8 mm (0.2 – 0.3 in.) in length.

**Best Workmanship Practice** 



UNACCEPTABLE EXCESSIVE OVERLAP

The soldered section shall be a minimum of 5-8 mm (0.2 - 0.3 in.) in length, but should not contact the insulation jacket(s) or the lead seal / weld bead. Excessive overlap increases stress on the component lead and body seal / weld bead.

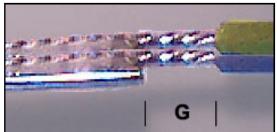
Best Workmanship Practice



#### ACCEPTABLE HOOK SPLICE ORIENTATION

The conductor and the component lead shall be aligned approximately  $90^{\circ}$  to, and in contact with, each other. In multiple conductor configurations, the direction of each additional conductor wrap shall be alternated, and shall not overlap.

Best Workmanship Practice



#### ACCEPTABLE INSULATION GAP

The conductor(s) shall exhibit proper insulation spacing. In multiple conductor configurations, the conductor insulation gaps shall be approximately equal.

NASA-STD-8739.3 [ 9.1.1 ], [ 9.1.2 ] NASA-STD-8739.4 [ 10.1.7.a ]

# NASA WORKMANSHIP STANDARDS



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#### THROUGH-HOLE SOLDERING SERVICE LEAD SPLICES (cont.)



#### ACCEPTABLE INSULATION SLEEVING APPLICATION

The completed solder joint shall be over-sleeved with transparent / translucent heat shrink tubing of sufficient length to cover the solder joint and extend over the insulation of each conductor a minimum of 5 mm (0.20 in.).

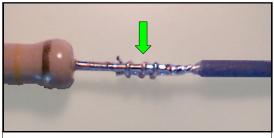
**Best Workmanship Practice** 



#### UNACCEPTABLE EXPOSED TERMINATION

The shrink tubing has been improperly installed, resulting in exposure of the conductive surface. The tubing should be of sufficient length to cover the solder joint and extend over the insulation of each conductor a minimum of 5 mm (0.20 in.).

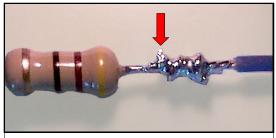
**Best Workmanship Practice** 



ACCEPTABLE LASH WINDING

The lash shall consist of a tinned, solid 34 AWG (or smaller) conductor, tightly wrapped in an open spiral of approximately 4-6 complete, non-overlapping turns, approximately centered over the splice. Lash ends shall be trimmed flush.

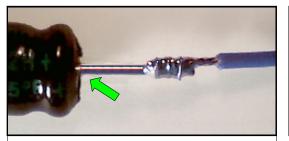
Best Workmanship Practice



UNACCEPTABLE IMPROPER LASH / WRAP

The lash has been completed with a conductor the same gage as the mated conductors, resulting in an insufficient number of wraps to achieve a secure mechanical termination.

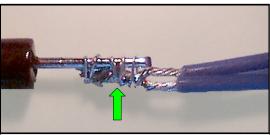
Best Workmanship Practice



ACCEPTABLE LEAD SEAL / WELD BEAD SPACING The component end of the solder joint shall not be closer than 2 lead diameters or 0.50 mm (0.020 in ) which over in larger from the lead cool

(0.020 in.), whichever is larger, from the lead seal / weld bead.

Best Workmanship Practice



#### ACCEPTABLE MULTIPLE / COMPLEX CONFIGURATIONS

In multiple conductor configurations, the conductor ends may be twisted together, with the twisted section parallel to, and in contact with, the component lead.

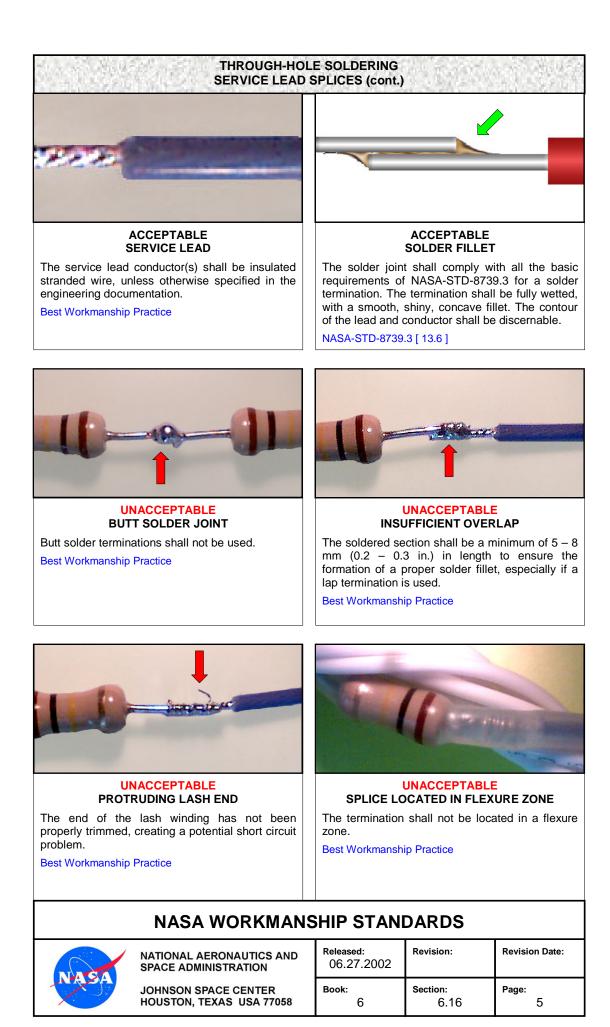
**Best Workmanship Practice** 

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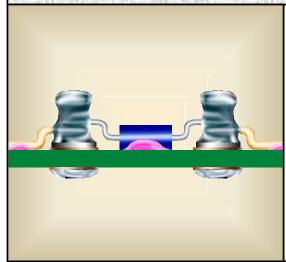
## THROUGH-HOLE SOLDERING SERVICE LEAD SPLICES (cont.)

12.46

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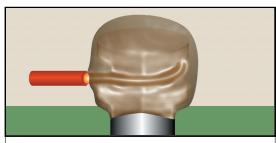
## THROUGH-HOLE SOLDERING HIGH-VOLTAGE TERMINATIONS



## **HIGH VOLTAGE TERMINATIONS**

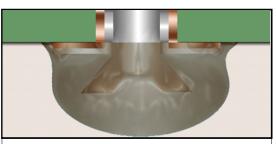
High-voltage terminations, where coronal suppression is necessary, will require special design. All aspects of the soldered joints shall be covered by smooth fillets, free of discontinuities or severe change in surface contour ( i.e.: sharp edges, points, angles, etc.).

See Section 6.01 "Through-Hole Soldering, General Requirements", for common accept / reject criteria.



PREFERRED

The solder connection has a completely rounded, continuous, and smooth profile. No evidence of sharp edges, points, icicles, inclusions (foreign material), or wire strands. Insulation clearance is as close to the solder connection as possible without embedment.

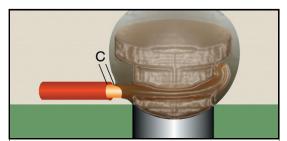


PREFERRED FLARED FLANGE (TERMINAL)

All edges of the terminal flange are completely covered with a continuous, smooth layer of solder to form a solder ball. The balled connection does not exceed specified height requirements.

Best Workmanship Practice

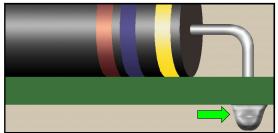
NASA-STD-8739.3 [ 10.3 ]



PREFERRED INSULATION CLEARANCE

The insulation gap is minimal, with the insulation as close to the solder connection as practical without embedment or damage.

NASA-STD-8739.3 [ 9.1.1 ]



#### PREFERRED THROUGH HOLE TERMINATION

All sharp edges of the component lead end are completely covered with a continuous, smooth, rounded layer of solder to form a solder ball. The balled connection does not exceed specified height requirements.

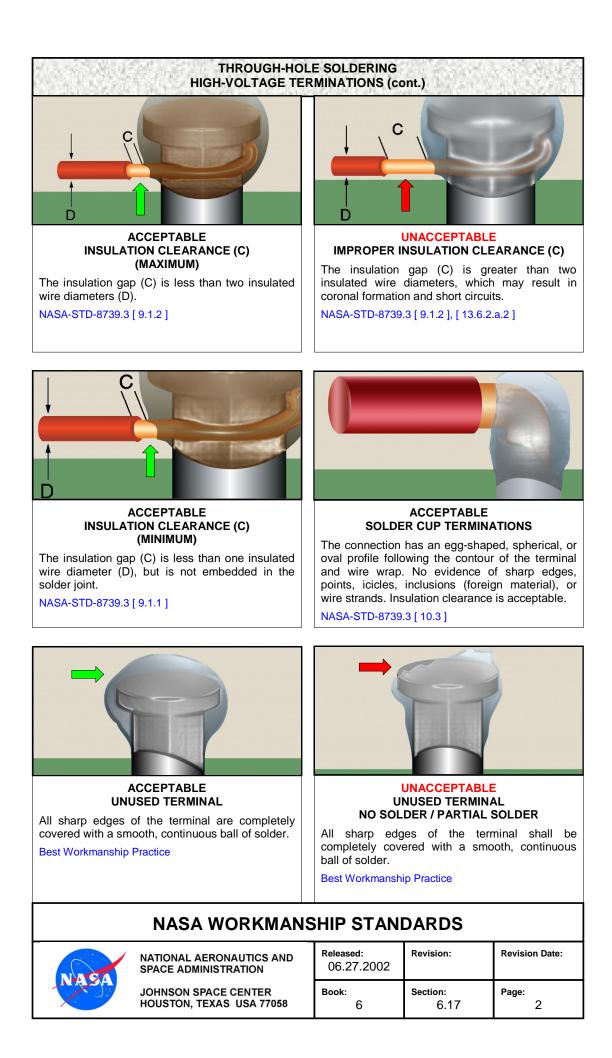
**Best Workmanship Practice** 

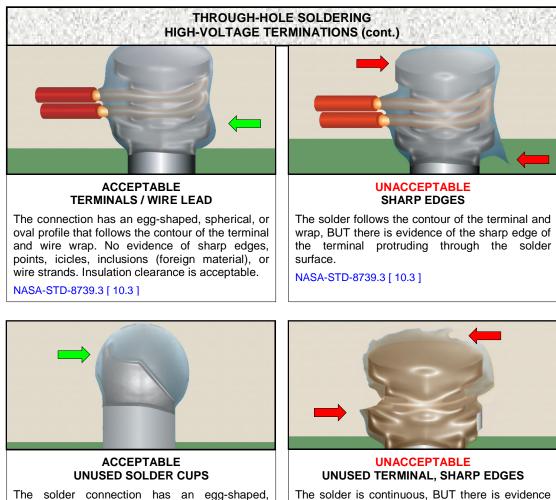
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spherical, or oval profile. No evidence of sharp

edges, points, icicles, inclusions (foreign

material), or wire strands.

**Best Workmanship Practice** 

The solder is continuous, BUT there is evidence of solder peaks, icicles, or sharp turret edges protruding.

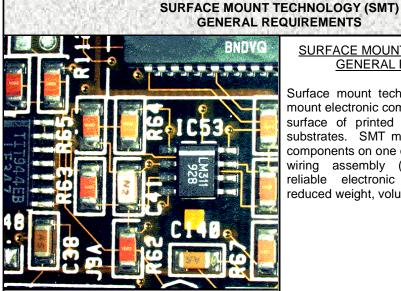
**Best Workmanship Practice** 

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## THROUGH-HOLE SOLDERING HIGH-VOLTAGE TERMINATIONS (cont.)

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## SURFACE MOUNT TECHNOLOGY (SMT) GENERAL REQUIREMENTS

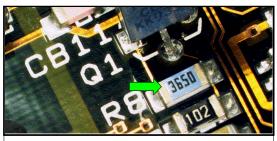
Surface mount technology (SMT) is used to mount electronic components on the metallized surface of printed wiring boards (PWB) or substrates. SMT makes it possible to mount components on one or both sides of the printed wiring assembly (PWA), producing more reliable electronic assemblies at greatly reduced weight, volume, and cost.



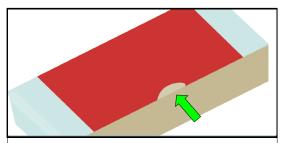
PREFERRED

The solder joint surface is smooth, nonporous, undisturbed, with a finish varying from satin to bright. The fillet completely wets all elements to the periphery of the connection and is concave.

NASA-STD-8739.2 [ 12.8.1 ]



PREFERRED Part markings are visible and oriented uniformly. NASA-STD-8739.2 [ 8.7.4.a ], [ 12.7.1.b ], [ 12.8.1.f ]



#### ACCEPTABLE CHIP-OUTS (NICKS)

Chip-outs (nicks) of the top surface (adhesive coating), less than 0.25mm from the component edge are acceptable. Chips in the component body, element area, or termination area are unacceptable.

NASA-STD-8739.2 [ 8.7.4.b ], [ 8.8.2 ]



#### UNACCEPTABLE CHIP-OUTS (NICKS)

The use of chip-scale parts with chips in the component body or termination area, and any resistive elements with chip outs, is prohibited.

NASA-STD-8739.2 [ 12.8.2.a.3 ]

# NASA WORKMANSHIP STANDARDS

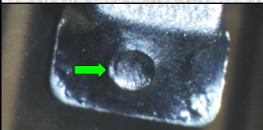


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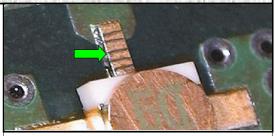


## SURFACE MOUNT TECHNOLOGY (SMT) GENERAL REQUIREMENTS (cont.)



ACCEPTABLE PITS

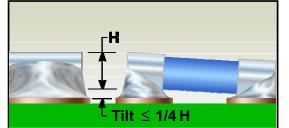
A solder pit is acceptable, provided the bottom of the cavity can be seen from all angles of vision. NASA-STD-8739.2 [ 3.1 ], [ 12.8.2.b.5 ]



ACCEPTABLE SMOOTH TOOL IMPRESSION MARKS

Smooth tool impression marks (slight cuts, nicks, scratches or scrapes) on the conductor surface, which do not expose base metal or reduce cross-sectional area are acceptable.

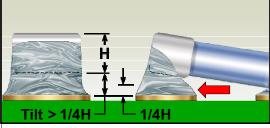
NASA-STD-8739.2 [ 12.8.2.a.4 ]



ACCEPTABLE TILT

Part tilt shall not exceed 25% of component height (H) or diameter (i.e.: MELFs), and shall not interfere with the proper placement of adjacent parts.

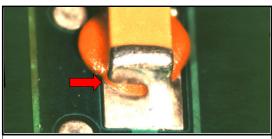
**Best Workmanship Practice** 



UNACCEPTABLE EXCESS TILT

Excessive tilting of a component may impact the long-term reliability and integrity of the solder termination, and may interfere with the proper placement and thermal profile of adjacent parts.

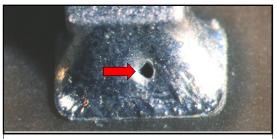
Best Workmanship Practice



UNACCEPTABLE ADHESIVE INCLUSION

Adhesive material in the solder joint shall be cause for rejection.

NASA-STD-8739.2 [ 8.10.3 ], [ 12.8.2.b.9 ]



#### UNACCEPTABLE BLOWHOLE

Blowholes are typically caused by trapped gases or flux during the formation of the solder fillet, and are unacceptable.

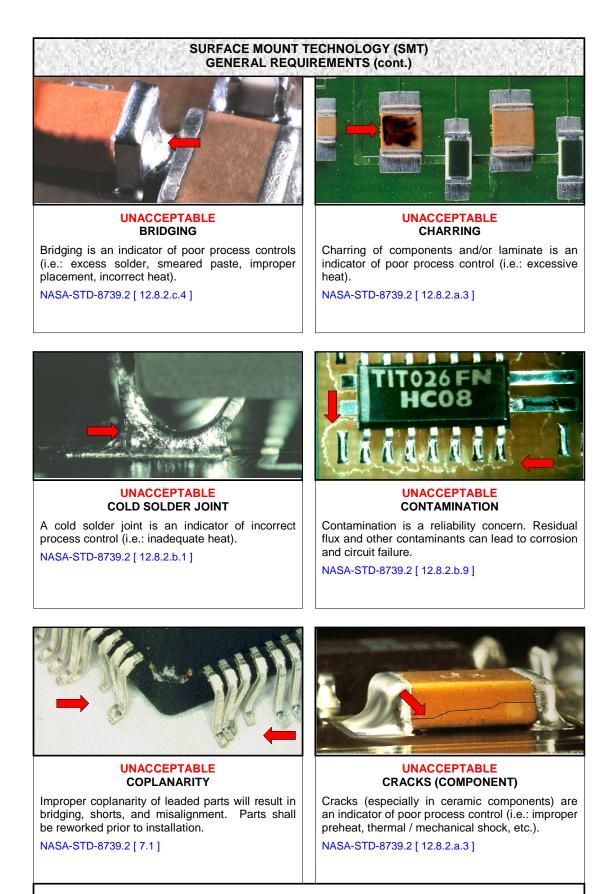
NASA-STD-8739.2 [ 12.8.2.b.5 ]

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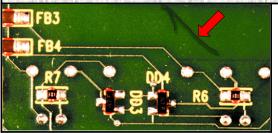
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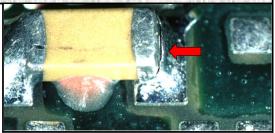
## SURFACE MOUNT TECHNOLOGY (SMT) GENERAL REQUIREMENTS (cont.)



UNACCEPTABLE CRACKS (LAMINATE)

Cracks in the laminate are a reliability concern and are a cause for rejection.

**Best Workmanship Practice** 



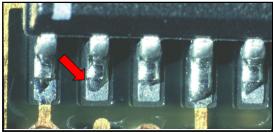
#### UNACCEPTABLE CRACKS (SOLDER FILLET)

Cracks or fractures in the solder fillet are an indication of mechanical / thermal shock, or temperature coefficient mismatch.

NASA-STD-8739.2 [ 12.8.2.b.3 ]



UNACCEPTABLE DAMAGED PART SEAL Parts with damaged seals shall not be used. NASA-STD-8739.2 [ 12.7.2.b ]



UNACCEPTABLE DEWETTING

Dewetting is an indicator of poor process control (i.e.: excessive heat dwell following reflow). NASA-STD-8739.2 [ 12.8.2.b.10 ]



#### UNACCEPTABLE DISCOLORED LAMINATE (BURNS)

Burns that physically damage the laminate surface or the assembly are not allowed. Slight discoloration is allowable.

NASA-STD-8739.2 [ 12.8.2.c.2 ]



#### UNACCEPTABLE DISTURBED SOLDER

A disturbed solder joint is an indicator of improper process control.

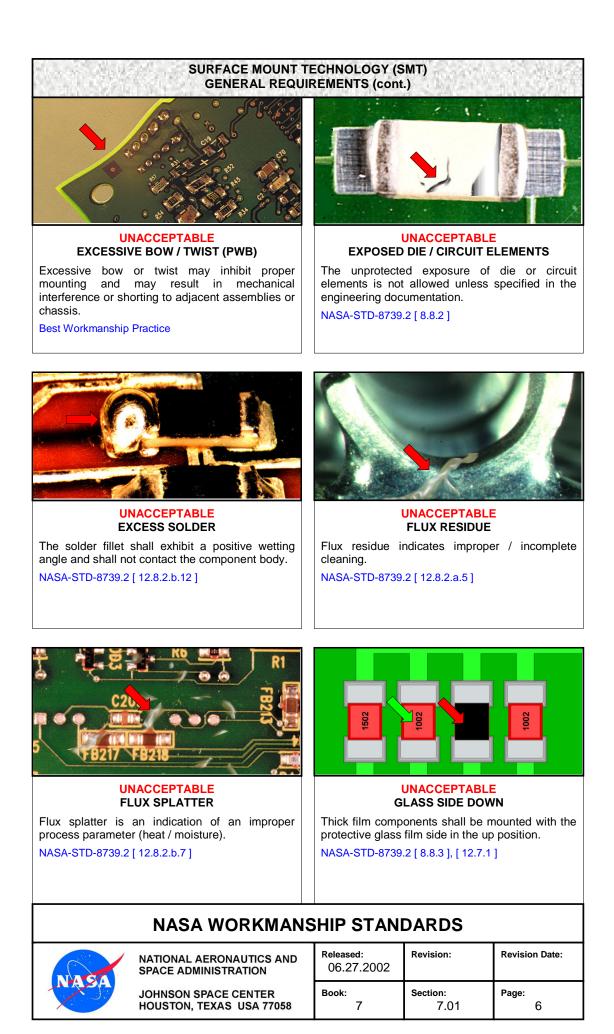
NASA-STD-8739.2 [ 12.8.2.b.3 ]

# NASA WORKMANSHIP STANDARDS



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NASA-STD-8739.2 [ 12.8.2.b.12 ]

NASA-STD-8739.2 [ 12.6.3.2 ], [ 12.8.2.a.3 ]

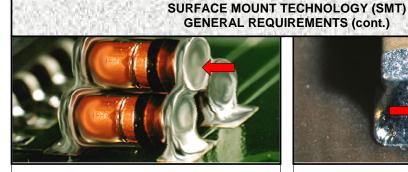
# NASA WORKMANSHIP STANDARDS



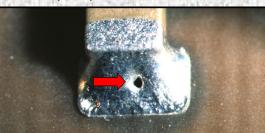
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UNACCEPTABLE PIGGYBACKED PARTS Piggybacking, or stacking, of parts not designed specifically for that configuration is prohibited. NASA-STD-8739.2 [ 8.7.4.e ], [ 12.6.2.a.1 ]



#### UNACCEPTABLE PINHOLE

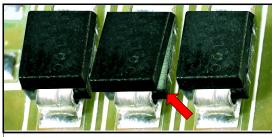
Pinholes are typically small holes in the solder surface, leading to a void of indeterminate size within the solder termination.

NASA-STD-8739.2 [ 12.8.2.b.5 ]



UNACCEPTABLE POOR WETTING

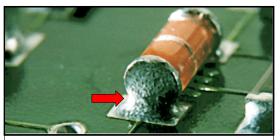
Poor wetting is an indicator of poor solderability, improper flux, or contamination. NASA-STD-8739.2 [ 12.8.2.b.4 ]



UNACCEPTABLE POPCORNING

Popcorning is caused by the release of pressure entrapped in the component body during the soldering process. The effect can be relatively minor (body distortion), or destructive (seal breach or delidding).

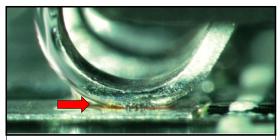
NASA-STD-8739.2 [ 12.7.2.b ], [ 12.8.2.a.3 ]



UNACCEPTABLE POROUS SOLDER

Porous solder is an indication of improper process control (i.e.: excessive flux, inadequate dwell time).

NASA-STD-8739.2 [ 12.8.2.b.17 ]



#### UNACCEPTABLE ROSIN SOLDER JOINT

A rosin solder joint is an indication of improper process control (i.e.: excessive flux, inadequate dwell time).

NASA-STD-8739.2 [ 12.8.2.b.8 ]

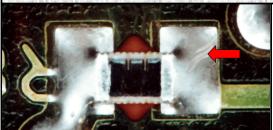
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## SURFACE MOUNT TECHNOLOGY (SMT) GENERAL REQUIREMENTS (cont.)



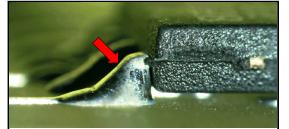
UNACCEPTABLE SCRATCHES (SOLDER FILLET) Scratches in the solder are prohibited. NASA-STD-8739.2 [ 12.8.2.b.14 ]



#### UNACCEPTABLE SOLDER BALLS / SOLDER FINES

Solder balls or fines are an indication of improper process control (inadequate preheat), and/or the use of outdated solder / flux.

NASA-STD-8739.2 [ 12.8.2.b.19 ]



UNACCEPTABLE SOLDER IN STRESS RELIEF BEND

Solder shall not extend into the stress relief bend of any leaded part. In this example, the solder is also in contact with the part body and the body seal.

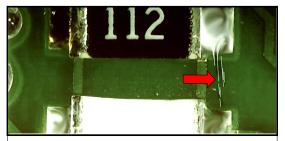
NASA-STD-8739.2 [ 12.8.2.b.16 ]



UNACCEPTABLE SOLDER PEAKS, ICICLES, SHARP EDGES

Solder peaks, icicles, and/or sharp edges are an indicator of an improper process parameter and are a reliability and short-circuit concern.

NASA-STD-8739.2 [ 12.8.2.c.4 ]



UNACCEPTABLE SOLDER SLIVERS

Solder slivers are an indication of improper process control, and are a reliability and short-circuit concern.

NASA-STD-8739.2 [ 12.8.2.b.20 ]



#### UNACCEPTABLE SOLDER WEBBING

Webbing is an indication of improper process control, and is a reliability and short-circuit concern.

NASA-STD-8739.2 [ 12.8.2.b.18 ]

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NASA-STD-8739.2	12.8.2.D.21

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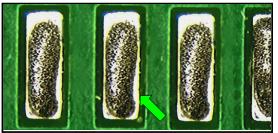
SURFACE MOUNT TECHNOLOGY (SMT) SOLDER PASTE / SOLDER PREFORM APPLICATION



#### SOLDER PASTE / PREFORM APPLICATION

Solder paste is a mixture of solder alloy particles, flux, and other materials, for use in reflow soldering (oven, vapor phase, or infrared) of surface mount technology (SMT) components.

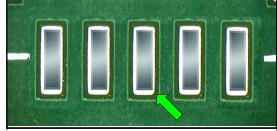
Solder preforms are generally made from solder alloy wire or stamped from solder alloy sheet material, and formed into specific shapes (typically toroids, washers, or donuts) for use in reflow soldering (oven, vapor phase, or infrared) of plated through hole (PTH) components and some surface mount technology (SMT) components.



ACCEPTABLE SOLDER PASTE APPLICATION

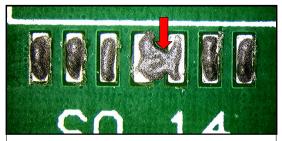
The paste is applied in a uniform thickness, with proper alignment and placement. There is no bridging, bubbles, crusting, separation, or smearing.

NASA-STD-8739.2 [ 8.2 ], [ 8.6 ]



ACCEPTABLE SOLDER PREFORM APPLICATION The preforms are applied with proper alignment and placement.

Best Workmanship Practice



UNACCEPTABLE BRIDGING

Bridging of lands is an indicator of improper screen alignment / paste application. NASA-STD-8739.2 [ 8.7.4.f ], [ 12.6.1.a.1 ]



UNACCEPTABLE BUBBLES

Bubbles in the paste are typically caused by overmixing, and can affect solder joint formation. NASA-STD-8739.2 [ 12.6.1.a.3 ]

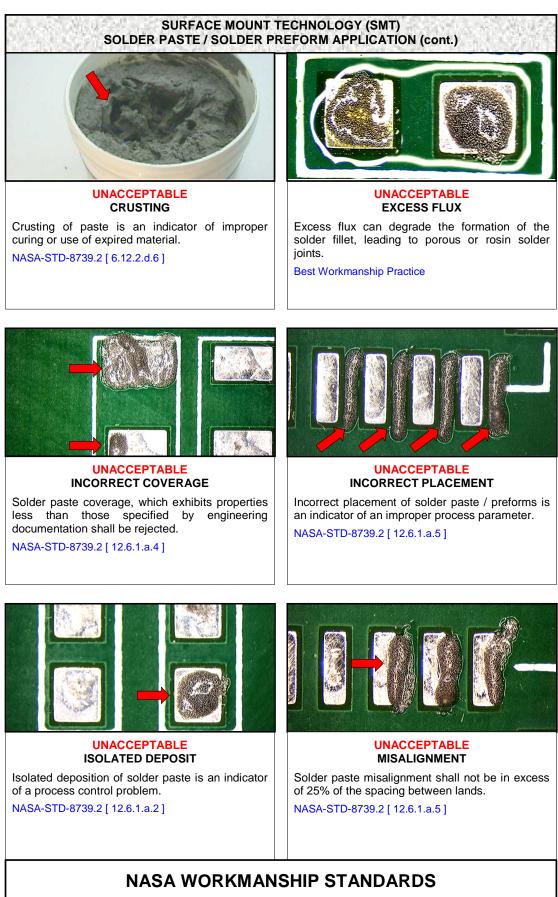
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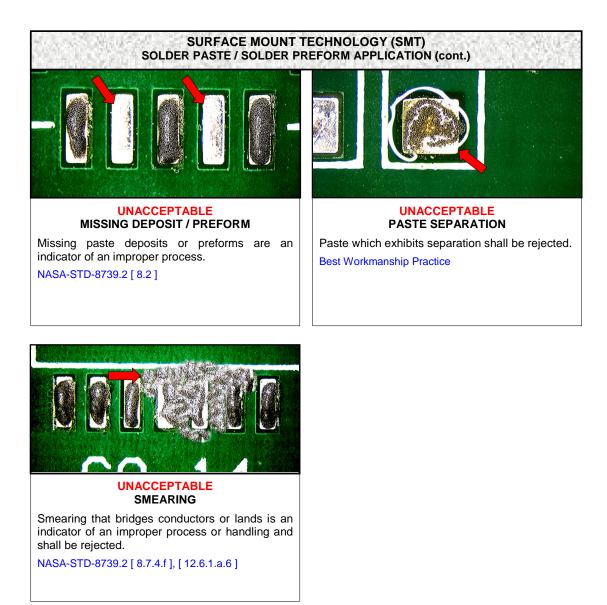
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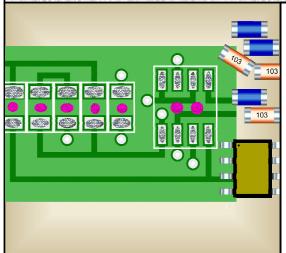
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## SURFACE MOUNT TECHNOLOGY (SMT) SOLDER PASTE / SOLDER PREFORM APPLICATION (cont.)

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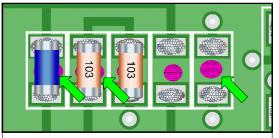
## SURFACE MOUNT TECHNOLOGY (SMT) ADHESIVE APPLICATION



## ADHESIVE APPLICATION

Adhesives are frequently used to temporarily hold SMT components in position prior to soldering. Once the soldering operations are completed, the adhesive residue is removed during the cleaning process.

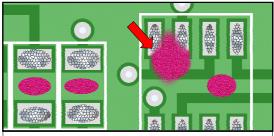
The application of adhesive should be controlled to ensure proper placement, amount, and cure. Excess adhesive, improper placement, or incomplete cure can contaminate solder paste and solderable surfaces, interfere with proper component alignment, and impact cleanability.



PREFERRED

The deposition of adhesive is consistent, properly placed, and repeatable. Dots are centered under the part body, equidistant between the land pattern areas.

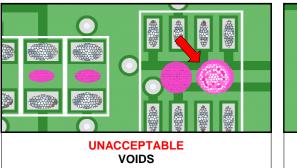
NASA-STD-8739.2 [ 8.9 ], [ 8.10.2 ]



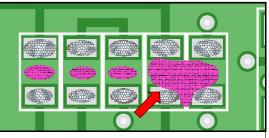
UNACCEPTABLE ADHESIVE ON LEADS / SOLDER PADS

Adhesive deposits on part leads and/or solder pads interfere with proper placement, component retention, and solderability.

NASA-STD-8739.2 [ 8.10.3 ]



Bubbles and voids in the adhesive reduce the deposit's cross-section and retention properties. NASA-STD-8739.2 [ 8.9 ], [ 8.10.1 ]



#### UNACCEPTABLE EXCESSIVE ADHESIVE

Excessive adhesive interferes with proper placement, component retention, and solderability.

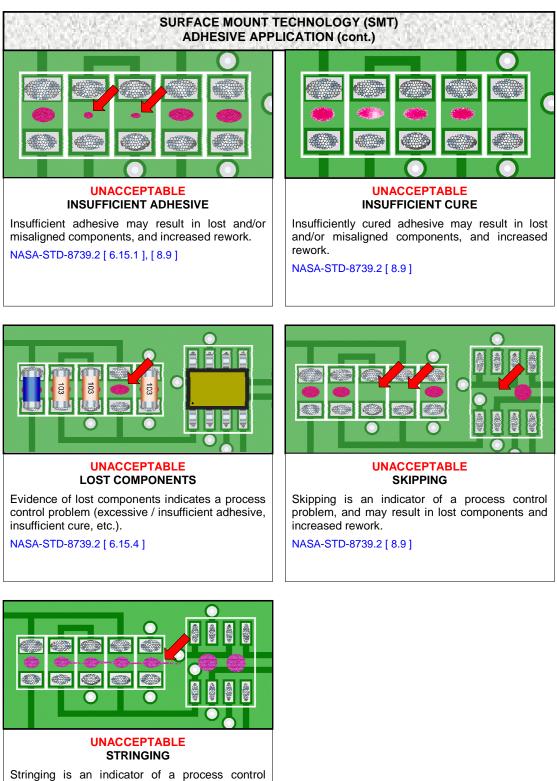
NASA-STD-8739.2 [ 8.10.3 ]

# NASA WORKMANSHIP STANDARDS



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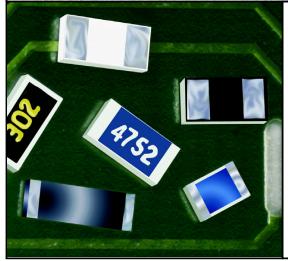


problem, is a contaminant, and affects overall solderability.

NASA-STD-8739.2 [ 6.15.1 ]

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#### SURFACE MOUNT TECHNOLOGY (SMT) CHIP COMPONENTS / BOTTOM-ONLY TERMINATIONS



#### CHIP COMPONENTS BOTTOM-ONLY TERMINATIONS

The mechanical properties of the solder joints of bottom-only terminations are slightly reduced from those of 1-3-5 chip components, as only the metallized termination pads on the underside of the component are available for mechanical and electrical attachment to the printed wiring board. The bottom only termination presents some difficulty during visual inspection, as very little of the actual termination is exposed or visible.

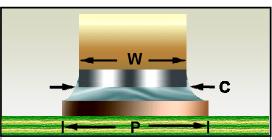
See Section 7.01 "Surface Mount Soldering, General Requirements", for common accept / reject criteria.



PREFERRED

The component is properly centered between the lands and exhibits acceptable solder thickness and tilt. No mechanical or heat damage is evident.

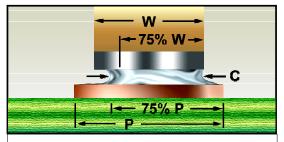
NASA-STD-8739.2 [ 12.9.1 ]



PREFERRED END JOINT WIDTH (C)

The width of the end joint is equal to the width of the component (W), and extends to the width of the land (P).

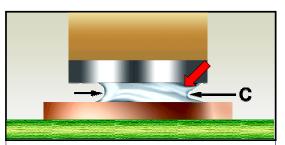
**Best Workmanship Practice** 



#### ACCEPTABLE END JOINT WIDTH (C)

End joint width shall not be less than 75% of the component termination width (W) or less than 75% of the land width (P).

Best Workmanship Practice



#### UNACCEPTABLE INSUFFICIENT END JOINT WIDTH (C)

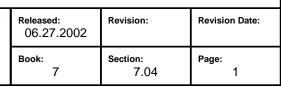
An end joint of insufficient width indicates that there may be solderability problems that may adversely impact the long-term reliability and integrity of the solder termination

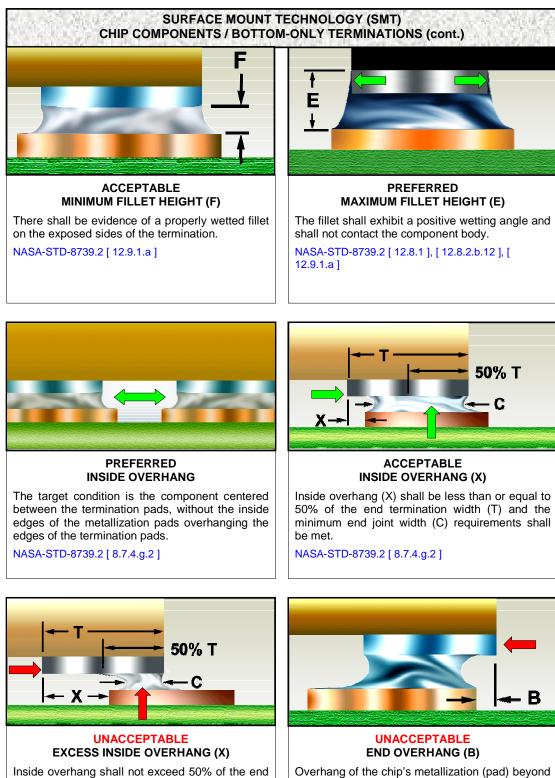
**Best Workmanship Practice** 

# NASA WORKMANSHIP STANDARDS



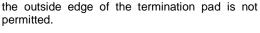
NATIONAL AERONAUTICS AND SPACE ADMINISTRATION





Inside overhang shall not exceed 50% of the end termination width (t) and the minimum end joint width (C) requirements shall be met.

NASA-STD-8739.2 [ 12.6.2.a.2 ]



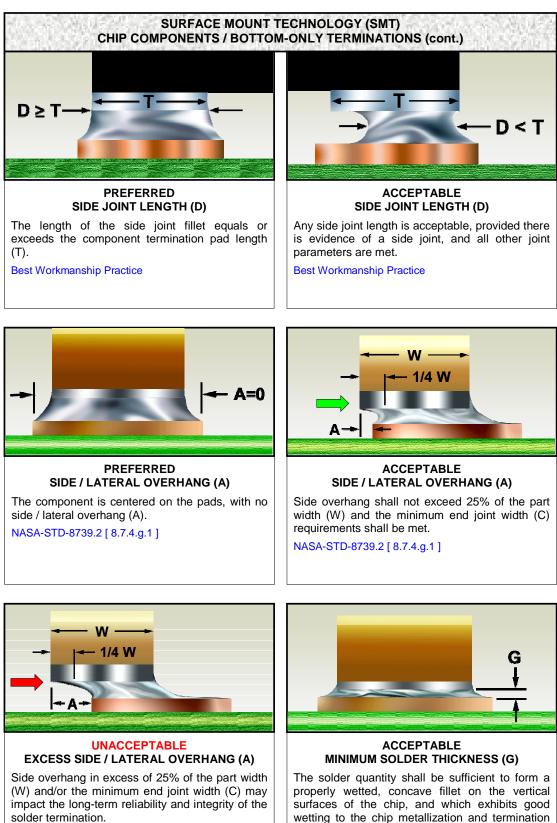
**Best Workmanship Practice** 

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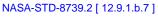


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solder termination.





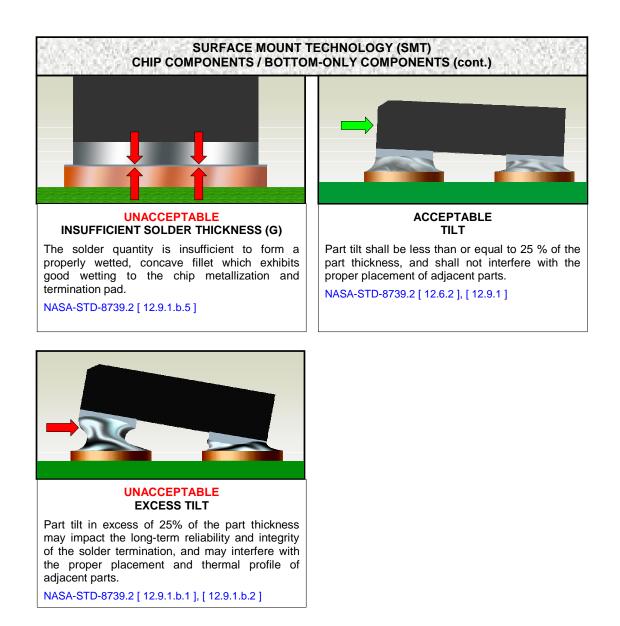
# NASA WORKMANSHIP STANDARDS

pad.



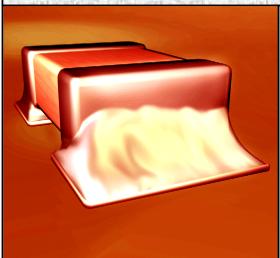
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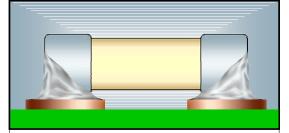
## SURFACE MOUNT TECHNOLOGY (SMT) CHIP COMPONENTS – RECTANGULAR / SQUARE END TERMINATIONS



#### <u>CHIP COMPONENTS - RECTANGULAR /</u> <u>SQUARE END TERMINATIONS</u> (<u>1-3-5 SIDED</u>)

Rectangular and square-end chip components are characterized by their metallized termination cap design. Unlike their bottomonly termination cousins, the standard chip may be supplied with metallization on the end surfaces (1-sided); the bottom, end, and top surfaces (3-sided); or, the bottom, end, top, and sides of the termination cap (5-sided).

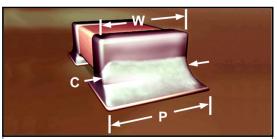
See Section 7.01 "Surface Mount Soldering, General Requirements", for common accept / reject criteria.



PREFERRED

Device is centered on the termination pads, with proper end overlap and no inside overhang. The solder termination exhibits a full concave fillet on the vertical terminal faces, with evidence of good wetting to the chip metallization and extends to the periphery of the land.

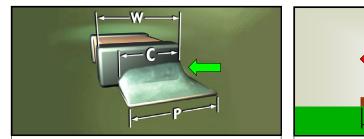
NASA-STD-8739.2 [ 8.7.4.g ], [ 12.9.1.a ]



PREFERRED END JOINT WIDTH (C)

The End Joint Width (C) shall be equal to or greater than the component width (W) or width of the land (P), whichever is less.

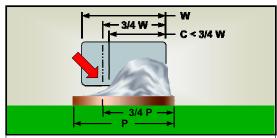
**Best Workmanship Practice** 



ACCEPTABLE END JOINT WIDTH (C)

The End Joint Width (C) shall be 75% of the component width (W) or width of the land (P), whichever is less.

**Best Workmanship Practice** 



#### UNACCEPTABLE END JOINT WIDTH (C)

The width of the end joint shall not be less than 75% of the component termination width (W), or 75% of the land width (P), whichever is less.

**Best Workmanship Practice** 

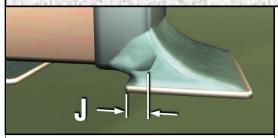
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#### SURFACE MOUNT TECHNOLOGY (SMT) CHIP COMPONENTS – RECTANGULAR / SQUARE END TERMINATIONS (cont.)



#### ACCEPTABLE END OVERLAP (J)

There shall be end overlap (J) between the component termination cap and the termination pad. Preferentially, the end overlap (J) should equal the termination cap length, with the component centered between the pads.

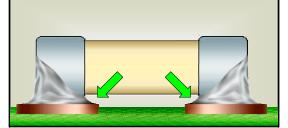




#### UNACCEPTABLE INSUFFICIENT END OVERLAP (J)

There shall be end overlap (J) between the component termination cap and the termination pad to ensure the proper formation of the solder fillet.

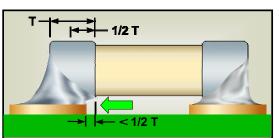
Best Workmanship Practice



PREFERRED INSIDE OVERHANG

The target condition is the component centered between the termination pads, without the inside edge(s) of the metallization pads overhanging the termination pad(s).

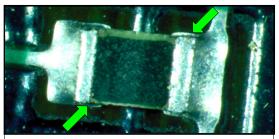
NASA-STD-8739.2 [ 8.7.4.g.2 ]



ACCEPTABLE INSIDE OVERHANG

Inside overhang of the chip's metallization pad shall be less than or equal to 50% of the end termination width (t) and the minimum end joint width (C) requirements shall be met.

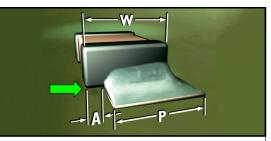
NASA-STD-8739.2 [ 8.7.4.g.2 ]



PREFERRED LATERAL / SIDE OVERHANG (A)

The target condition is no lateral / side overhang, with the component centered on the land.

NASA-STD-8739.2 [ 8.7.4.g.1 ]



#### ACCEPTABLE LATERAL / SIDE OVERHANG (A)

Lateral / side overhang (A) shall not exceed 25% of the component termination area (W) or land width (P), whichever is smaller.

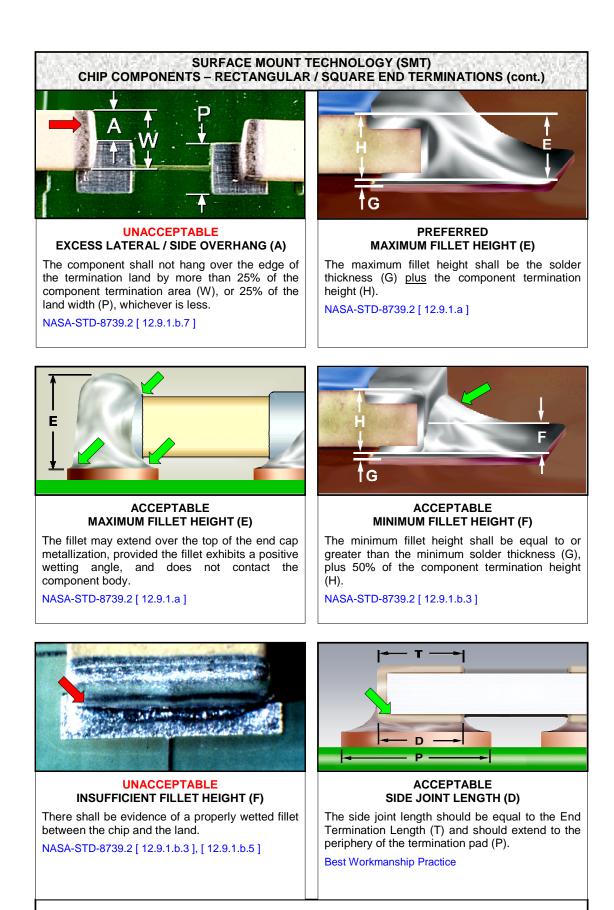
NASA-STD-8739.2 [ 8.7.4.g.1 ]

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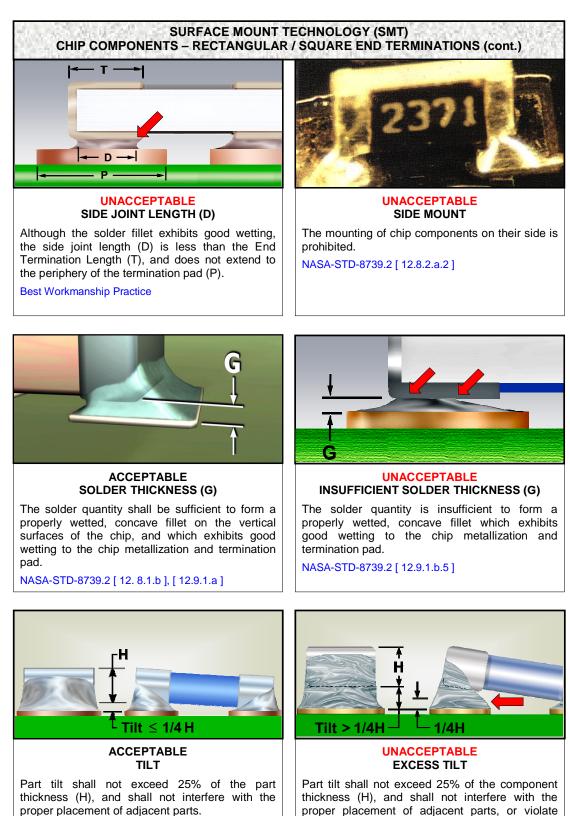


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NASA-STD-8739.2 [ 12.6.2.a.3 ]

minimum electrical spacing requirements. NASA-STD-8739.2 [12.9.1.b.1], [12.9.1.b.2]

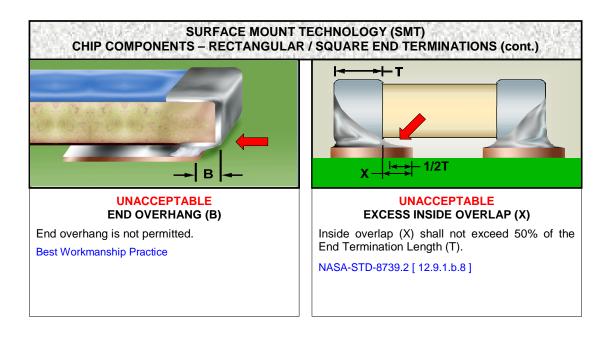
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## SURFACE MOUNT TECHNOLOGY (SMT) CHIP COMPONENTS – RECTANGULAR / SQUARE END TERMINATIONS (cont.)

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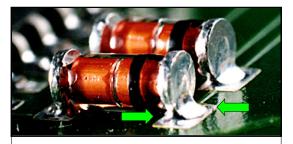
SURFACE MOUNT TECHNOLOGY (SMT) METALLIZED ELECTRODE FACE - MELF



#### METALLIZED ELECTRODE FACE (MELF)

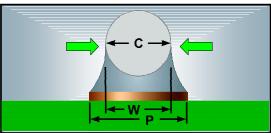
The Metallized Electrode Face (MELF) termination is characterized as a cylindrical package with metallized end caps, and is commonly used for the packaging of discrete diodes, capacitors, and resistors. Since they are cylindrical, the MELF does not have to be placed with the resistive elements facing away from the board surface, as is the case with rectangular chip packages. Like their throughhole axial cousins, MELFs are typically color-coded for value.

See Section 7.01 "Surface Mount Soldering, General Requirements", for common accept / reject criteria.



**PREFERRED** The termination exhibits a concave fillet on the terminal faces, with evidence of good wetting to the metallization and the periphery of the land.

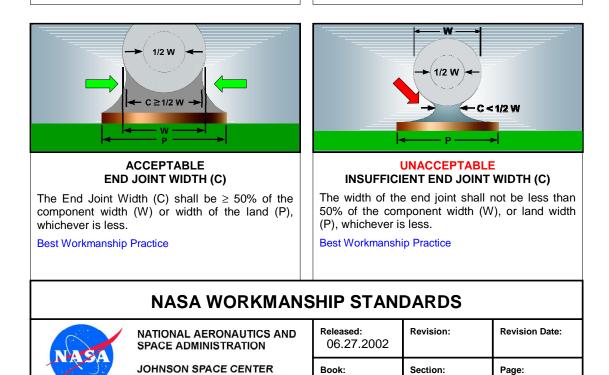
NASA-STD-8739.2 [ 8.7.4 ], [ 12.9.6 ]



PREFERRED END JOINT WIDTH (C)

The End Joint Width (C) shall be equal to or greater than the component width (W) or width of the land (P), whichever is less.

Best Workmanship Practice



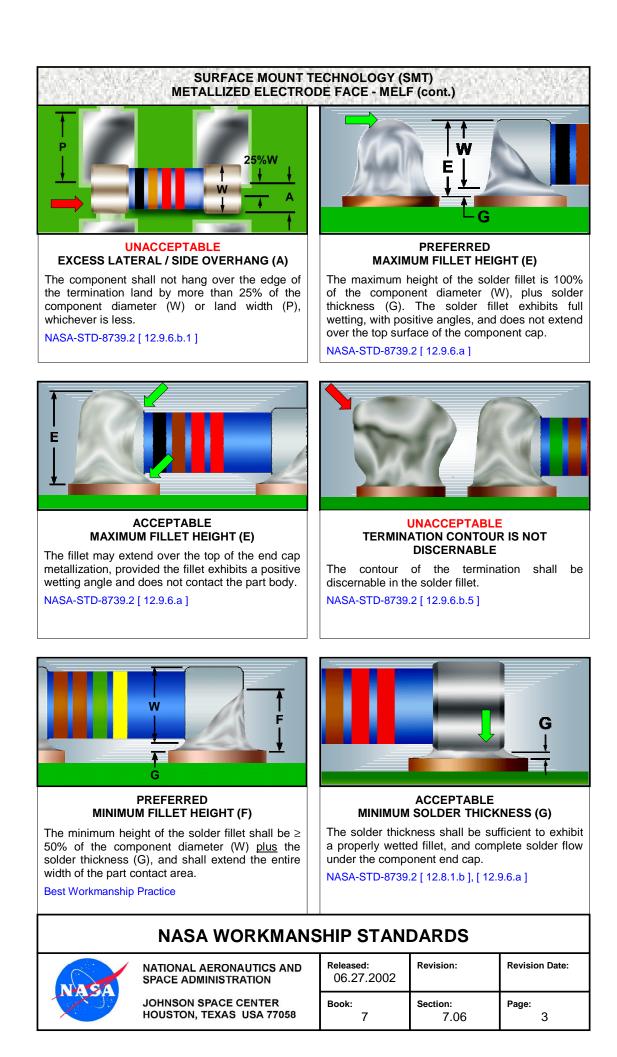
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HOUSTON, TEXAS USA 77058







### SURFACE MOUNT TECHNOLOGY (SMT) GULL-WING / "L" LEADED PACKAGES



### GULL-WING / "L" LEADED PACKAGES

Gull-Wing IC package leads are formed in a profile very similar to the outline of a seagull's wings. The Gull-Wing is considered one of the most reliable terminations for fine-pitch, high pin-count packages.

"L" leaded IC packages have leads formed in a configuration very similar to the outline of the letter "L". The leads are shorter (length and height) than the "Gull-Wing" and tend to be much stiffer (hardened).

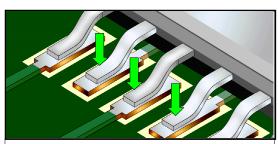
See Section 7.01 "Surface Mount Soldering, General Requirements", for common accept / reject criteria.



PREFERRED

The part is properly oriented to the land pattern, with each lead centered across the width of the land. Leads are planar, fillets are shiny and concave, and heel fillet is evident.

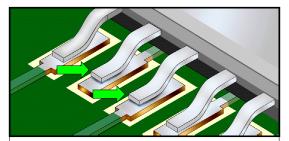
NASA-STD-8739.2 [ 8.7.4.h ], [ 12.6.2 ], [ 12.8 ]



PREFERRED COPLANARITY

The preferred planarity of the lead to the land pattern area is with the foot parallel and in full contact with the pad.

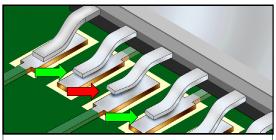
NASA-STD-8739.2 [7.1]



ACCEPTABLE COPLANARITY

The maximum acceptable non-planarity between any portion of the lead foot and the pad shall not exceed 0.26 mm (0.010").

NASA-STD-8739.2 [ 7.1 ], [ 12.9.2.b.3 ]



UNACCEPTABLE IMPROPER COPLANARITY

The maximum acceptable non-planarity between any portion of the lead foot and the pad shall not exceed  $0.26 \text{ mm} (0.010^{\circ})$ .

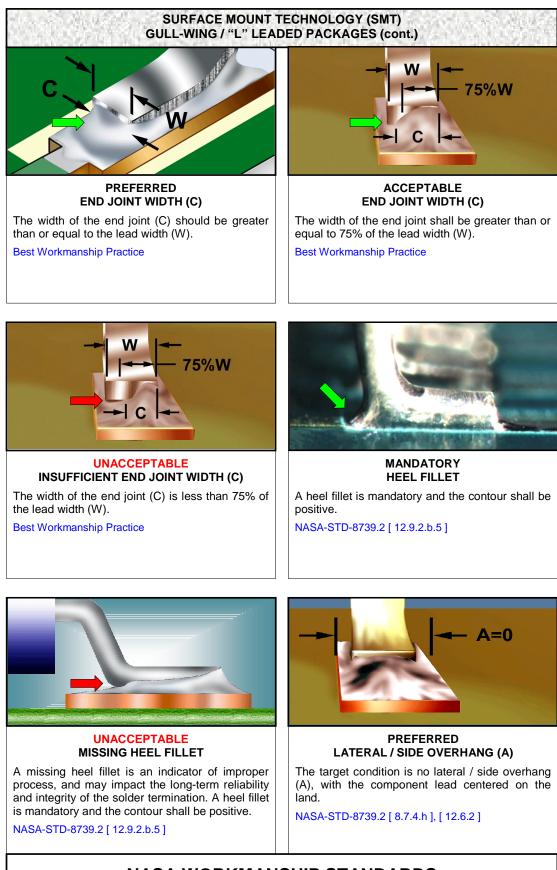
NASA-STD-8739.2 [ 12.9.2.b.3 ]

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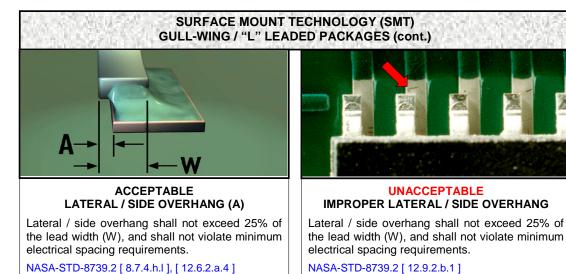


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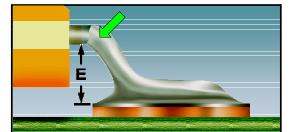


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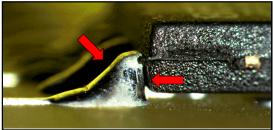
NASA-STD-8739.2 [ 12.9.2.b.1 ]



ACCEPTABLE **MAXIMUM HEEL FILLET HEIGHT (E)** 

Solder may extend through the stress relief bend, but must not contact the lead seal. Solder shall exhibit a concave fillet and the lead contour shall be visible.

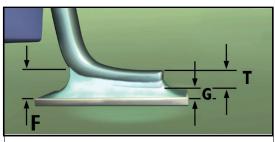
NASA-STD-8739.2 [ 12.8.1.b ], [ 12.8.2.b.16 ]



**UNACCEPTABLE EXCESS SOLDER** 

The lead contour is not discernable; the solder extends through the stress-relief bends; and, the solder contacts the component body and seal.

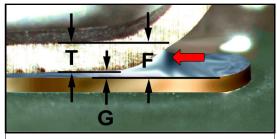
NASA-STD-8739.2 [ 12.8.2.b.12 ], [ 12.8.2.b.16 ], [12.9.2.a.2]



ACCEPTABLE **MINIMUM HEEL FILLET HEIGHT (F)** 

The fillet height shall be equal to or greater than the minimum solder thickness (G), plus one (1) lead thickness (T).

**Best Workmanship Practice** 



**UNACCEPTABLE INSUFFICIENT HEEL FILLET HEIGHT (F)** 

The heel fillet height is less than the minimum solder thickness (G), plus one (1) lead thickness (T). This may result in a weakened solder termination.

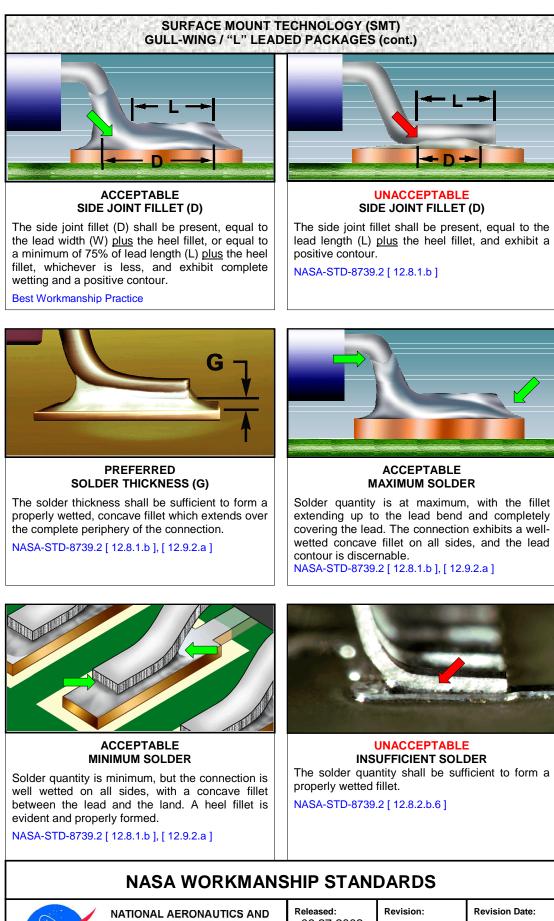
**Best Workmanship Practice** 

# NASA WORKMANSHIP STANDARDS



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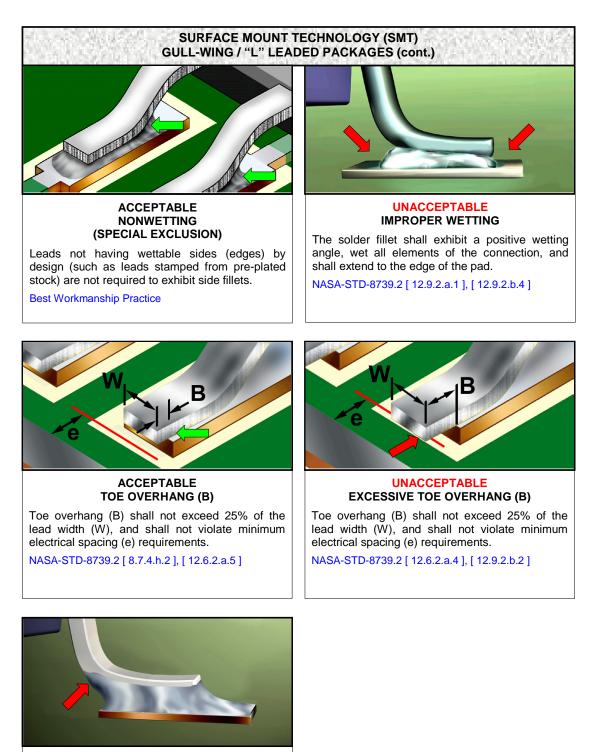
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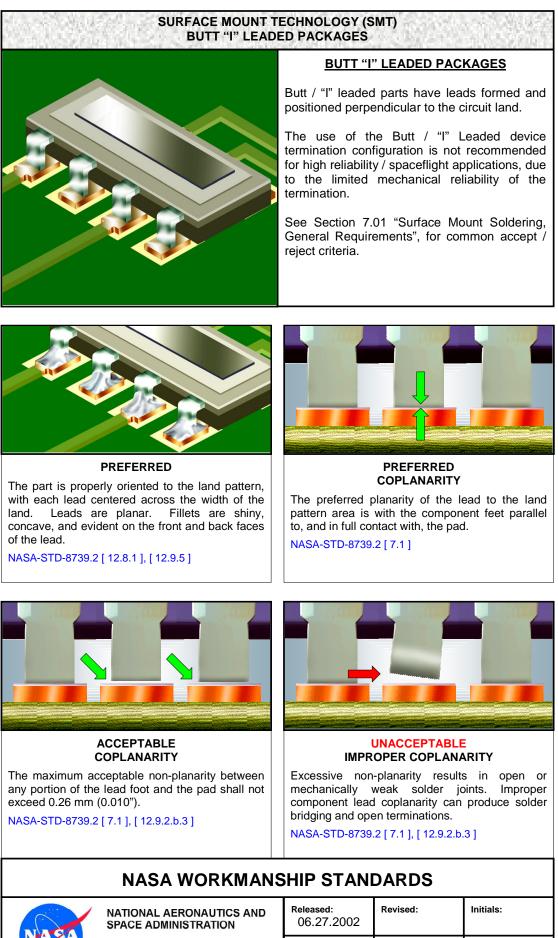
UNACCEPTABLE HEEL OVERHANG

Heel overhang is prohibited. Best Workmanship Practice

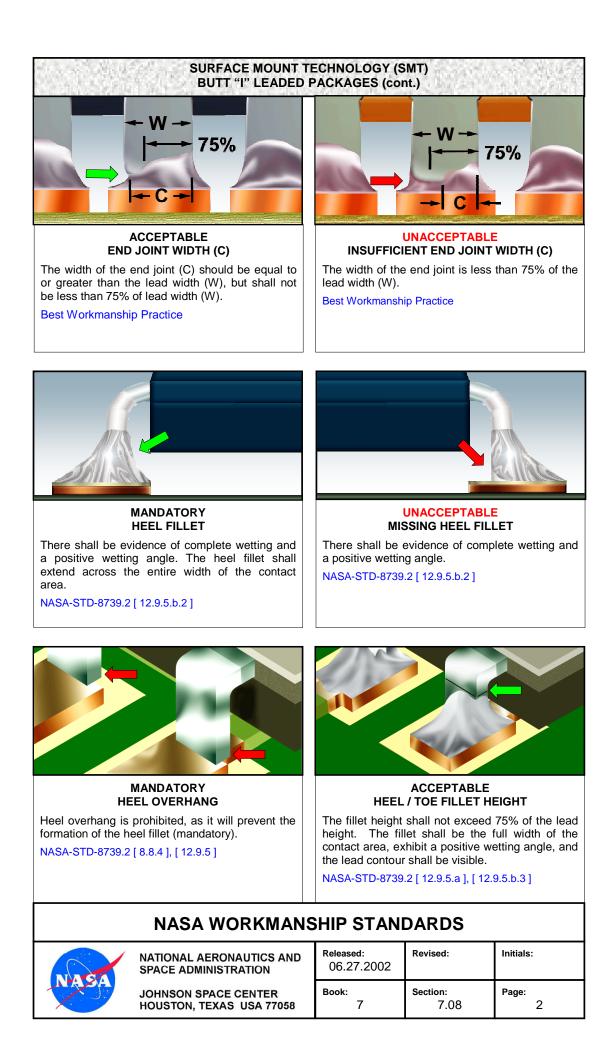
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### SURFACE MOUNT TECHNOLOGY (SMT) GULL-WING / "L" LEADED PACKAGES (cont.)

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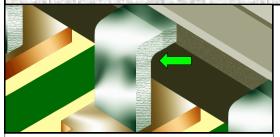


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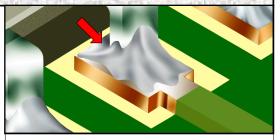


### SURFACE MOUNT TECHNOLOGY (SMT) BUTT "I" LEADED PACKAGES (cont.)



#### ACCEPTABLE NONWETTING (SPECIAL EXCLUSION)

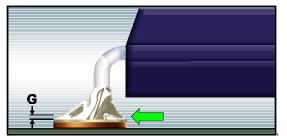
Leads not having wettable sides (edges) by design (such as leads stamped from pre-plated stock) are not required to exhibit side fillets. Best Workmanship Practice



#### UNACCEPTABLE IMPROPER WETTING

The solder fillet shall exhibit a positive wetting angle, wet all elements of the connection with smooth flow lines, and shall extend to the edge of the pad.

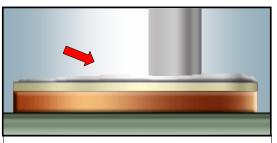
NASA-STD-8739.2 [ 12.9.5.a.1 ], [ 12.9.5.b.4 ]



PREFERRED SOLDER THICKNESS (G)

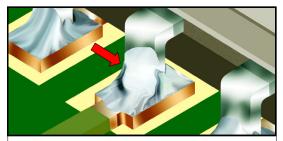
The solder thickness shall be sufficient to form a properly wetted, concave fillet that extends over the complete periphery of the connection.

NASA-STD-8739.2 [ 12.8.1.b ], [ 12.9.5.a ]



UNACCEPTABLE INSUFFICIENT SOLDER QUANTITY The solder quantity shall be sufficient to form a properly wetted, concave fillet.

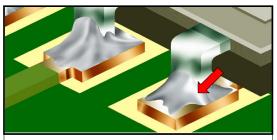
NASA-STD-8739.2 [ 12.9.5.a.1 ], [ 12.9.5.b.4 ]



UNACCEPTABLE EXCESS SOLDER

The solder fillet may be convex, but shall exhibit a positive wetting angle, and the lead contour shall be visible.

NASA-STD-8739.2 [ 12.9.5.a.2 ]



UNACCEPTABLE INCOMPLETE SOLDER FILLET

The solder fillet shall exhibit complete wetting and extend over the complete periphery of the connection.

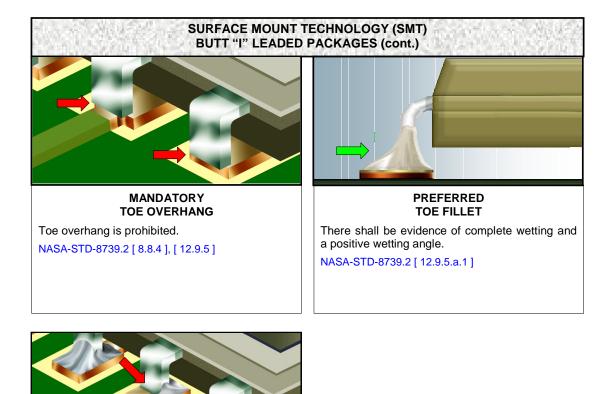
NASA-STD-8739.2 [ 12.9.5.b.4 ]

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UNACCEPTABLE MISSING TOE FILLET There shall be evidence of complete wetting and a positive wetting angle that extends over the complete periphery of the connection.

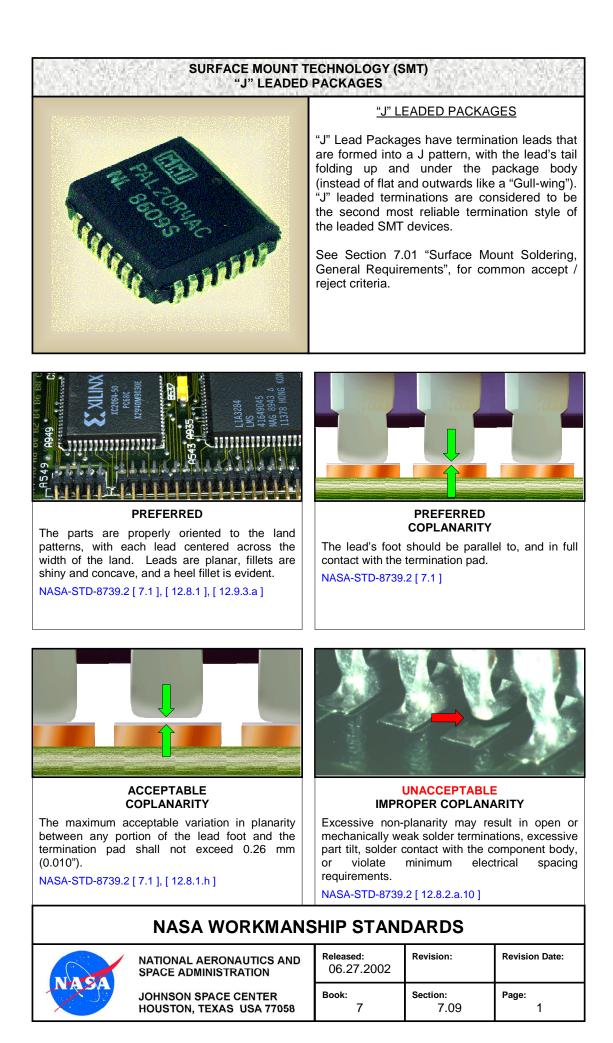
NASA-STD-8739.2 [ 12.8.1.b ], [ 12.9.5 ]

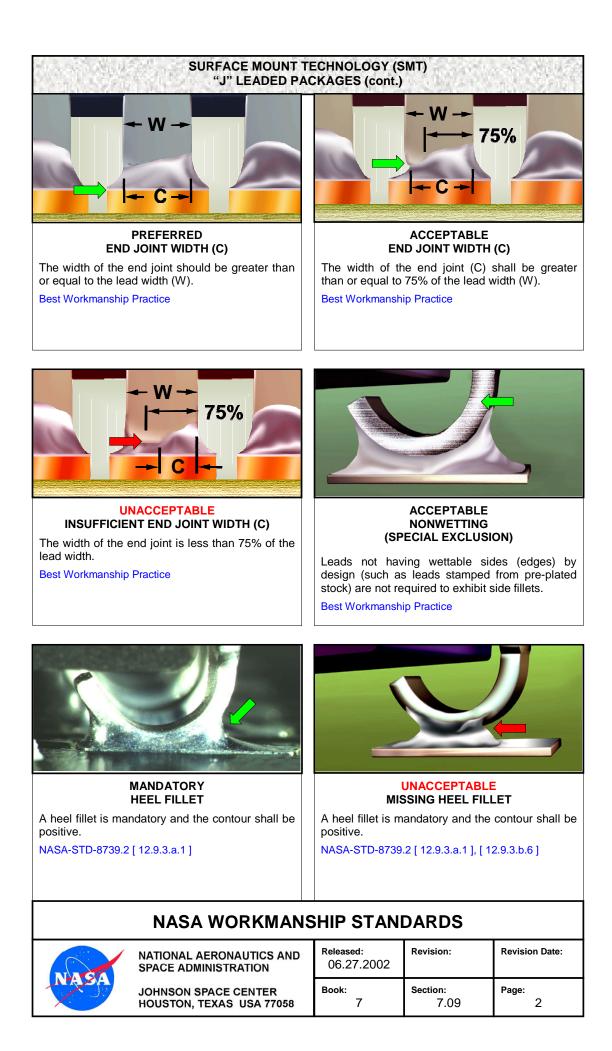
NASA WORKMANSHIP STANDARDS				
	NATIONAL AERONAUTICS AND SPACE ADMINISTRATION	Released: 06.27.2002	Revised:	Initials:
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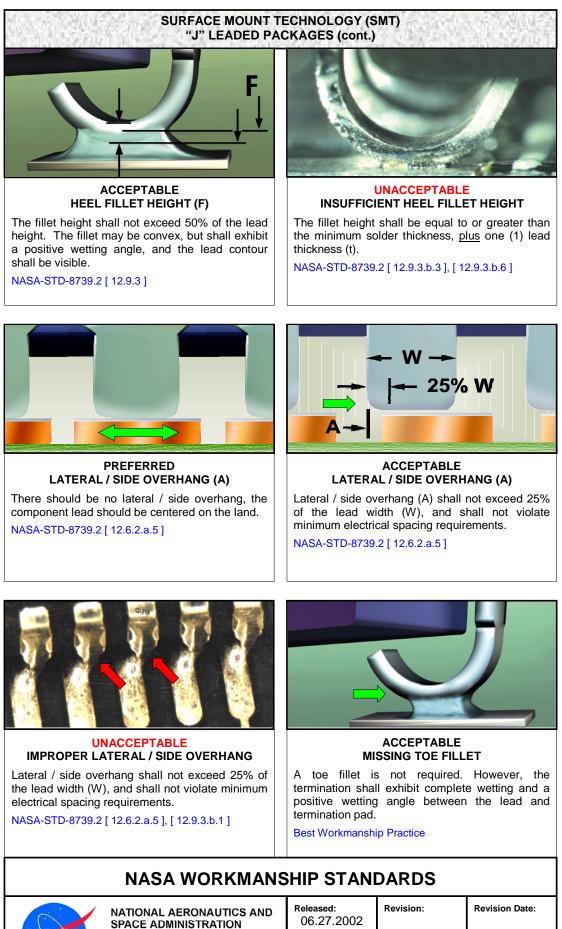
### SURFACE MOUNT TECHNOLOGY (SMT) BUTT "I" LEADED PACKAGES (cont.)

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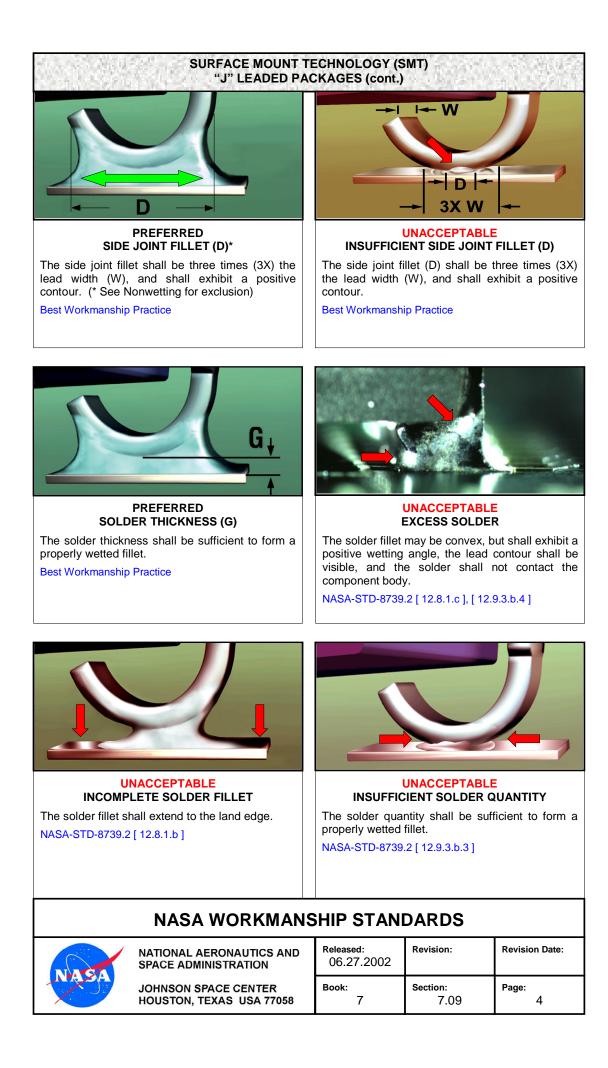
NASA WORKMANSHIP STANDARDS				
	NATIONAL AERONAUTICS AND SPACE ADMINISTRATION	Released: 06.27.2002	Revised:	Initials:
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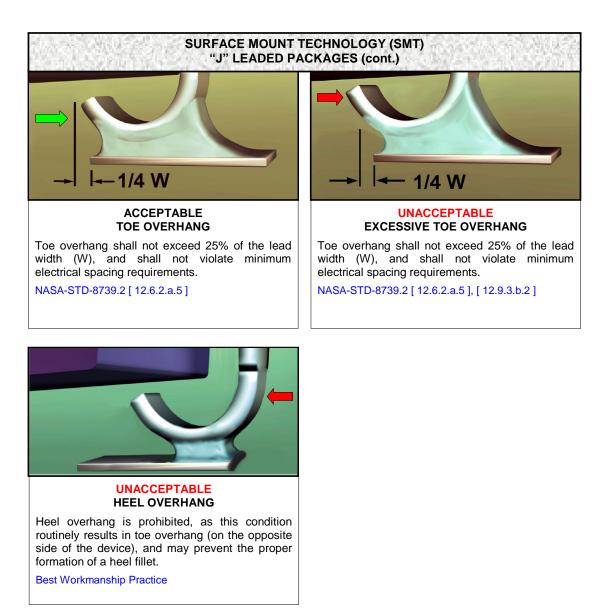






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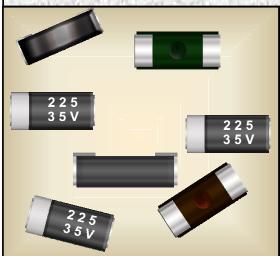
### SURFACE MOUNT TECHNOLOGY (SMT) "J" LEADED PACKAGES (cont.)

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NASA WORKMANSHIP STANDARDS				
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#### SURFACE MOUNT TECHNOLOGY (SMT) INWARD-FORMED "L" LEAD PACKAGES



**INWARD-FORMED "L" LEAD PACKAGES** 

Inward-formed "L" lead packages have leads that are formed in a configuration very similar to the outline of the letter "L", with the lead bent underneath the component package.

The "L" lead configuration is a shortened (both length and height) version of the "Gull-Wing" and the leads tend to be much stiffer, reducing co-planarity / planarity problems and offering a smaller "footprint".

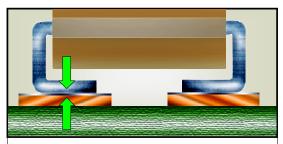
See Section 7.01 "Surface Mount Soldering, General Requirements", for common accept / reject criteria.



PREFERRED

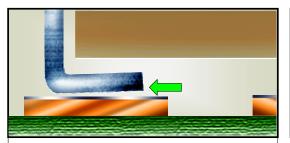
The parts are properly oriented to the land pattern, with each lead centered across the width of the land. Leads are planar and fully wetted, fillets are shiny and concave, and heel fillet is evident.

NASA-STD-8739.2 [ 8.7.4.j ], [ 12.6.2 ], [ 12.8 ], [ 12.9.4 ]



PREFERRED COPLANARITY

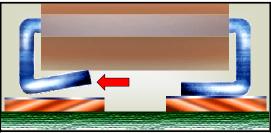
The lead's foot should be parallel to, and in full contact with the termination pad. NASA-STD-8739.2 [ 7.1 ]



ACCEPTABLE COPLANARITY

The maximum acceptable variation in planarity between any portion of the lead foot and the termination pad shall not exceed 0.26 mm (0.010").

NASA-STD-8739.2 [ 7.1 ], [ 12.8.1.h ]



#### UNACCEPTABLE IMPROPER COPLANARITY

Excessive non-planarity may result in open or mechanically weak solder terminations, excessive part tilt, solder contact with the component body, or violate minimum electrical spacing requirements.

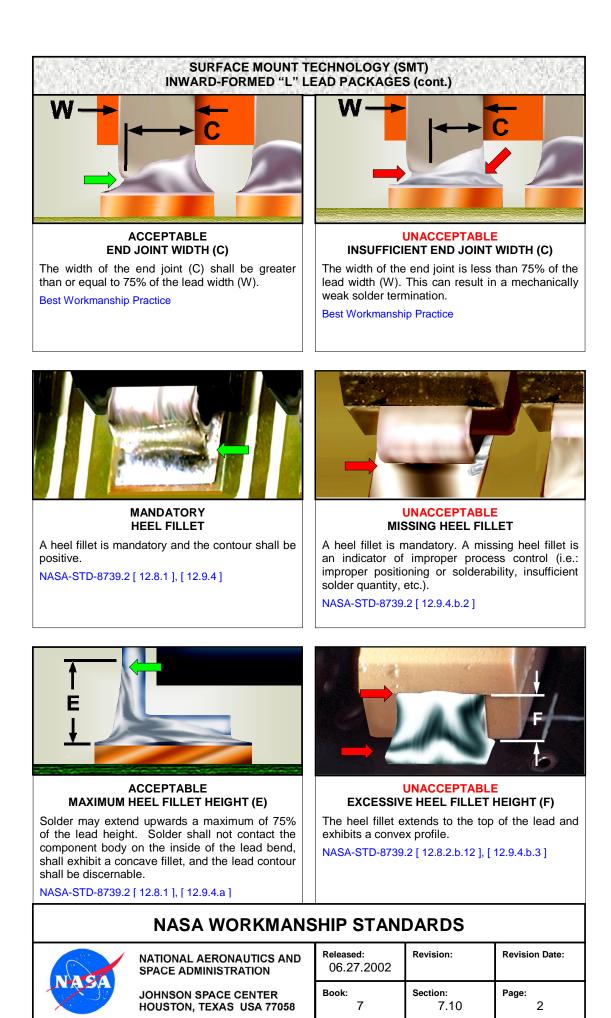
NASA-STD-8739.2 [ 12.8.2.a.10 ]

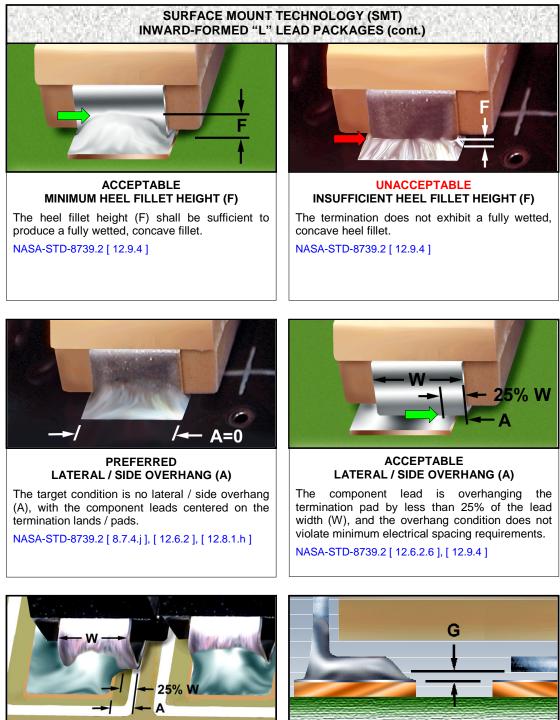
# NASA WORKMANSHIP STANDARDS



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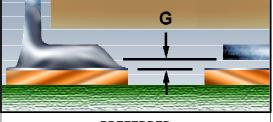




### **UNACCEPTABLE IMPROPER LATERAL / SIDE OVERHANG (A)**

Lateral / side overhang (A) shall not exceed 25% of the lead width (W), and shall not violate minimum electrical spacing requirements.

NASA-STD-8739.2 [ 12.9.4.b.1 ]



#### PREFERRED SOLDER THICKNESS (G)

The solder thickness shall be sufficient to form a properly wetted, concave fillet which extends over the complete periphery of the connection.

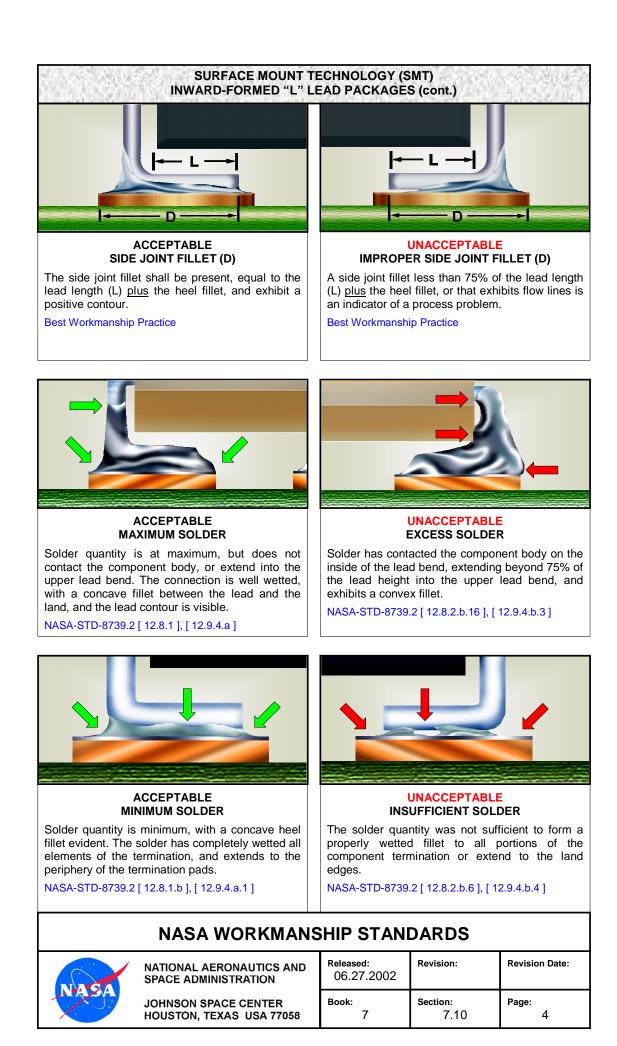
NASA-STD-8739.2 [ 12.8.1.b ], [ 12.9.4.a ]

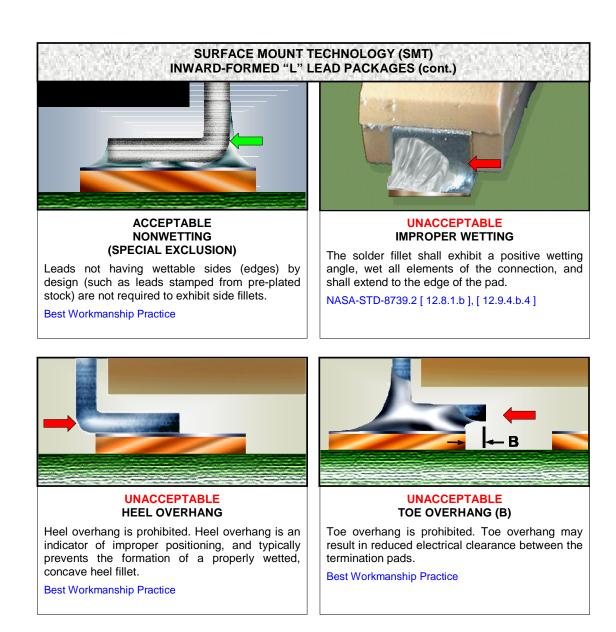
# NASA WORKMANSHIP STANDARDS



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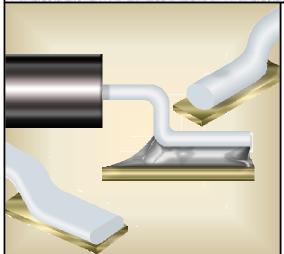


NASA WORKMANSHIP STANDARDS				
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### SURFACE MOUNT TECHNOLOGY (SMT) INWARD-FORMED "L" LEAD PACKAGES (cont.)

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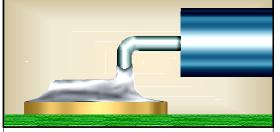
### SURFACE MOUNT TECHNOLOGY (SMT) LEADED PACKAGES / PARTS - ROUND OR FLATTENED "COINED" LEADS



#### LEADED PACKAGES / PARTS ROUND OR FLATTENED (COINED) LEADS

This category encompasses discrete component and integrated circuit packages, which share requirements of through hole soldering (NASA-STD-8739.3) and surface mount technology soldering (NASA-STD-8739.2). In typical applications, the leads are formed and bent in a pattern configuration similar to "Gull-wing" devices. Leads may be in the original round cross-section, or flattened to increase surface contact to the land / pad.

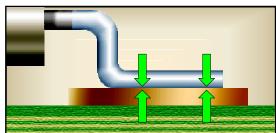
See Section 7.01 "Surface Mount Soldering, General Requirements", for common accept / reject criteria.



PREFERRED

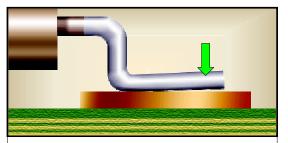
The part is properly oriented to the land pattern, with each lead centered across the width of the land. Lead feet are in full contact with the termination pad, fillets are shiny and concave, and heel fillet is evident.

NASA-STD-8739.2 [ 12.8.1 ], [ 12.9.2.a ] NASA-STD-8739.3 [ 13.6 ]



PREFERRED COPLANARITY

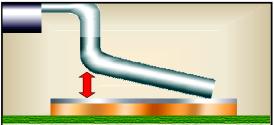
The lead's foot should be parallel to, and in full contact with the termination pad. NASA-STD-8739.2 [ 7.1 ] NASA-STD-8739.3 [ 8.5 ]



ACCEPTABLE COPLANARITY

The maximum acceptable variation in planarity between any portion of the lead foot and the termination pad shall not exceed 0.26 mm (0.010").

NASA-STD-8739.2 [ 7.1 ], [ 12.8.1.h ] NASA-STD-8739.3 [ 8.5.1 ]



#### UNACCEPTABLE IMPROPER COPLANARITY

Excessive non-planarity may result in open or mechanically weak solder terminations, excessive part tilt, or violate minimum electrical spacing requirements.

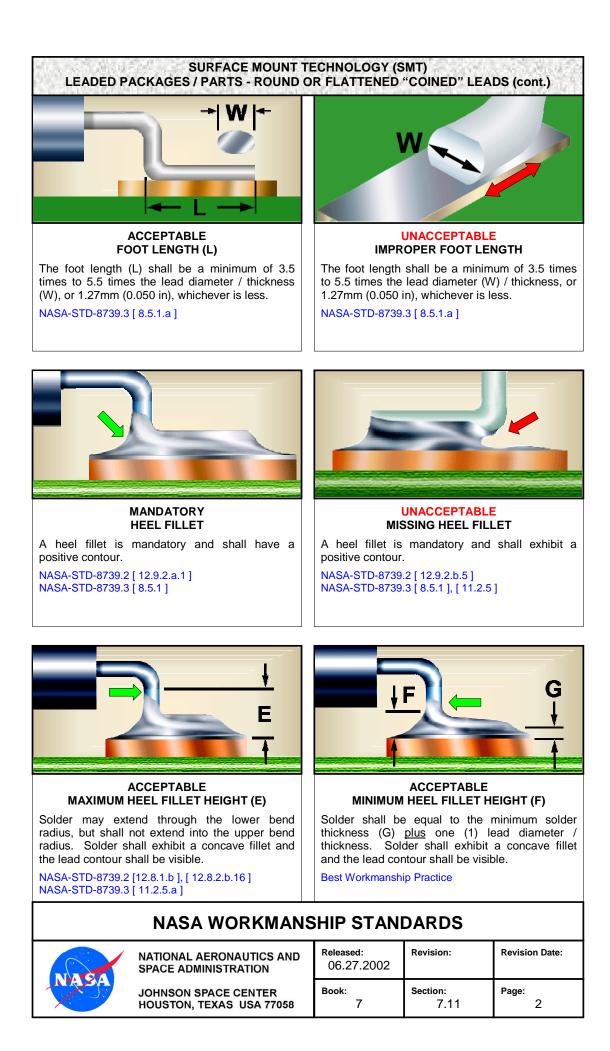
NASA-STD-8739.2 [ 12.8.2.a.10 ], [ 12.9.2.b.3 ] NASA-STD-8739.3 [ 13.6.2.a.5 ], [ 13.6.2.a.22 ]

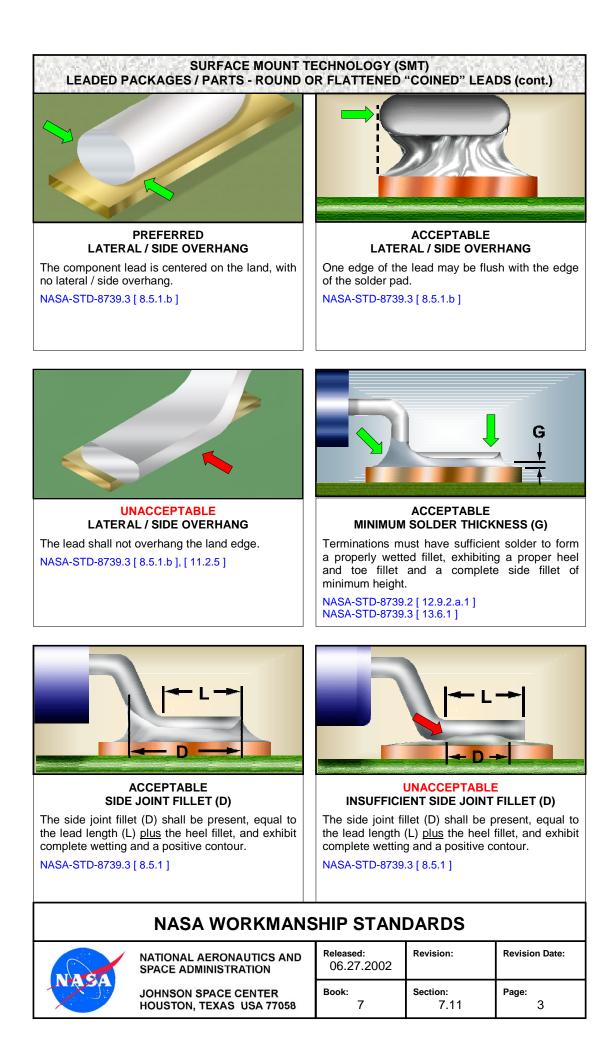
# NASA WORKMANSHIP STANDARDS

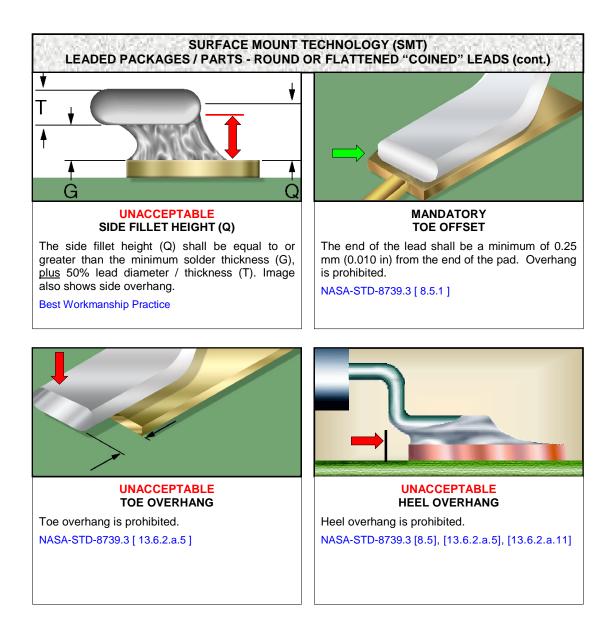


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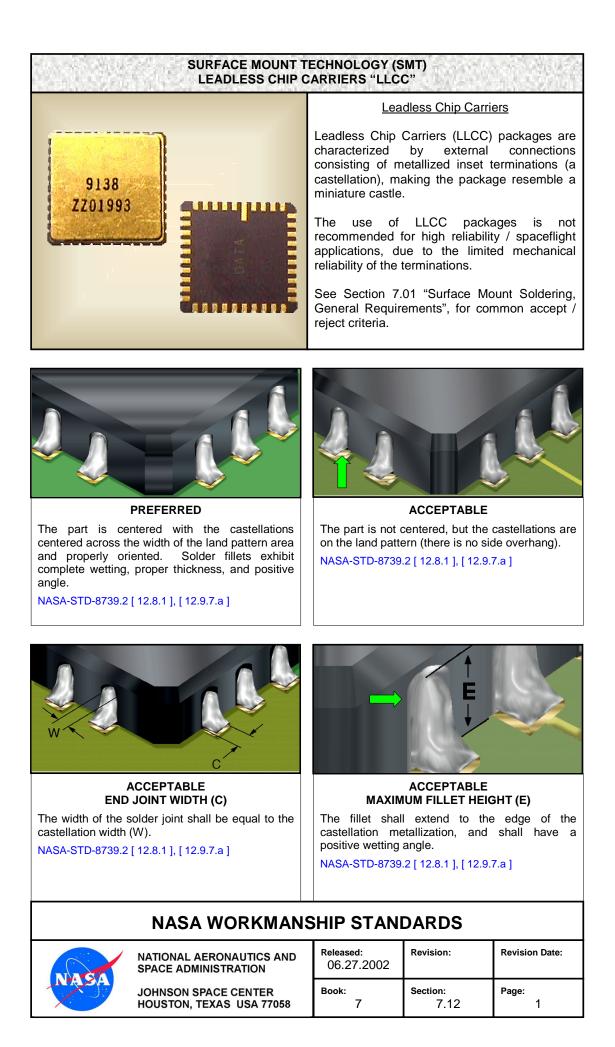
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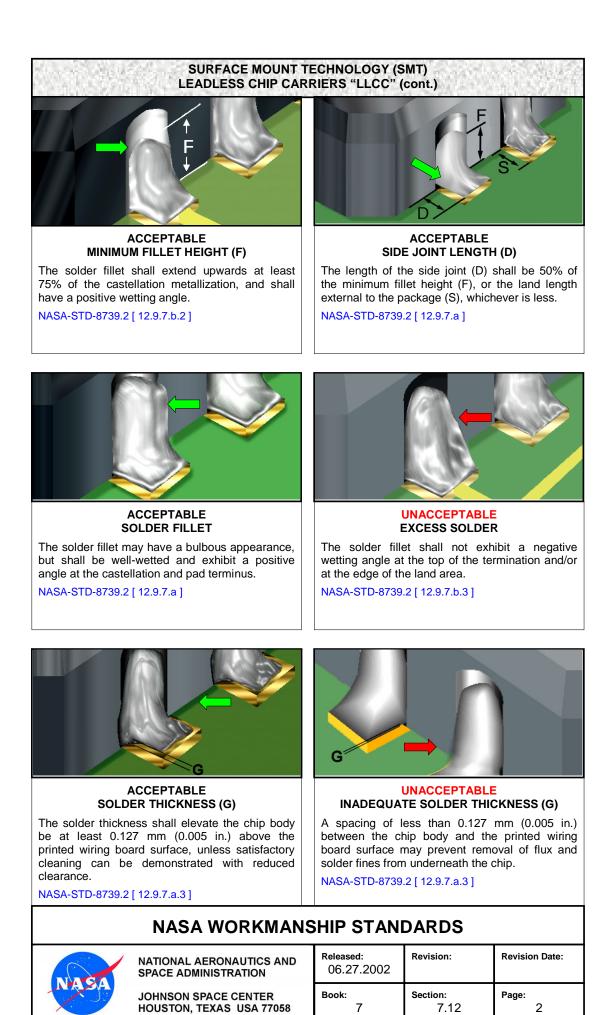


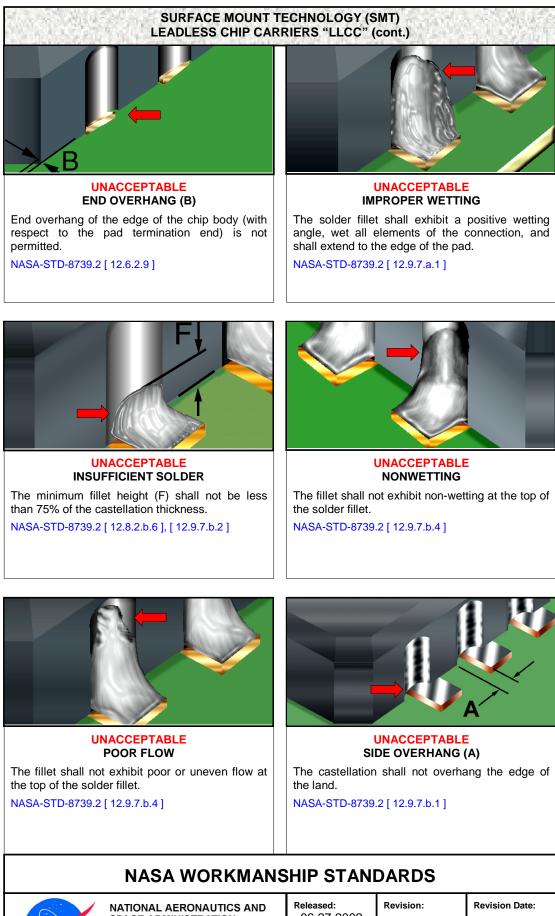




NASA WORKMANSHIP STANDARDS				
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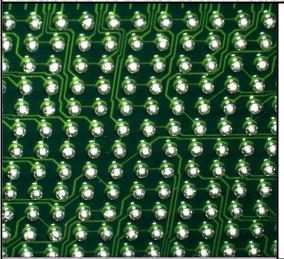
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### SURFACE MOUNT TECHNOLOGY (SMT) LEADLESS CHIP CARRIERS "LLCC" (cont.)

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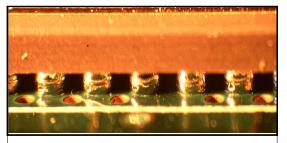
### SURFACE MOUNT TECHNOLOGY (SMT) BALL GRID ARRAY - BGA



### BALL GRID ARRAY - BGA

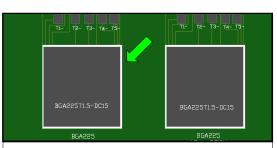
Ball Grid Array packages were designed to provide a device having high density input/output (I/O) array pattern interconnects, while minimizing device footprint and temperature coefficient (TC) problems. The array design features a low profile with shorter interconnections – resulting in superior electrical performance, speed, heat dissipation and noise reduction.

The placement of the interconnects on the bottom of the package limits visual inspection of the inner terminations, requiring the use of special microscopes or three-dimensional X-ray.



PREFERRED

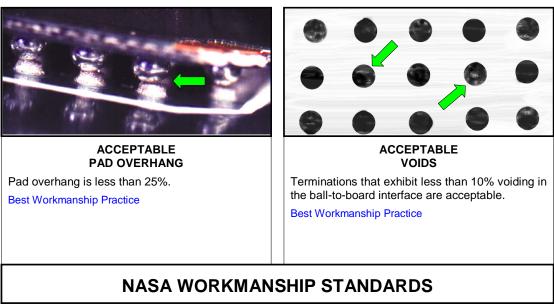
Solder terminations are smooth and rounded, with a clearly defined boundary. Terminations exhibit no voids, and are of the same diameter, volume, darkness and contrast. Registration is straight, with no pad overhang or rotation. No solder balls are present.



PREFERRED FIDUCIAL ALIGNMENT

Alignment within the fiducial marks provides a rapid, visual indication of proper device alignment.

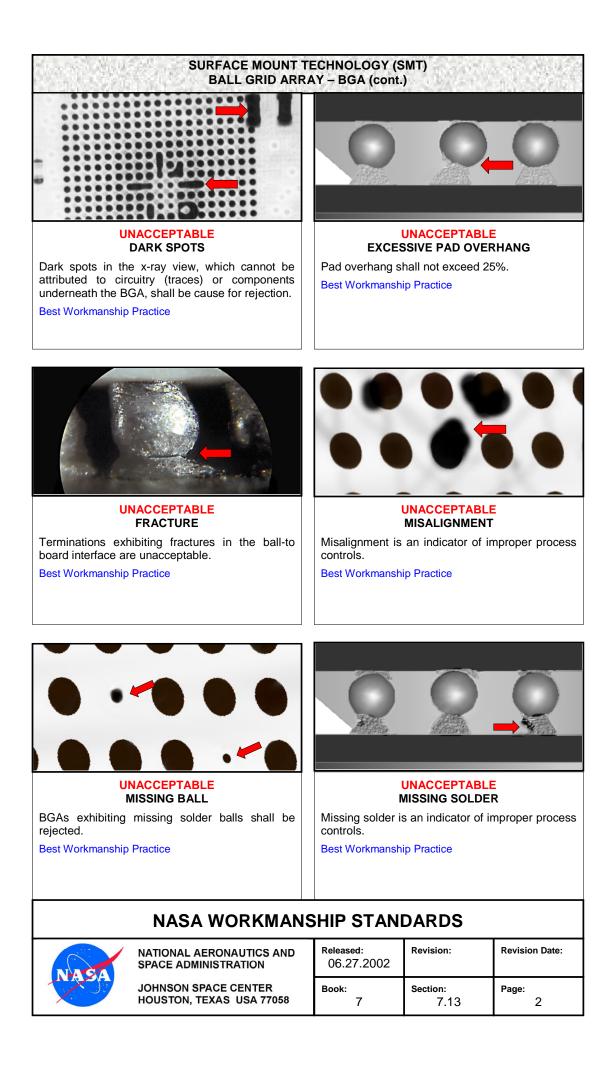
Best Workmanship Practice

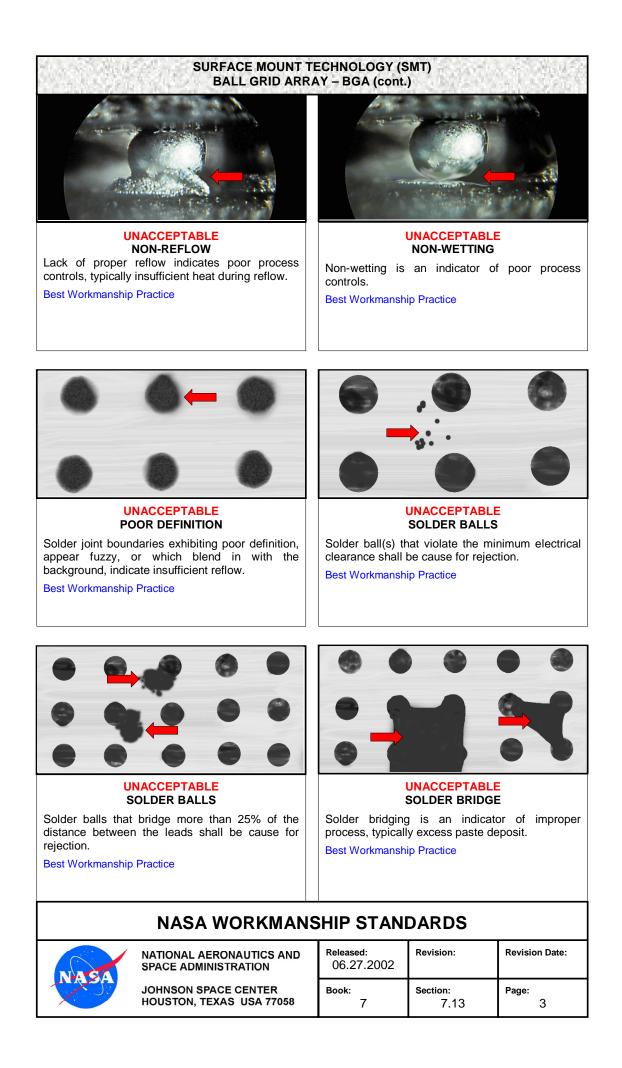


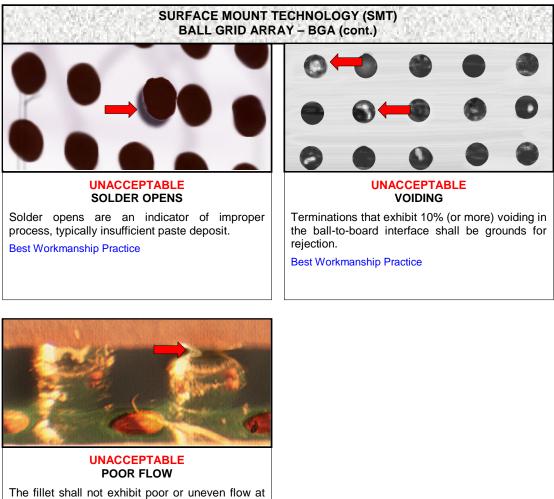


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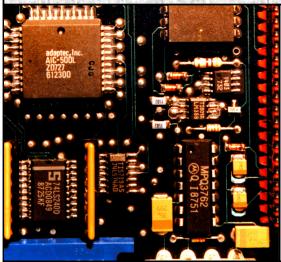




The fillet shall not exhibit poor or uneven flow the top of the solder fillet. Best Workmanship Practice

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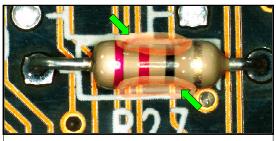
### CONFORMAL COATING and STAKING (BONDING) GENERAL REQUIREMENTS



### **GENERAL REQUIREMENTS**

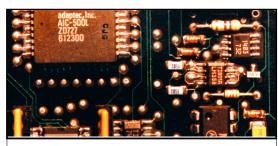
Reliable staking and conformal coating results from proper design, control of equipment, materials, work environments, and careful workmanship by trained and certified personnel.

The staking and conformal coating materials shall have dielectric properties that will meet the minimum circuit requirements in all anticipated environments. The materials shall be compatible, noncorrosive, and curable under conditions that will not change or adversely affect the performance or reliability of the parts on the PWA.



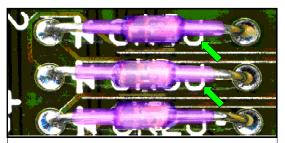
PREFERRED ADHESIVE BONDING / STAKING

Adhesive bonding / staking material has been applied to the parts and locations specified by the approved engineering specification. Material quantity is sufficient to provide required support, but does not negate stress relief or mechanically compromise hardware reliability.



PREFERRED CONFORMAL COATING

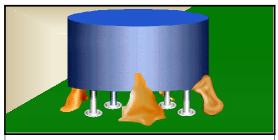
Coating covers all areas as specified on the engineering documentation. Coating exhibits uniform color, thickness, proper adhesion, is smooth, and tack free. No bubbles, entrapped contaminants or particles, excessive fillets, runs, drips, etc.



#### PREFERRED GLASS-BODIED PARTS

Glass encased parts (i.e., diodes, etc.) shall be covered with transparent, resilient sleeving or other approved material, prior to staking or conformal coating with a rigid material.

NASA-STD-8739.1 [ 9.2.3.c ], [ 11.6.3.e ] NASA-STD-8739.3 [ 8.1.4 ]



#### PREFERRED 3.5 GM PER LEAD / 7 GM TOTAL RULE

Components weighing 7 grams (0.25 oz.) total, or 3.5 grams (0.12 oz.) per lead, shall be bonded to the mounting surface, in at least four evenly spaced places around component, when no other mechanical support is used.

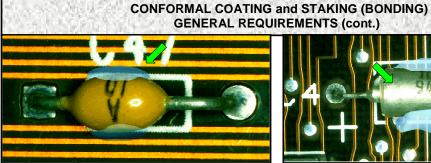
**Best Workmanship Practices** 

# NASA WORKMANSHIP STANDARDS



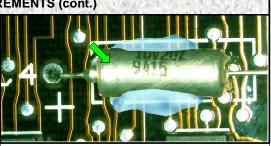
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PREFERRED **TANTALUM CAPACITORS** All axial-leaded solid-slug tantalum capacitors shall be staked.

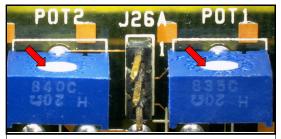
NASA-STD-8739.1 [ 9.2.4 ], [ 11.6.3.d ]



PREFERRED UNINSULATED METALLIC-CASED COMPONENT

Metallic-cased components mounted over printed conductors or which are in close proximity to uncommon conductive surfaces shall be separated by insulation of suitable thickness.

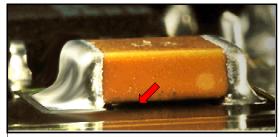
NASA-STD-8739.3 [ 8.1.2.b ]



UNACCEPTABLE ADJUSTABLE COMPONENTS

The adjustable portion of adjustable components (i.e., potentiometers, variable capacitors, etc.), as well as electrical and mating surfaces shall be left uncoated.

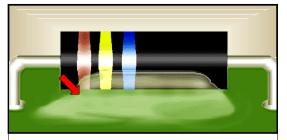
**Best Workmanship Practice** 



**UNACCEPTABLE BRIDGING / UNDERFILL** 

Conformal coating and/or staking materials shall not be allowed to bridge between the bottom of ceramic-bodied DIPs, flatpacks, or surface mounted parts and the PWB.

NASA-STD-8739.1 [ 9.2.1 ], [ 11.6.3.b ]



#### **UNACCEPTABLE** CONFORMAL COAT USED AS STAKING

Conformal coating shall not be used as a staking material. Components should be properly staked prior to being conformally coated.

**Best Workmanship Practice** 

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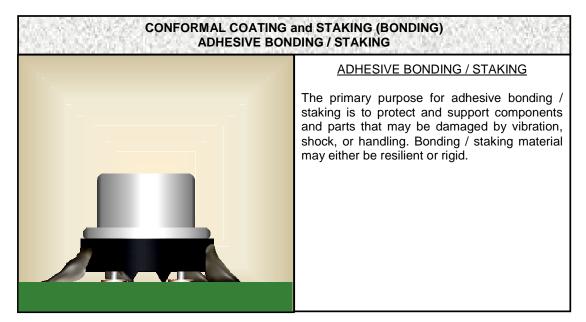
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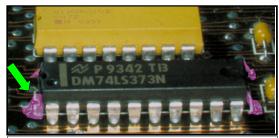
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PREFERRED ADHESIVE BONDING / STAKING

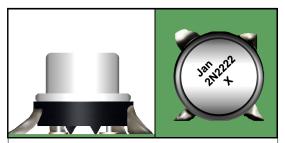
Adhesive bonding / staking material has been applied to the parts and locations specified by the approved engineering specification. Material quantity is sufficient to provide required support, but does not negate stress relief or mechanically compromise hardware reliability.



PREFERRED FLEXIBLE MATERIALS

Flexible staking materials with a high thermal expansion coefficient shall not be applied where excessive stress may be damaging. As depicted, the staking material has been applied to the corners of the package.

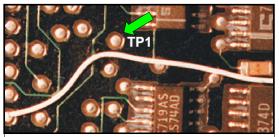
NASA-STD-8739.1 [ 9.2.1 ]



PREFERRED PERIPHERY RULE

Staking material shall be of sufficient quantity to result in a minimum of 20% of the component's periphery being bonded.

**Best Workmanship Practice** 



PREFERRED SOLDERABLE AREAS / TEST POINTS

Adhesive / staking material shall not be applied to areas that are to be soldered, or are to be used as test points. Contamination / solderability issue.

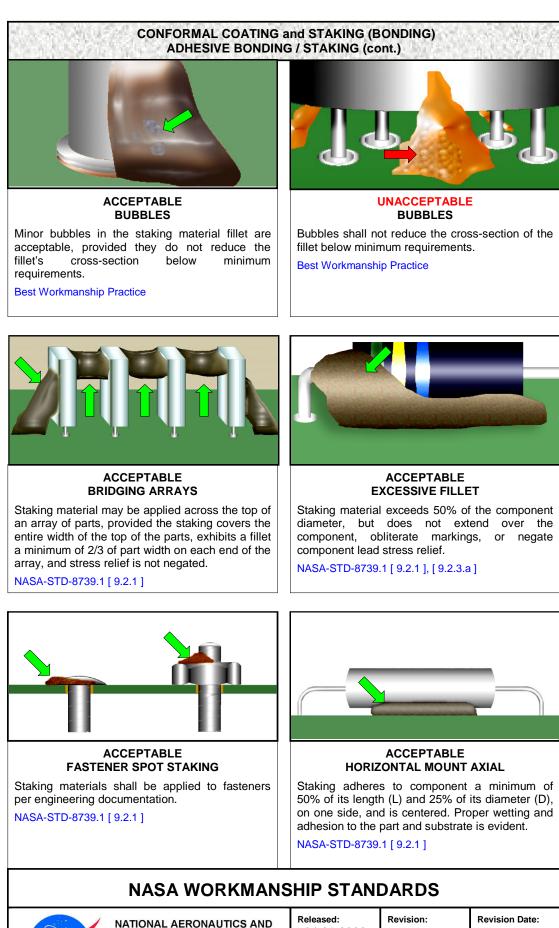
**Best Workmanship Practice** 

# NASA WORKMANSHIP STANDARDS



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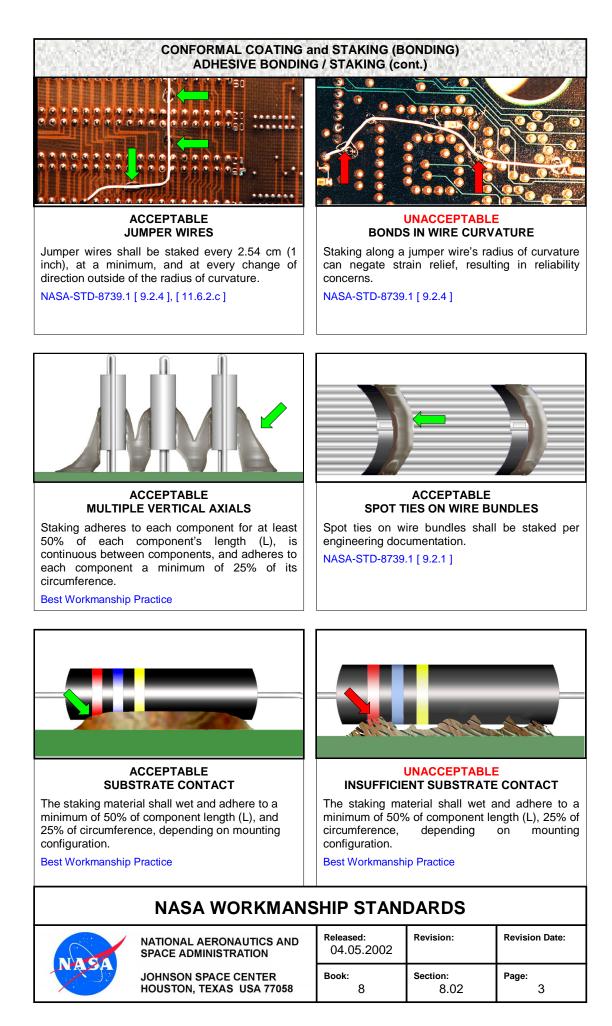
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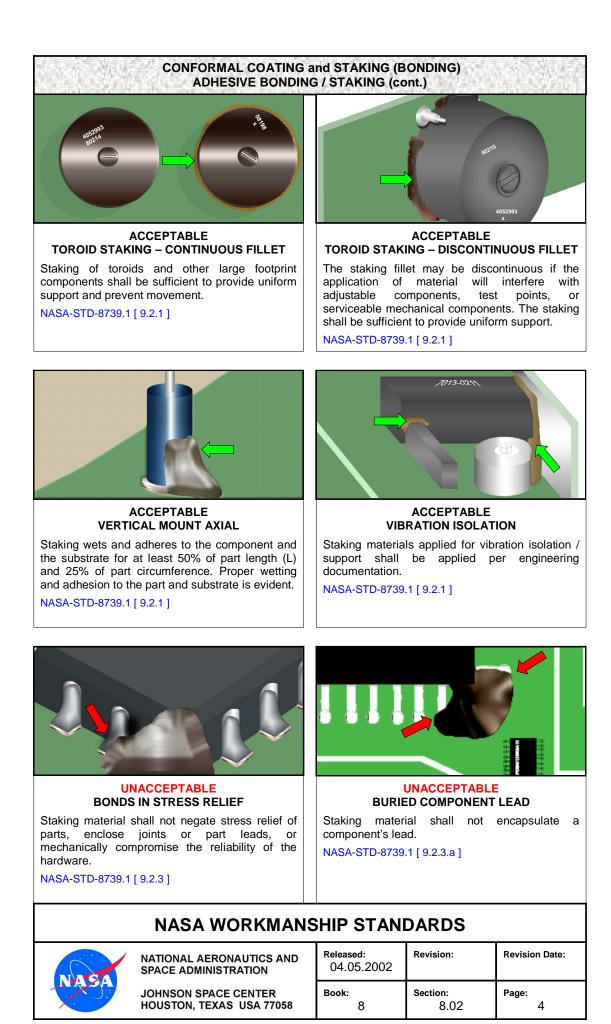


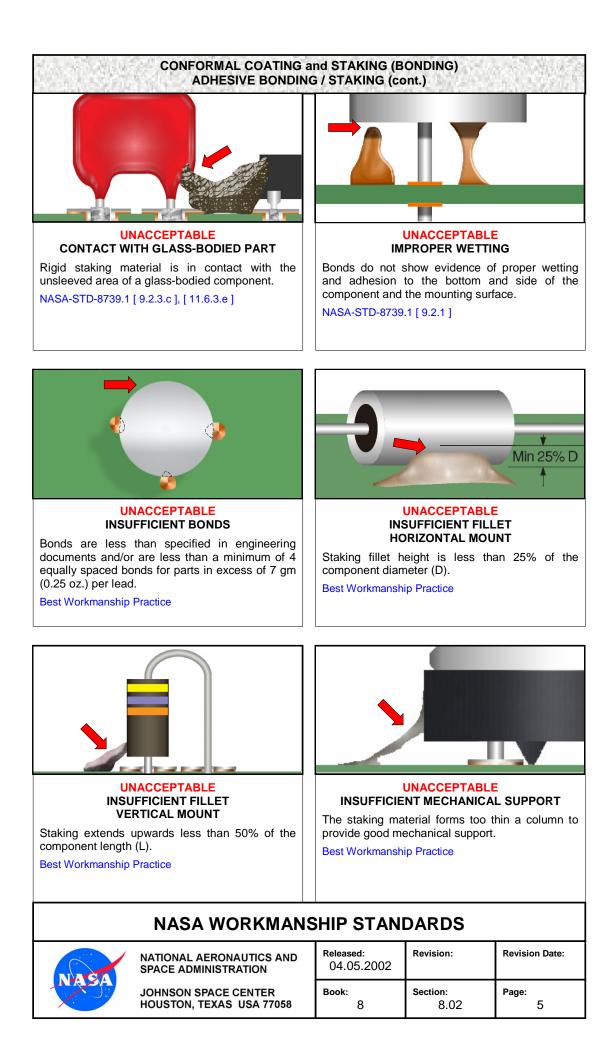
NASA

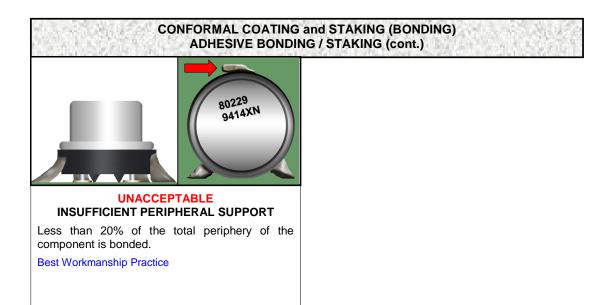
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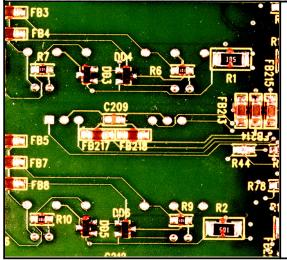






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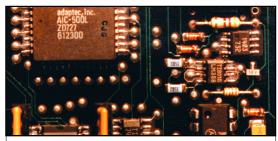
**CONFORMAL COATING and STAKING (BONDING) CONFORMAL COATING** 



### **CONFORMAL COATING**

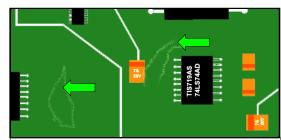
Conformal coatings are intended to provide electrical insulation and environmental protection to the PWA, eliminating or minimizing any performance degradation caused by humidity, handling, debris, and/or contamination.

Typical conformal coatings (i.e., Acrylic, Urethane, Epoxy, Silicone) can be applied in any standard vented environment by automatic / manual operations (i.e., spraying, brushing, dipping, or a combination thereof). High performance coatings (i.e., Paraxylene) require a highly controlled environment, and are applied by Chemical Vapor Deposition (CVD).



PREFERRED

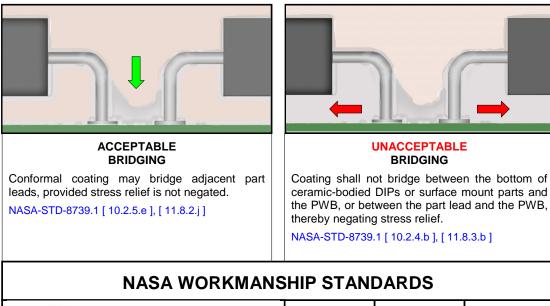
Conformal coating covers all areas as specified on the engineering documentation. Coating uniform color, thickness, proper exhibits adhesion, and is smooth, transparent, and tack free. No bubbles, entrapped contaminants or particles, excessive fillets, runs, drips, etc. Identification markings are visible.



ACCEPTABLE **BRUSH APPLICATION** 

The conformal coating is evenly applied, without forming excessive fillets or thick areas. Minor brush marks are acceptable, provided minimum thickness is maintained.

NASA-STD-8739.1 [ 10.2.2.b ]





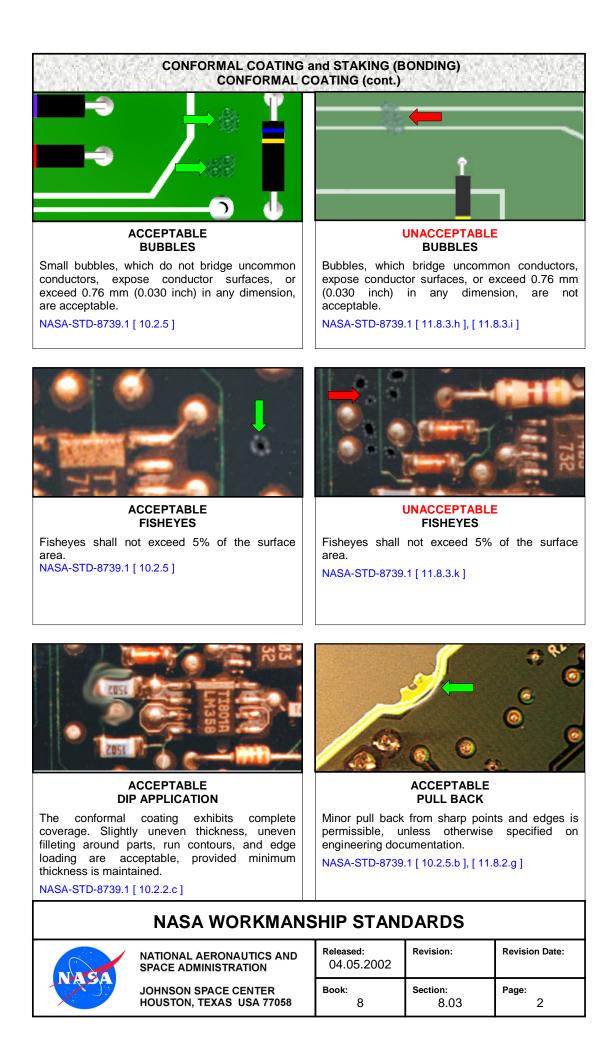
NATIONAL AERONAUTICS AND SPACE ADMINISTRATION

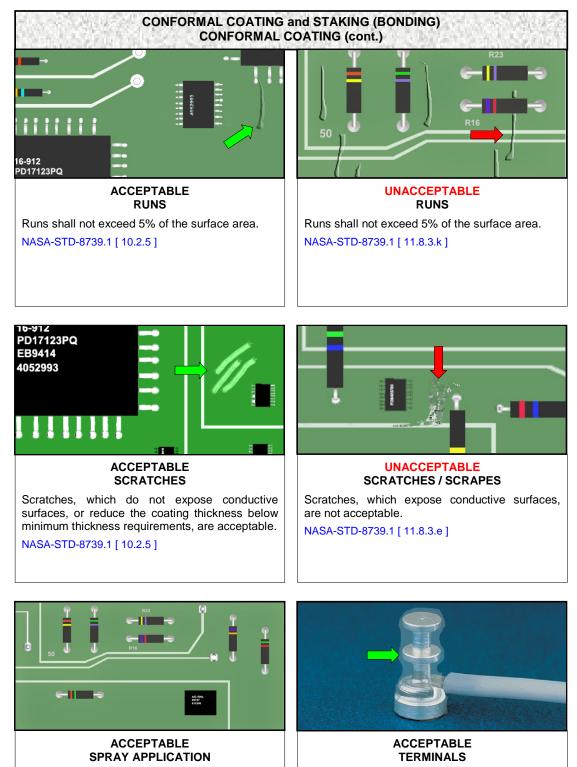
JOHNSON SPACE CENTER HOUSTON, TEXAS USA 77058

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**UNACCEPTABLE** 

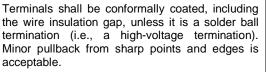
BRIDGING





The conformal coating is evenly applied, exhibits uniform color, thickness, proper adhesion, and is smooth and tack free. No evidence of shadowing, orange peel, or dusting.

NASA-STD-8739.1 [ 10.2.2.a ]



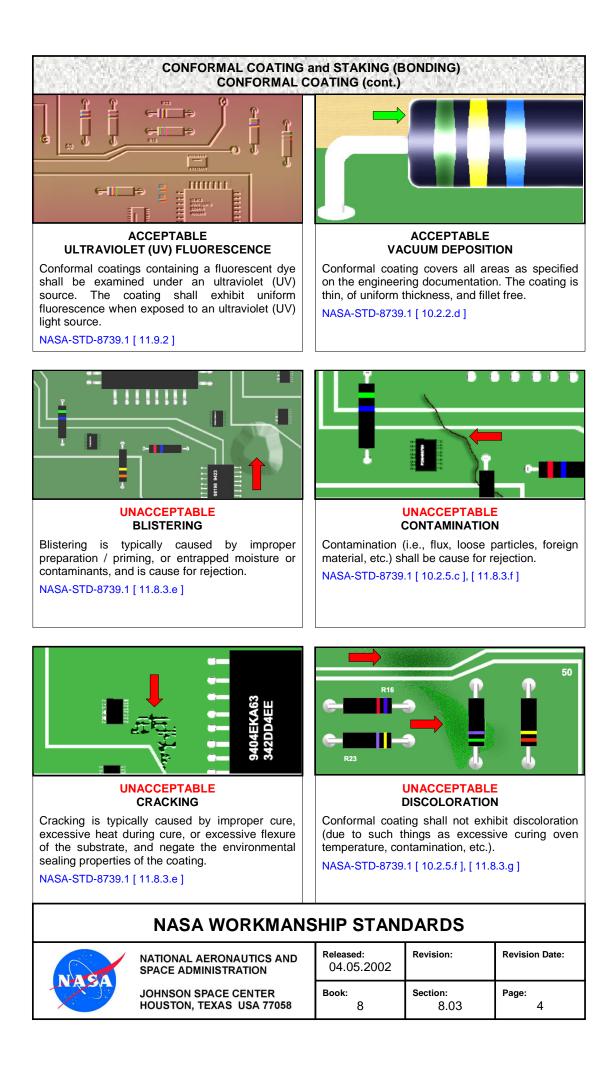
NASA-STD-8739.1 [ 10.2.5.d ]

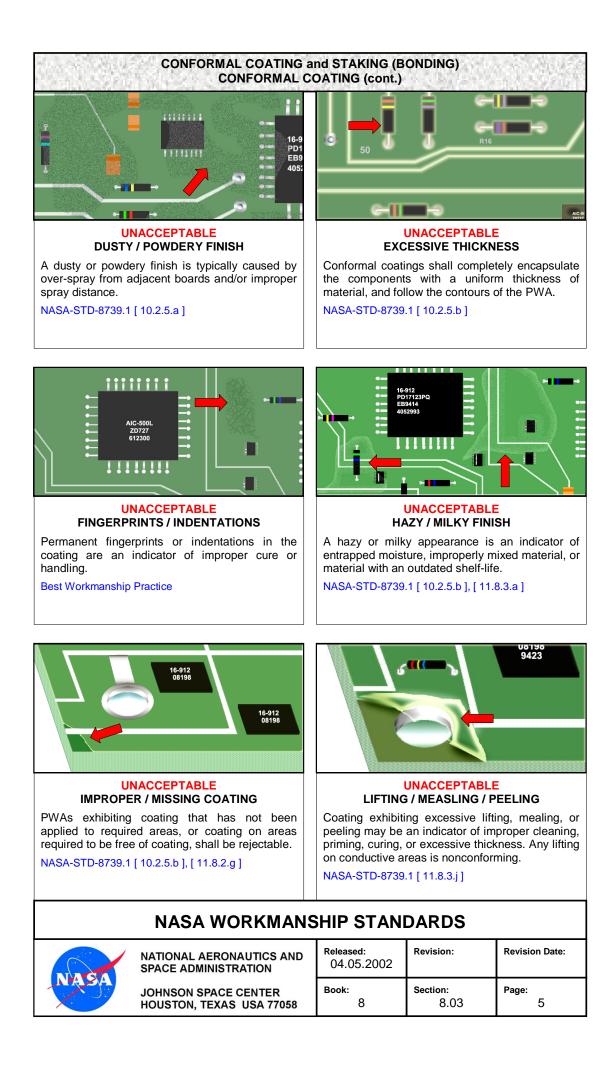
# NASA WORKMANSHIP STANDARDS

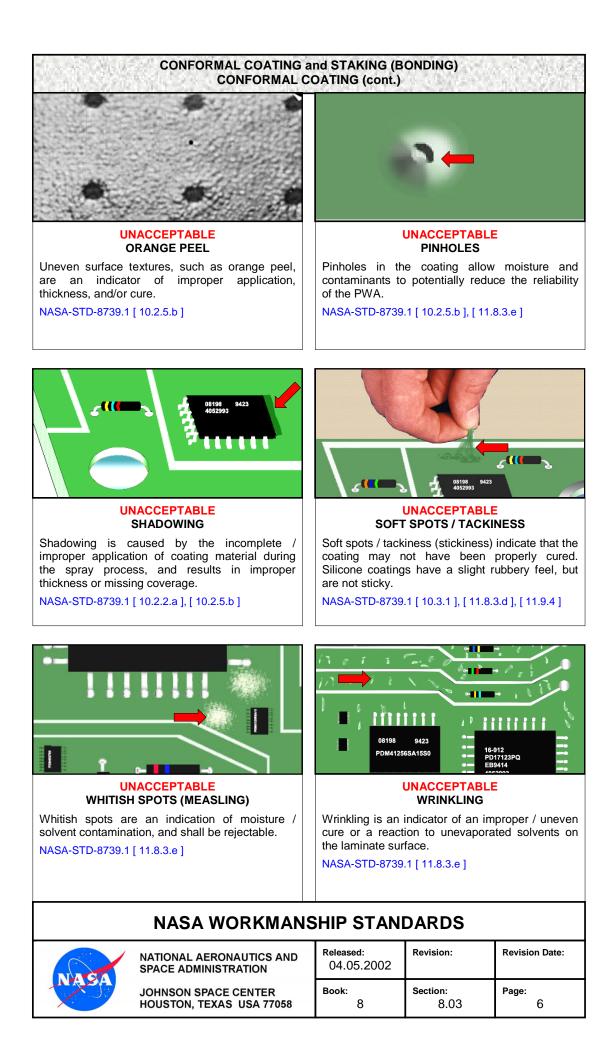


NATIONAL AERONAUTICS AND SPACE ADMINISTRATION

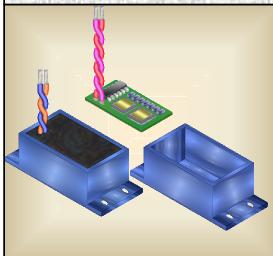
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### POLYMERIC SYSTEMS GENERAL REQUIREMENTS

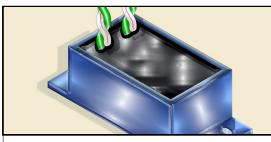


### **GENERAL REQUIREMENTS**

Polymeric systems provide mechanical and cushioning support to components, improved thermal profile / thermal sinking, and tamper-resistant, environmental packaging.

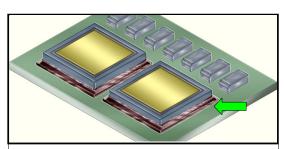
Encapsulation is a process in which electronic subassemblies (i.e.: power supplies, amplifiers, hybrid circuits, etc.) are embedded in a polymer (i.e.: silicon, epoxy gel) to produce a unitized, sealed assembly.

Underfill is a process in which a polymer (i.e.: epoxy) is injected under an electronic component to improve thermal coefficient (tc) match and extend solder joint fatigue life.



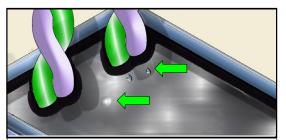
PREFERRED ENCAPSULANT

Material is fully cured, with a smooth, continuous surface extending over all embedded components, and exhibits fully wetted, continuous contact fillets with protruding devices (wires, cable, connector, etc.) and the enclosure wall. No bubbles, cavities, striation marks, or spillage.



PREFERRED UNDERFILL

Material exhibits complete and uniform flow under the component body. Peripheral fillets are smooth and uniform, with a concave profile. No bubbles, cavities, or spillage.



#### ACCEPTABLE BUBBLES / CAVITIES

Minor surface bubbles or cavities that do not extend to underlying components or conductive surfaces, are isolated, or bridge between conductors are acceptable.

Best Workmanship Practice



#### UNACCEPTABLE BUBBLES / CAVITIES

Bubbles or cavities that bridge conductors are unacceptable.

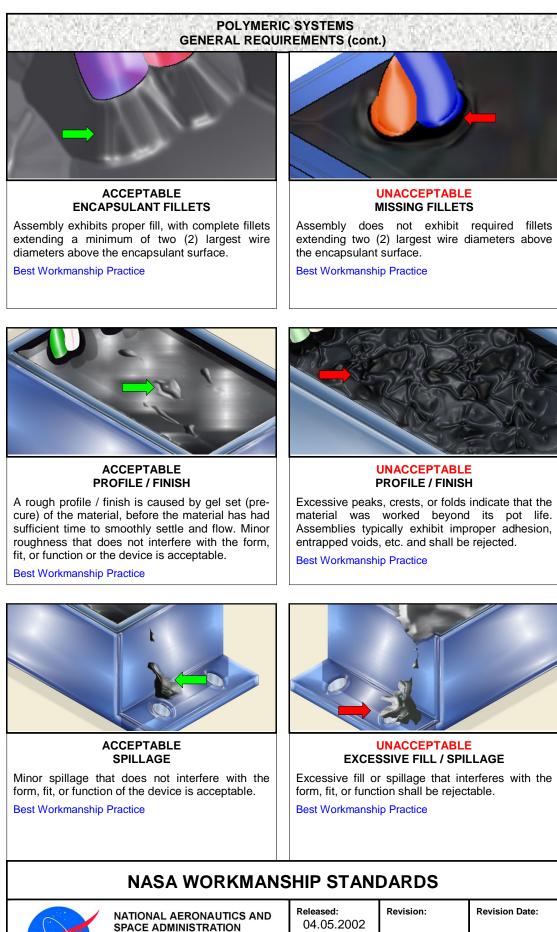
**Best Workmanship Practice** 

# NASA WORKMANSHIP STANDARDS



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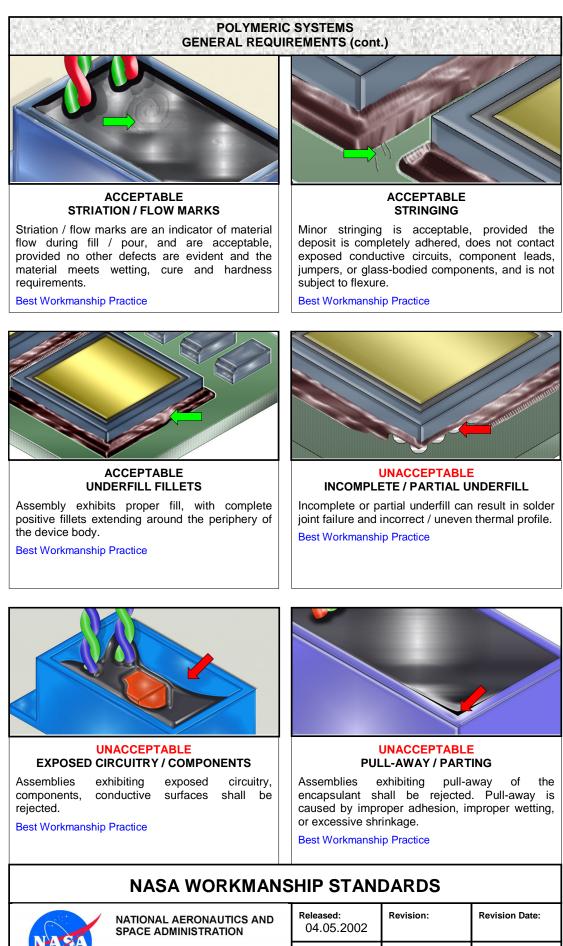


JOHNSON SPACE CENTER

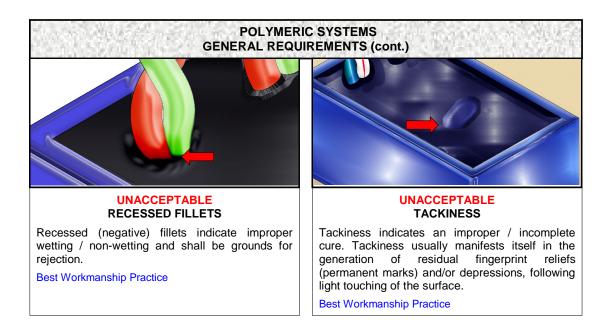
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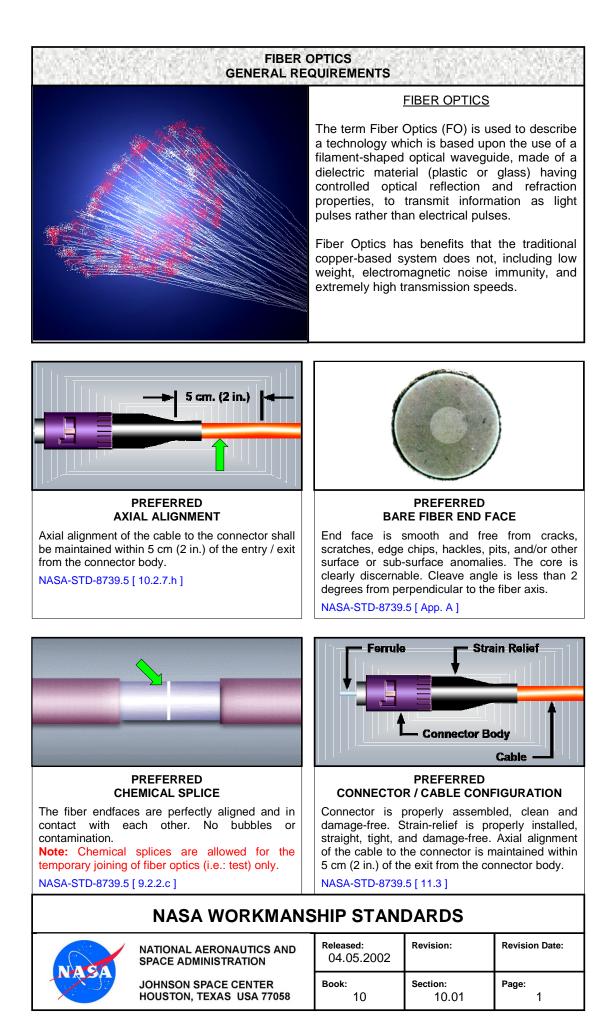
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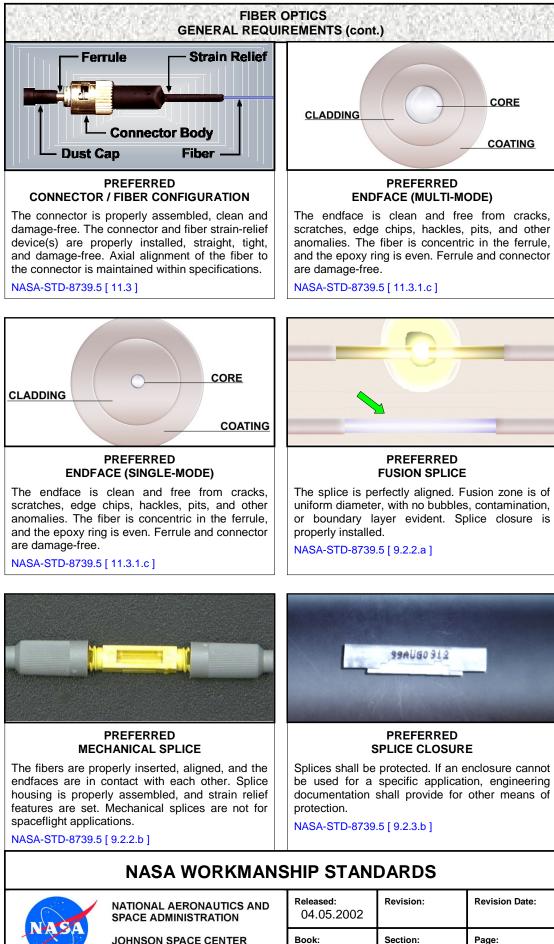


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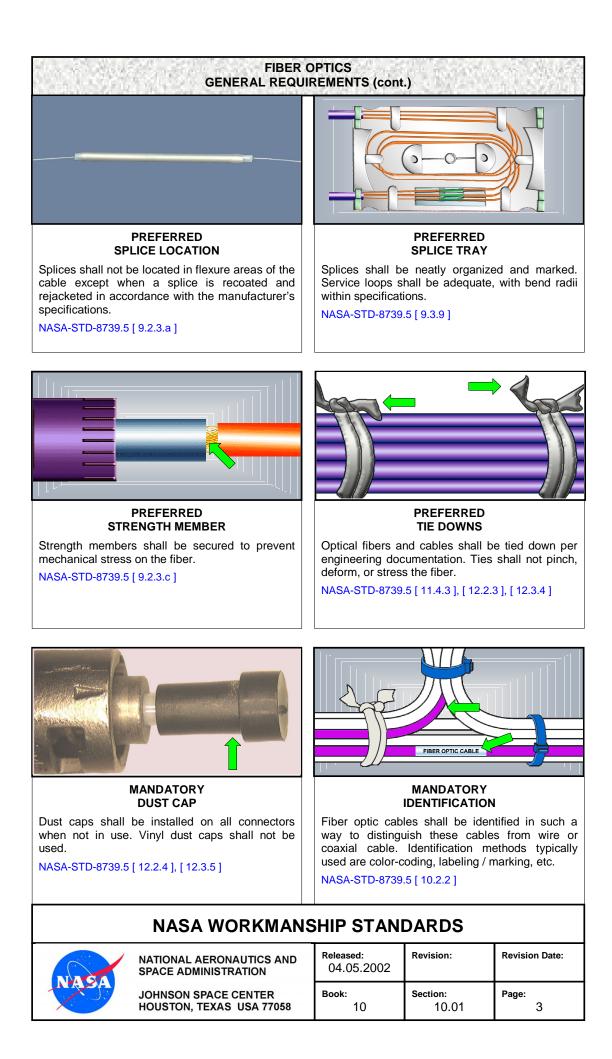
NASA WORKMANSHIP STANDARDS				
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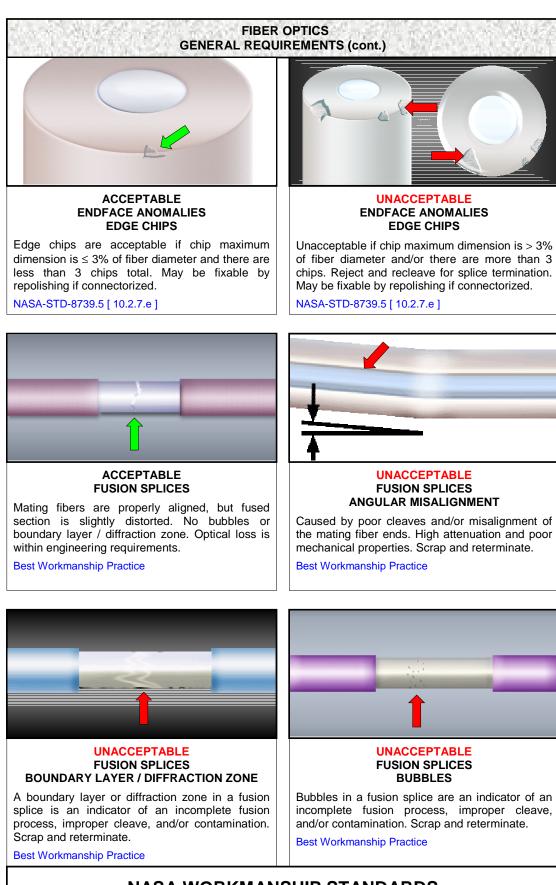




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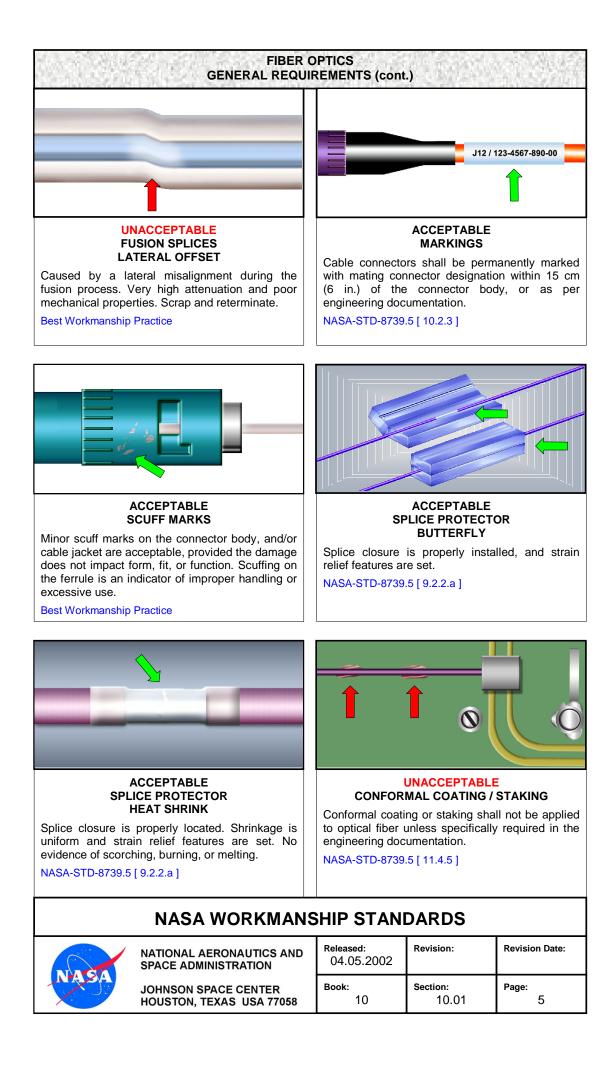


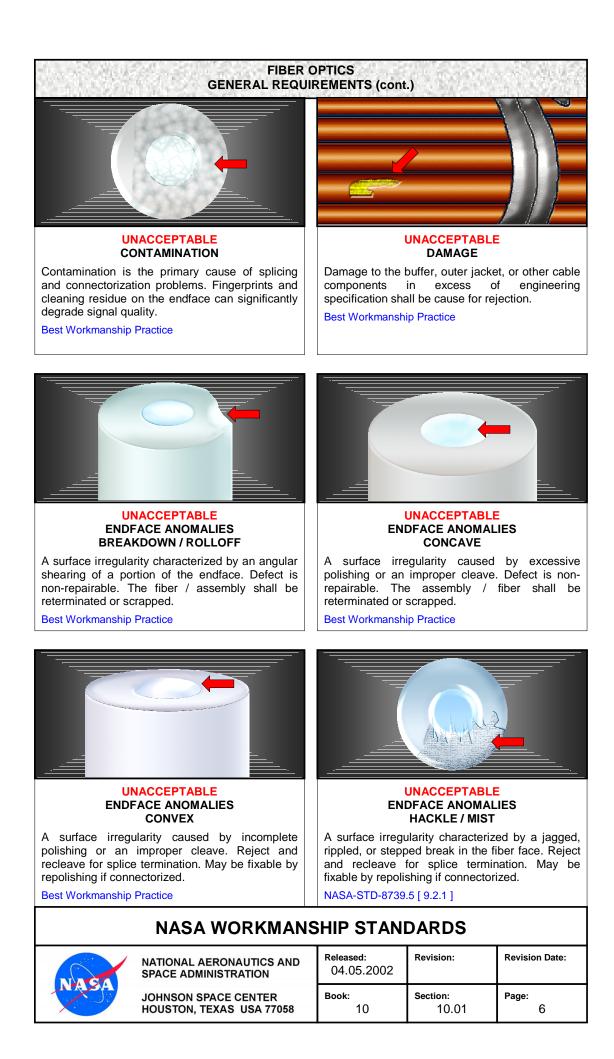
## NASA WORKMANSHIP STANDARDS

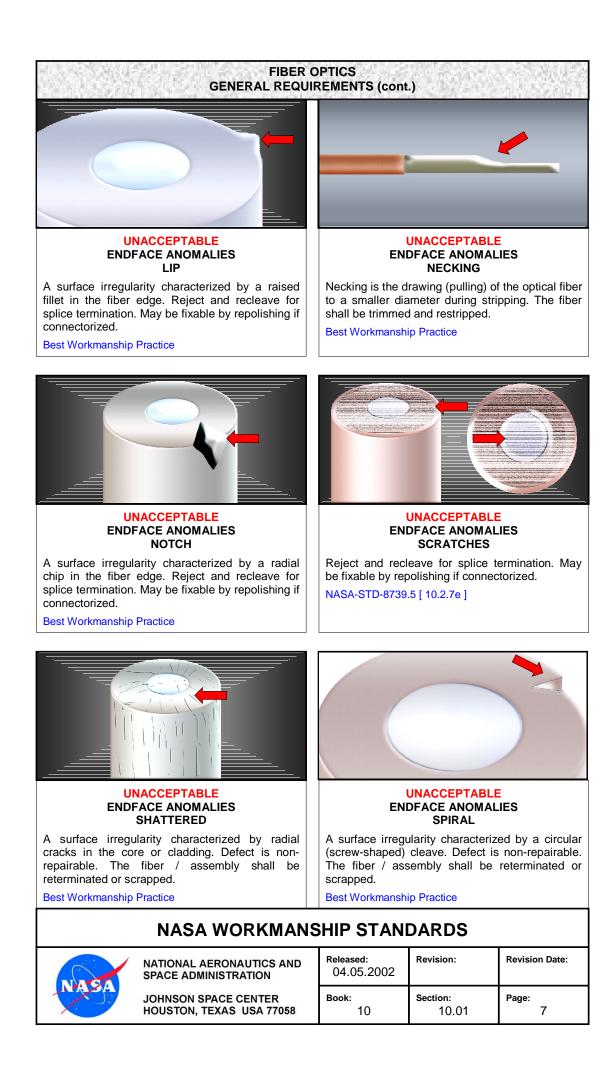


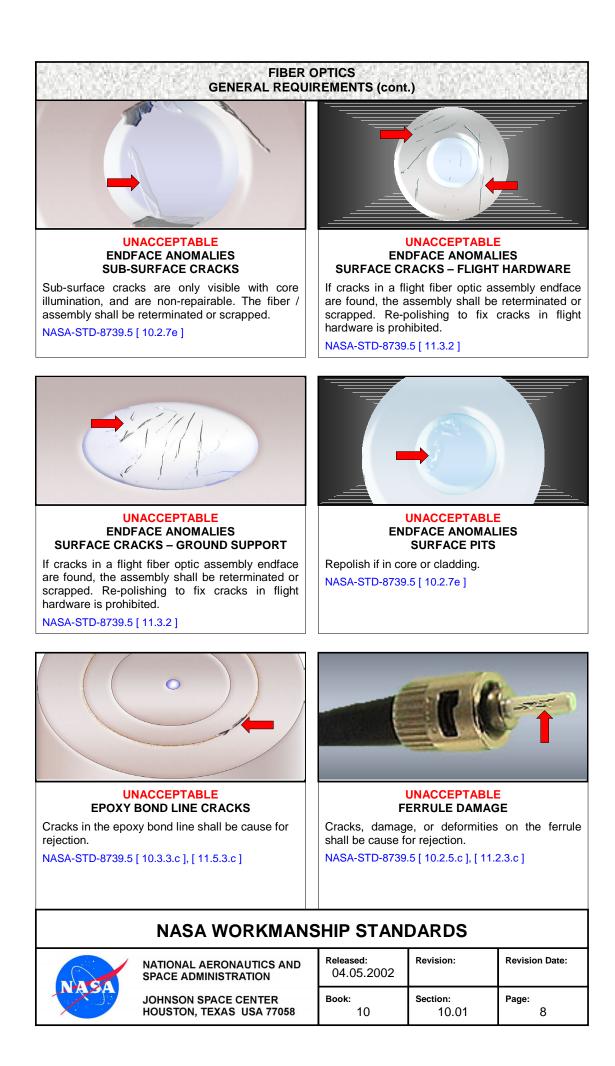
NATIONAL AERONAUTICS AND SPACE ADMINISTRATION

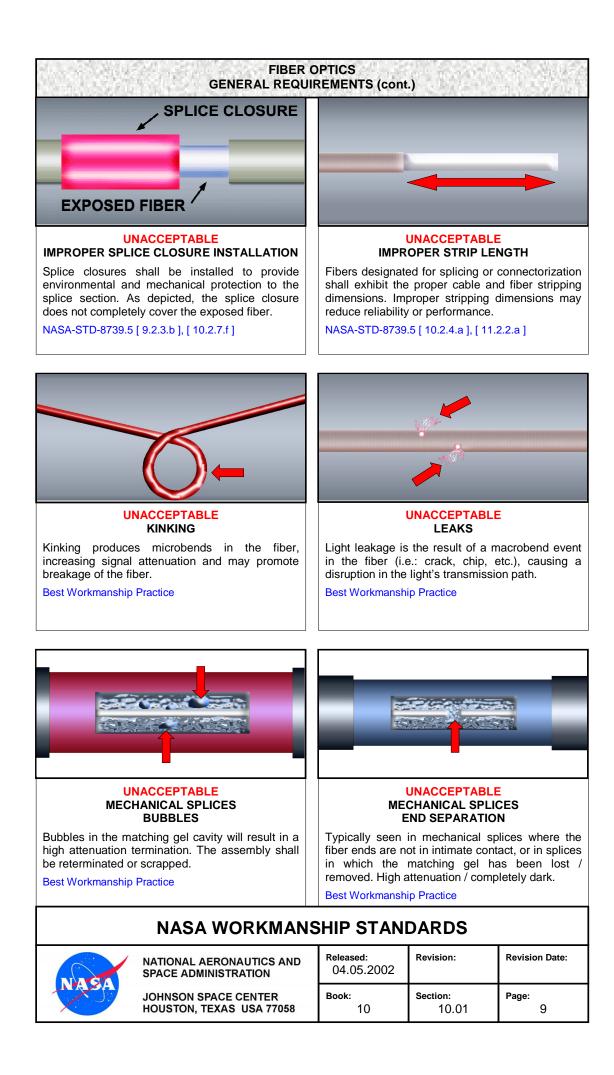
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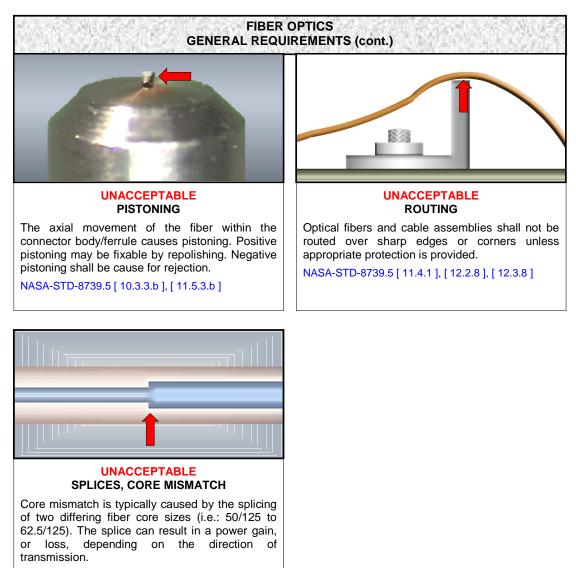






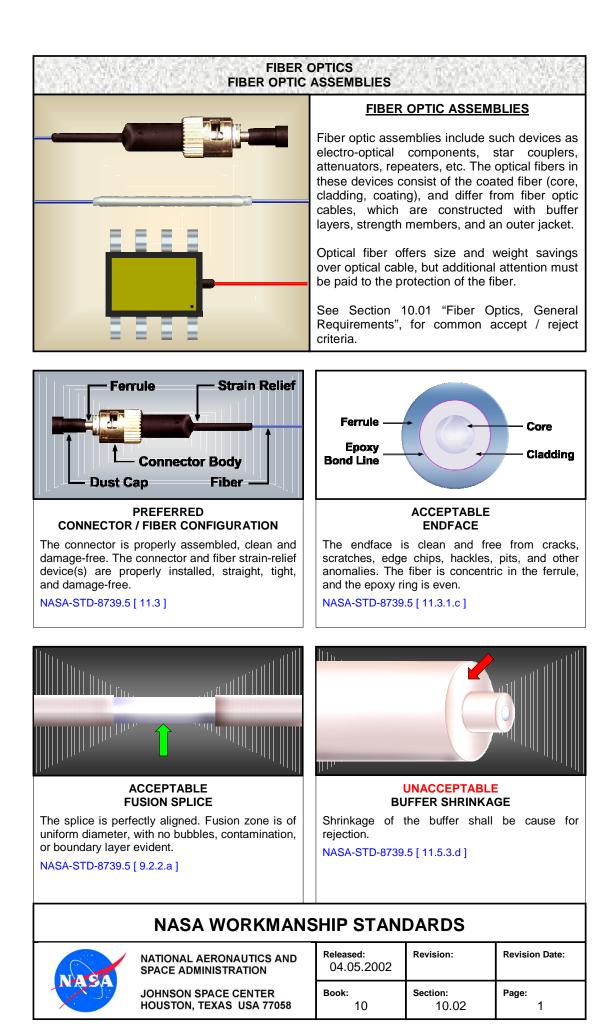


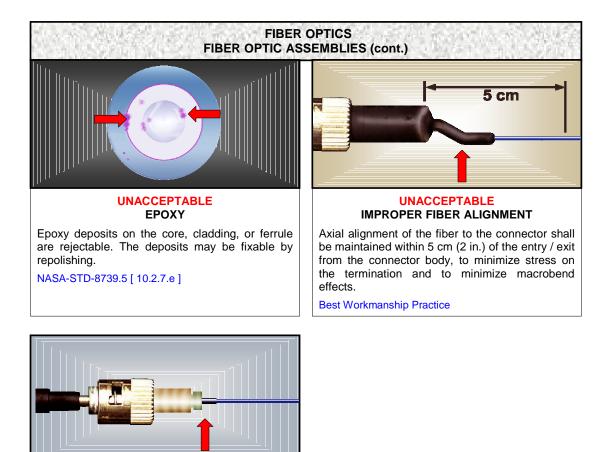




**Best Workmanship Practice** 

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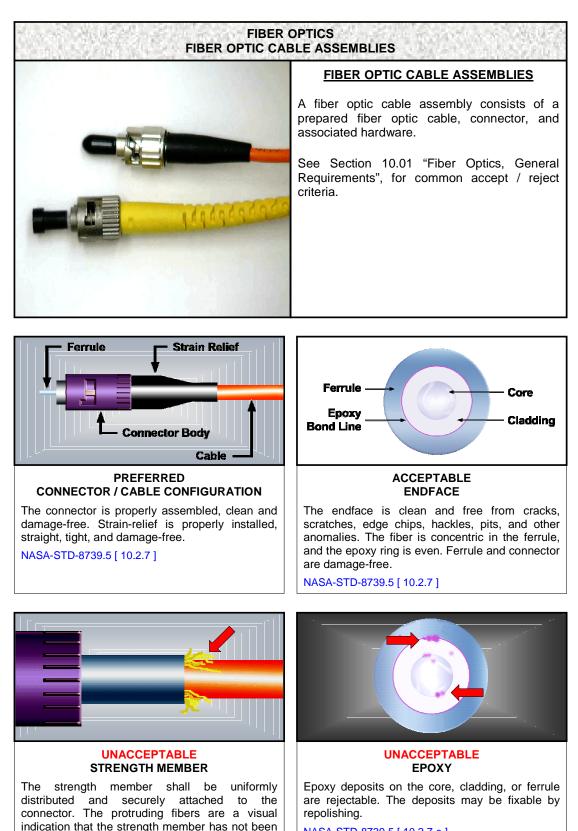




UNACCEPTABLE IMPROPER STRAIN RELIEF The strain relief shall be positioned and attached per engineering documentation. As depicted, the strain relief boot has not been installed.

NASA-STD-8739.5 [ 10.2.7.f ]

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NASA-STD-8739.5 [ 10.2.7.e ]

# NASA WORKMANSHIP STANDARDS

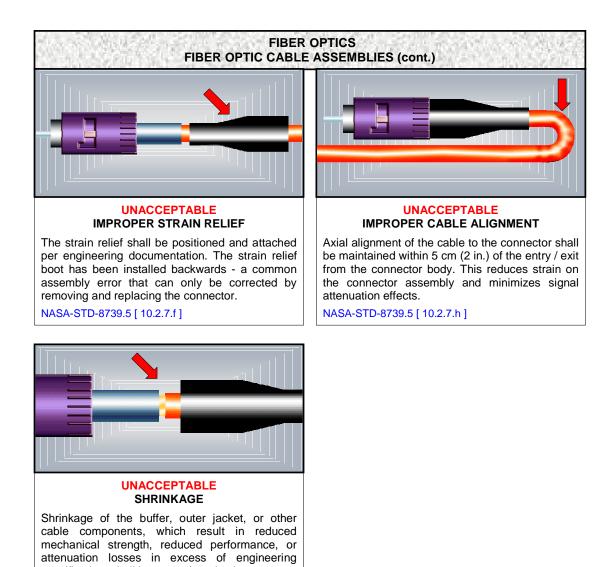


properly crimped to the connector bayonet.

NASA-STD-8739.5 [ 10.2.7.a ]

NATIONAL AERONAUTICS AND SPACE ADMINISTRATION

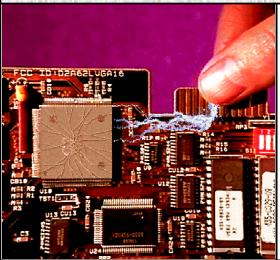
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specification, shall be cause for rejection. NASA-STD-8739.5 [ 10.3.3.d ], [ 11.5.3.d ]

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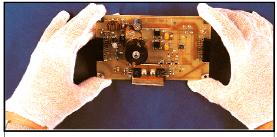
## ELECTROSTATIC DISCHARGE [ ESD ] GENERAL REQUIREMENTS



#### ELECTROSTATIC DISCHARGE (ESD) GENERAL REQUIREMENTS

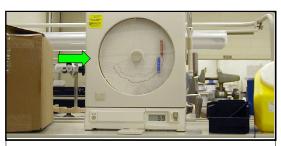
Electrostatic Discharge (ESD) is the rapid, uncontrolled discharge and transfer of accumulated electrical charge between two or more bodies at different electrical potentials, often resulting in significant Electrical Overstress (EOS) damage to sensitive electrical / electronic components.

The best prevention program is a combined effort aimed at the prevention and the controlled elimination of static charges, through the practice of proper behavior / procedures, workstation design and layout, environmental controls, tooling, and component handling.



**CLOTHING REQUIREMENTS** 

Non-static generating clothing shall be worn in ESD-protected areas or static dissipative smocks shall be worn as an outer garment. Finger cots and gloves, when worn in an ESD-protected area, shall be made of static dissipative, lint-free, particle-free materials.



HUMIDIFICATION

The relative humidity shall be monitored and maintained in ESD-protected work areas at 30% to 70%. At levels below 30%, additional precautions shall be employed (e.g.: air ionizers, humidifiers, etc.).

NASA-STD-8739.7 [ 7.2.7 ], [ 9.2.1.d ]

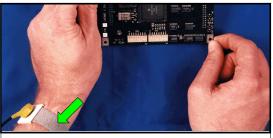
NASA-STD-8739.7 [ 7.7 ]



**IDENTIFICATION / MARKING** 

ESDS items, equipment, and assemblies shall be identified so as to warn personnel before any ESD damaging procedure can be performed.

NASA-STD-8739.7 [ 8.5 ]



PERSONNEL GROUNDING DEVICES

Personnel grounding devices (such as wrist straps) shall be supplied to all personnel working with or handling ESDS items to prevent the accumulation of dangerous electrostatic charge levels.

NASA-STD-8739.7 [ 7.2.5 ], [ 8.3 ]

## NASA WORKMANSHIP STANDARDS



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PREVENTATIVE EQUIPMENT

Air ionizers are recommended where grounding is impractical, where extremely ESD sensitive devices are used (<100V HBM), or where additional prevention against EOS / ESD are desired.

NASA-STD-8739.7 [ 7.2 ]



**PROHIBITED MATERIALS** 

The area shall be maintained in a clean and orderly condition. Smoking, eating, and drinking in ESD-protected areas shall not be permitted. Unapproved tools, static generating materials, and/or materials unessential to the fabrication area are also prohibited at the workstation.

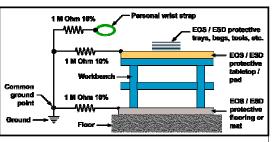
NASA-STD-8739.7 [ 7.2.2 ]



**PROTECTIVE PACKAGING** 

Electrostatic protective packaging shall prevent the generation of charge and provide protection from strong electrostatic fields. Materials used shall satisfy the resistivity requirements to avoid triboelectric charge build-up.

NASA-STD-8739.7 [ 7.3 ]



WORKSTATION GROUNDING SYSTEM

All work surfaces / workstations in an ESDprotected area shall be static dissipative and electrically connected to the common point ground system.

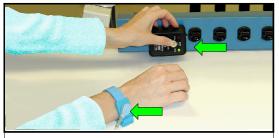
NASA-STD-8739.7 [ 7.2.3.a ]



WORKSTATION IDENTIFICATION / ACCESS

The ESD-protected area shall be clearly identified by prominently placed signs and marking systems (barrier tape, partition, rope guard, etc.). Access to such areas shall be limited to trained and equipped personnel.

NASA-STD-8739.7 [ 7.2.1 ]



WRIST STRAP TESTING

A wrist strap tester shall be available in all areas where ESDS items are handled. Wrist strap and foot grounding devices shall be tested daily.

NASA-STD-8739.7 [7.1.2], [7.6.3]

## NASA WORKMANSHIP STANDARDS



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Space between							
Conductor / Cable Type	Optimum Bend Radius (O.D.)	Minimum Bend Radius (O.D.)	Space between constraint point to start of bend (O.D.)				
Coaxial Cable	10	6	6				
Coaxial Cable (Rigid)	3.5	2	6				
Coaxial Cable (Semi-Rigid)	3.5	2	6				
Component Lead (Flat)	2	1	0.5mm (0.020 in.)				
Component Lead (Round)	2	1	2				
Fiber Optic Cable (Flight Applications)	15	10	10				
Fiber Optic Cable (Mission Critical Ground Support)	15	10	10				
Fiber Optic Cable (Hybrid)	20	10	10				
Fiber Optic, Individual (Tight Buffer)	15	10	10				
Flat Cable	10	3	3				
Flat Cable (Shielded)	10	3	3				
Harness (with coaxial cable, fiber optic, or individual conductors 8 AWG or larger)	10	6	6				
Harness (with individual conductors 10 AWG or smaller, no coaxial or fiber optic)	10	3	3				
Harness with polyimide (Kapton <sup>®</sup> ) insulated wires.	15	10	10				
Individual Insulated Conductor	3	2	2				
Multiconductor (Non-shielded)	10	3	3				
Multiconductor (Shielded)	10	6	6				
Polyimide (Kapton <sup>®</sup> ) Insulated	15	10	10				
Ribbon Cable	10	3	3				
		1	1				



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## APPENDIX BEND RADIUS TABLE (cont.)

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#### APPENDIX CRIMP TERMINATIONS

Crimping is an efficient and highly reliable method to assemble and terminate contacts, pins, lugs, coaxial connectors and ferrules to stranded conductors for assembly into connector bodies. To ensure the quality of the crimp termination, destructive testing is performed on pre- and post-production run samples. Test values and visual examples of accept / reject criteria follow:

CRIMP PINS, SOCKETS & LUGS							
			CRIMP PINS,	SOCKETS & I	LUGS		
Crir Co	np / Cono mbinatio	ductor n <sup>[*1]</sup>		Minimum Axial Strength (Tensile) Pounds Force (Newtons) <sup>[*2]</sup>			
Crimp Siz	Barrel œ	Wire Size (AWG)	Silver- or tin- plated Copper Wire	plated Copper Copper Wire Copper Wire			
LARGE	08	08	- N/A -	- N/A -	288.0 (1281.1)	- N/A -	
	10	10	- N/A -	- N/A -	159.0 (707.3)	- N/A -	
	12	12	112.4 (500)	101.2 (450)	103.2 (459.1)	- N/A -	
	12	14	71.9 (320)	60.7 (270)	65.1 (289.6)	- N/A -	
		16	51.7 (230)	38.2 (170)	41.2 (183.3)	- N/A -	
	16	18	34.8 (155)	- N/A -	32.0 (142.3)	- N/A -	
		20	20.2 (90)	20.2 (90)	20.6 (91.6)	- N/A -	
		20	20.2 (90)	13.5 (60)	20.6 (91.6)	- N/A -	
	20	22	11.2 (50)	9 (40)	12.8 (56.9)	22.2 (98.7)	
		24	9 (40)	6.7 (30)	- N/A -	14.4 (64.0)	
		22	11.2 (50)	9 (40)	12.8 (56.9)	22.2 (98.7)	
	22 22D	24	9 (40)	5.2 (23)	- N/A -	14.4 (64.0)	
		26	- N/A -	- N/A -	- N/A -	8.0 (35.6)	
		28	- N/A -	- N/A -	- N/A -	4.8 (21.3)	
	22M 24	24	9 (40)	5.2 (23)	- N/A -	14.4 (64.0)	
		26	- N/A -	- N/A -	- N/A -	8.0 (35.6)	
		28	- N/A -	- N/A -	- N/A -	4.8 (21.3)	
	26	26	- N/A -	- N/A -	- N/A -	10.8 (48)	
V	20	28	- N/A -	- N/A -	- N/A -	5.6 (25)	
SMALL	28	28	- N/A -	- N/A -	- N/A -	5.6 (25)	
		SHIELD	CRIMPS WITH	GROUNDING	LEAD <sup>[*1, *3]</sup>		
		Wire Size (AWG)	Silver- or tin- plated Copper Wire	Nickel-plated Copper Wire	Copper Wire	High- Strength Copper Alloy Wire	
		20	20.2 (90)	13.5 (60)	20.6 (91.6)	- N/A -	
		22	11.2 (50)	9 (40)	12.8 (56.9)	22.2 (98.7)	
		24	9 (40)	6.7 (30)	- N/A -	14.4 (64.0)	
		NASA	WORKMA		NDARDS		
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JOHNSON SPACE CENTER

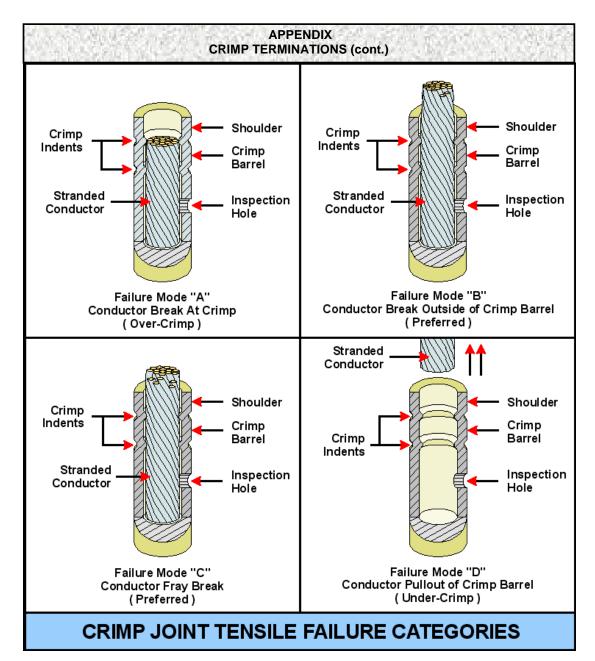
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[\*] Notes:

- 1. Stranded wire only. Crimping of solid wire, and stranded wire that has been solder tinned, is prohibited.
- 2. For contact-conductor crimp combinations not listed in the table, the tensile strength of the crimp termination shall be no less than 60 percent of the tensile strength of the conductor.
- 3. Tensile values are for the ground lead-crimp termination only. Tensile tests are not typically performed on the shield-crimp termination.
- 4. Only full-cycle, ratcheting, non-user-adjustable tools shall be used.
- 5. Failure Modes: All Failure categories (modes) are acceptable, provided separation failure occurs above the minimum axial (tensile) strength.
- 6. Conductor breaks at the entrance of the contact wire barrel, caused by conductor cutting because the contact is not held squarely in the tester jaws, shall not be considered a preferred break.

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#### APPENDIX CRIMP TERMINATIONS (cont.)

Inspection and verification of assembled connectors shall include contact seating and retention tests, in applications in which the engaging (mating) ends of the pins or socket contacts are accessible.

Push Test: Push testing shall utilize a tool that minimizes the possibility of accidental contact bending and applies a controlled, preset pressure to the contact before releasing the force. Socket testing probes shall be undersized (compared to mating pin diameters) and shall not cause a mating cycle to occur.

Pull Test: Pull force contact retention testing shall be performed only on crimp-contact connectors in which the contact engaging (mating) ends are not accessible.

CONTACT RETENTION TEST [*1]						
Contact Sizes		Push Test Force Pounds (Newtons)	Pull Test Force <sup>[*2]</sup> Pounds (Newtons)			
LARGE	12	10 – 12 ( 44.5 – 53.4 )	4 - 7 ( 17.8 – 31.1 )			
	16	8 - 10 ( 35.6 – 44.5 )	4 - 7 ( 17.8 – 31.1 )			
	20	5 - 7 ( 22.2 – 31.1 )	3 - 5 ( 13.3 – 22.2 )			
SMALL	22, 22D, 22M	4 - 6 ( 17.8 – 26.7 )	3 - 5 ( 13.3 – 22.2 )			

[ \*] Notes:

1. For contacts not listed, refer to connector manufacturer's recommendations.

2. The listed values are based on a conductor size of 24 AWG. If a smaller wire gage is used, the listed values should be adjusted accordingly. Wire shall not be pulled to a force in excess of 80 percent of the specified minimum crimp tensile requirement. This requirement must be met to avoid damage to the wire / contact crimp joint.

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## APPENDIX ENVIRONMENTAL CONDITIONS

All Flight hardware fabrication operations shall be performed in a controlled environment that limits the entry of contamination. Environmental parameters shall be recorded and documented.

The appropriate temperature and humidity limits for the different assembly operations are given as follows:

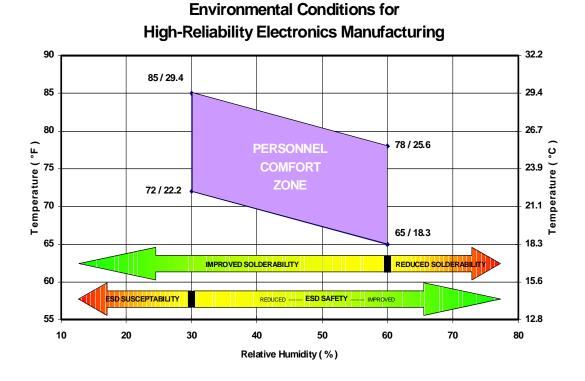
ENVIRONMENTAL CONDITIONS	Temperature (°C / °F)			nidity RH)
TASK / OPERATION	Lower Limit	Upper Limit	Lower Limit	Upper Limit
Cable & Harness Assembly	20 / 68	30 / 85	30	70
Conformal Coating	18 / 66	29 / 84	30	60
Crimping	18 / 66	32 / 90	10	90
Electrostatic Discharge (ESD) Protection	N/A	N/A	30 ① 40 ②	70 ① 60 ②
Encapsulating / Potting	18 / 66	29 / 84	30	60
Fiber Optic Cable Assembly	20 / 68	30 / 85	30	70
Hand Soldering (PWB)	20 / 68	30 / 85	30	70
Wire Wrap	18 / 66	32 / 90	10	90
Printed Wiring Board (PWB) Assembly	16 / 65	32 / 90	10	90
Staking	18 / 66	29 / 84	30	60
Surface Mount Technology (SMT)	16 / 65	30 / 85	30	60

Notes:

- 1. Relative humidity ranges for Electrostatic Discharge (ESD) Protection
  - ① Nominal % R.H.
  - ② Desired % R.H.
- 2. Special Environmental Requirements. Parts or equipment being processed that require more stringent control of environmental conditions than those stated above, shall have those requirements and controls identified and specified in the engineering documentation.

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#### PERSONNEL COMFORT ZONE

The temperature and humidity of the work area shall be maintained within the limits defined as the comfort zone. The supplier shall monitor and maintain records of the work area temperature and humidity conditions.

#### LIGHTING

Light intensity shall be a minimum of 1077 Lumens per square meter (Lm/m<sup>2</sup>) or 100 foot-candles, measured on the work surface. Supplemental lighting may be used to achieve the required lighting levels.

#### **VENTILATION SYSTEM**

Areas used for cleaning parts, and areas where toxic or volatile vapors are generated, shall have an adequate ventilation system for removing air contaminants. The ventilation system shall comply with the recommendations and guidelines of the Occupational Safety and Health Administration (OSHA) requirement 29CFR.

#### **REMOTE / FIELD OPERATIONS**

In remote / field operations, the required controlled conditions cannot be effectively achieved. Special precautions shall be taken to minimize the effects of the uncontrolled environment on the operation being performed on the hardware. These precautions shall be identified in the appropriate engineering documentation.

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APPENDIX INSPECTION OPTICS

Inspection aids shall be selected appropriate to the item(s) or task(s) being inspected. Inspections shall be performed, using aids conforming to the following requirements:

- 1. Microscopes equipped with refractor boxes, oblique illumination (or other 45° angle viewing aids), video cameras, monitors, and/or still photographic capabilities are permissible.
- 2. Inspection light sources shall provide shadowless illumination.
- 3. The use of coherent light sources for inspection of fiber optic terminations shall be prohibited.
- 4. For inspection of solder connections, magnification aids that permit simultaneous viewing with both eyes (stereoscopic) are preferred, but not mandatory.
- 5. Use only glass optical elements.
- 6. The use of nondestructive inspection methods (e.g. x-ray, laser, and automated inspection systems) is permissible; however, the process shall be fully documented and shall not damage or degrade parts.

OPTICAL INSPECTION REQUIREMENTS	MAGNIFICATIO	ON POWER [*1]
Operation / Task	Lower Limit	Upper Limit
Cable & Harness Assembly	4X	10X
Conformal Coating (Requires black-light inspection)	4X	10X
Crimping	4X	10X
Electrostatic Discharge Protection (ESD)	N/A	N/A
Encapsulating / Potting / Underfill	4X	10X
Fiber Optic Cable Assembly [*2]	22222222	XXXXXXXX
a. General	50X	80X
b. Endface / cleaved end inspection	100X	200X
Hand / Through-Hole Soldering (NPTH / PTH / PWB)	4X	10X
Printed Wiring Board (PWB) Assembly	3X	10X
Staking / Bonding	4X	10X
Surface Mount Technology (SMT)	2222222222	2222222222
a. Pre-soldering operations (Assembly / component placement / coplanarity / part alignment / paste testing / tinning)	4X	45X
b. Soldered connections: Land width $\geq$ 0.65mm (0.025")	10X	25X
c. Soldered connections: Land width < 0.65mm (0.025")	25X	40X
d. Soldered connections: Land width < 0.39mm (0.015")	25X	45X
e. Ball Grid Array (BGA) <sup>[*3]</sup>	4X	45X
f. Chip-On-Board (COB) / Multi-Chip Module (MCM)	10X	200X
Wire Wrap	3X	10X

#### [ \* ] **NOTES**:

1. Additional magnification shall be used as necessary to resolve suspected defects.

2. WARNING: Extreme caution shall be exercised during the handling and optical inspection of fiber optics. Some light sources used in the testing and operation of fiber optics are extremely intense, may be operating in the visible or invisible spectrum, and can cause serious and permanent eye damage (often without any initial sensation of pain). Always assume an optical fiber is powered and operational, until confirmed otherwise !!

3. Three-dimensional (3-D) X-ray laminography is recommended.

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#### APPENDIX HARNESS TIE SPACING

Discrete wiring assembled into interconnecting cables or harnesses should be properly secured to ensure a highly reliable, robust assembly, providing proper stress relief and conductor support for the intended application.

All harness ties (i.e.: spot, plastic strap, stitch, etc.) shall be snug and properly spaced, without pinching or crushing the insulation, or bunching the conductors. Special attention should be given to harnesses containing coaxial and/or fiber optic cables, as these are extremely impedance-sensitive to crushing / deformation.

HARNESS TIE SPACING					
Harness Outer Diameter [O.D.] mm (inches)	Max. Distance Between Ties mm (inches)	Max. Distance From Connector Or Connector Accessory To First Tie mm (inches)			
≤ 6.4 ( 0.2 5 )	19.1(0.75)	25.4 – 50.8 (1 - 2)			
12.7 ( 0.5 )	38.1 ( 1.50 )	25.4 – 50.8 (1 - 2)			
25.4 ( 1.00 )	50.8 ( 2.00 )	50.8 - 76.2 (2-3)			
> 25.4(1.00)	76.2 ( 3.00 )	76.2 – 101.6 ( 3 – 4 )			

NOTES:

- 1. Spot ties (lacing) shall consist of a clove hitch, followed by a square knot (or other non-slip knot).
- 2. Lacing tie ends shall be trimmed. When knots are to be staked, the necessary compounds, as well as any special design requirements shall be specified.
- 3. Plastic strap / cable ties (i.e.: Ty-Rap<sup>®</sup>, etc.) should have metal tangs, and shall be of the locking / permanent design. The "ribbed" side of the strap shall be placed against the wires, and tightened to prevent movement on the assembly. Surplus strap ends shall be trimmed flush at the back of the strap head.
- 4. Ties shall be placed immediately before and immediately after any breakout of a wire or cable from the harness.

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APPENDIX HARNESS TIE SPACING (cont.)

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DEFINITIONS

## **Active Device**

A discrete electronic component whose state (conductive) properties change when subjected to the application of an applied electrical signal (i.e.: diode, integrated circuit, transistor, etc.).

## Attenuation

A reduction of signal amplitude (power), measured in decibels (db).

## **Best Workmanship Practice**

A procedure, practice, or process attribute that has been demonstrated through use and experience, to result in a robust design and high reliability; but, which has not been identified as a specific attribute / requirement in the NASA Technical Standard series, NASA-STD-8739.

## **Blind Via**

A via (plated-through hole) that extends to only one surface (primary / secondary) of a multilayer printed wiring board, with the other end terminating to an internal plane or land.

## **Breakdown / Rolloff**

A surface irregularity associated with fiber optics, characterized by an angular shearing of a portion of the endface resulting in a rounded edge.

## **Buried Via**

A via (plated-through hole) that does not extend to either surface of a multilayer printed wiring board, but instead terminates to internal planes / lands.

## **Chip-On-Board (COB)**

A printed wiring board assembly process in which unpackaged die (or dice) are bonded to the board surface, and interconnected to the surrounding printed circuitry and/or adjacent die by wire bonding techniques.

## **Component Side**

The primary side of a printed wiring board, from which through-hole components are typically inserted and which is opposite the solder application side of the board in solder wave assembly processes. The majority of the active circuit components typically populate the component / primary side. See also "Solder Side".

## **Dead-Bug**

An industry nickname for the discrete components added and wired into a printed wiring assembly (PWA) to facilitate circuit modifications, rather than redesign and manufacture a new board. The nickname comes from their general appearance on the board: upside down, with their termination leads (legs) up in the air – like a dead bug.

## Dice

Two or more die.

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**DEFINITIONS** (cont.)

## Die

The basic, leadless form of an electronic component (active, passive, or integrated circuit) supplied on a silicon substrate / chip.

## Discrete Component

A separate component that performs a single circuit function (i.e.: resistor, capacitor, diode, transistor, etc.).

## Double-Sided Assembly

A printed wiring assembly (either double-side or multi-layer) with components mounted on both the primary (component) and secondary (solder) sides.

## Drain Wire

An uninsulated wire that is used for the electrically conductive termination of a foil mylar shield or ground plane.

## Edge Flash

A thin layer of insulation that is produced during the stripping of insulated conductors.

## **Fiducial Mark**

An artwork feature that provides a visual guide for component orientation and mounting.

## Hackle

A surface irregularity associated with fiber optics, and characterized by a jagged, rippled, or stepped break in the fiber face, similar in appearance to a stepped mountain range or the rough fur on a dog's back.

## Haywire

A discrete conductor used to facilitate minor circuit modifications to printed wiring assemblies (PWA), rather than redesign and manufacture a new board. (a.k.a.: white wire, jumper).

## Key

A mechanical device or feature in addition to, or in lieu of, a polarization feature that ensures the coupling of identical connectors / components can occur in only one orientation and only to similar keyed connectors / components.

## Mist

See "Hackle"

## **Mixed Technology**

A surface mount term, referring to the use of through-hole and surface mount components on the same printed wiring assembly.

## Multilayer Printed Wiring Board

A rigid, flexible, or rigid-flex printed wiring board having three or more printed wiring board layers that are mechanically bonded together and electrically interconnected.

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## **Passive Device**

A discrete electronic component whose state properties do not change when subjected to the application of an applied electrical signal (i.e.: resistor, capacitor, inductor, etc.).

## Piggyback

The mounting of components on top of each other.

## **Pink Ring**

A defect condition where the conductive layer around a through-hole / inner-layer interface has been stripped of its copper oxide coating, producing a "pinkish" coloration.

## Popcorn

Popcorning is caused by the release of gas pressure entrapped in the component body during the soldering process. The effect can be relatively mild (body deformation) or can be destructive (seal breach or delidding). Popcorning is typically seen in plastic bodied devices that were exposed to an uncontrolled, high humidity environment during storage and/or assembly prior to soldering.

## **Primary Side**

See "Component Side".

#### **Reflow Soldering**

The process of mass soldering a printed wiring assembly in which all (or a majority) of the components have been installed with a solder tinning, solder paste, or solder preform deposit between the component lead(s) and the land, and where the soldering process is completed by exposing the entire assembly to a heated environment sufficient to cause the solder deposits to flow.

## Secondary Side

See "Solder Side".

## Shadowing

A defect caused by the "blocking effect" of a component or other physical obstruction during the spray application of conformal coating, resulting in improper thickness or incomplete coverage.

## Single-Sided Printed Wiring Board

A printed wiring board with a conductive pattern on only on side, typically the secondary (solder) side.

## Solder Side

The secondary side of a printed wiring board, which is typically exposed to the application of solder during a mass soldering process (i.e. solder wave or solder fountain).

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DEFINITIONS (cont.)	

## Underfill

A polymeric substance injected under an electronic component to provide mechanical support and thermal conductivity.

Via

A plated through hole that is used as an interlayer electrical connection, but is sized to prevent the insertion of component lead or other reinforcing material.

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